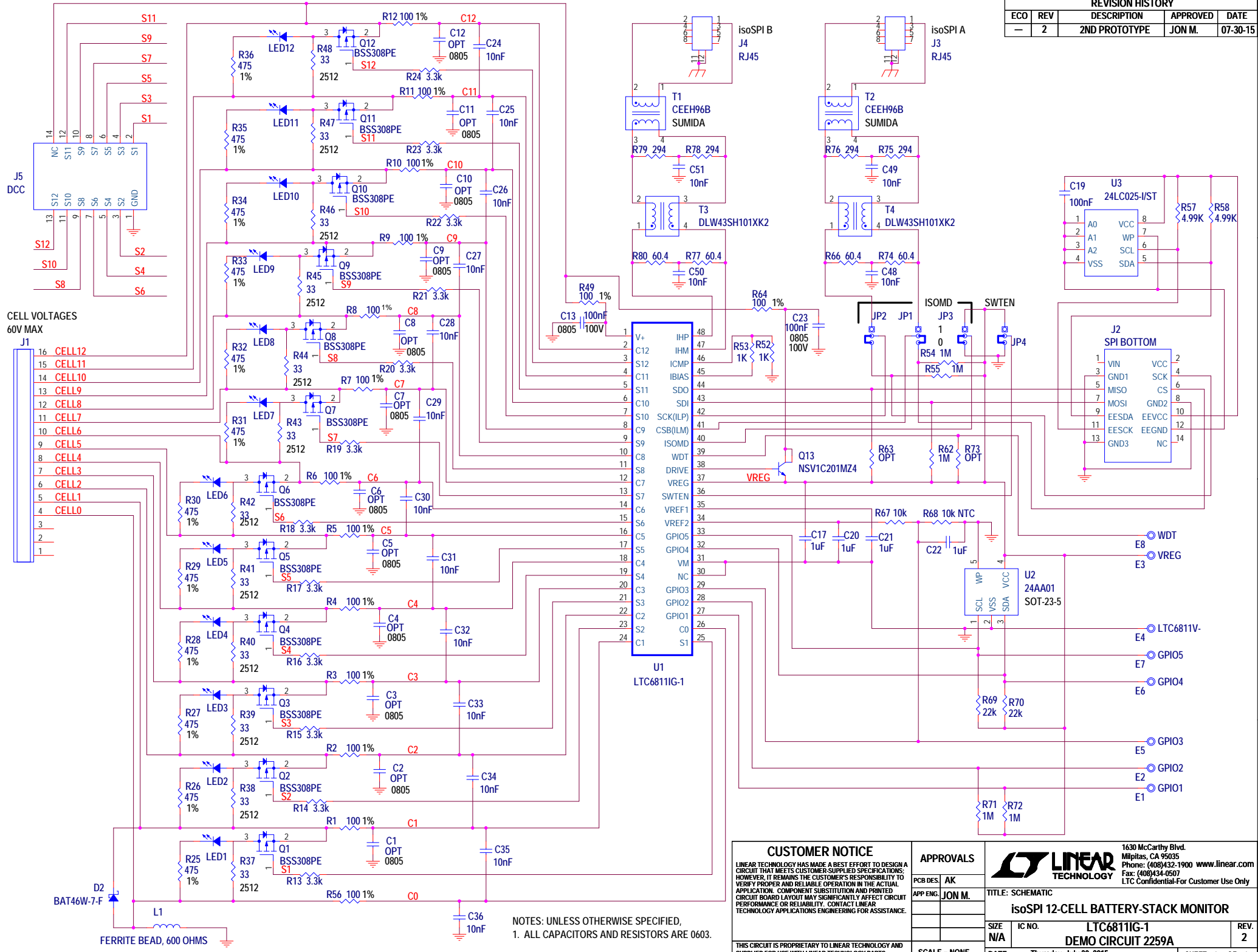


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	2ND PROTOTYPE	JON M.	07-30-15



- CELL VOLTAGES
60V MAX
- 16 CELL12
 - 15 CELL11
 - 14 CELL10
 - 13 CELL9
 - 12 CELL8
 - 11 CELL7
 - 10 CELL6
 - 9 CELL5
 - 8 CELL4
 - 7 CELL3
 - 6 CELL2
 - 5 CELL1
 - 4 CELLO
 - 3
 - 2
 - 1

NOTES: UNLESS OTHERWISE SPECIFIED,
1. ALL CAPACITORS AND RESISTORS ARE 0603.

CUSTOMER NOTICE
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS
PCB DES. AK
APP ENG. JON M.

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TITLE: SCHEMATIC
isoSPI 12-CELL BATTERY-STACK MONITOR

SIZE	IC NO.	LTC6811G-1	REV.	2
N/A		DEMO CIRCUIT 2259A		
DATE:	Thursday, July 30, 2015		SHEET	1 OF 1