

## C1210T682J5GCLTU

Aliases (C1210T682J5GCL7800)

SMD COTS COG, Ceramic, 6800 pF, 5%, 50 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 1210



Click [here](#) for the 3D model.

### Dimensions

Chip Size	1210
L	3.2mm +/-0.2mm
W	2.5mm +/-0.2mm
T	0.78mm +/-0.10mm
B	0.5mm +/-0.25mm

### Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	4000

### General Information

Series	SMD COTS COG
Style	SMD Chip
Description	SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	No
Prop 65	<b>⚠ WARNING:</b> Cancer and reproductive harm - <a href="http://www.p65warnings.ca.gov">http://www.p65warnings.ca.gov</a> .
SCIP Number	2d771165-5336-48a3-96fa-3663929fd828
Termination	Lead (SnPb)
Marking	No
Failure Rate	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A
AEC-Q200	No
Component Weight	30 mg
Shelf Life	78 Weeks
MSL	1

### Specifications

Capacitance	6800 pF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	5%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1kHz 1.0Vrms
Dissipation Factor	0.1% 1kHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms