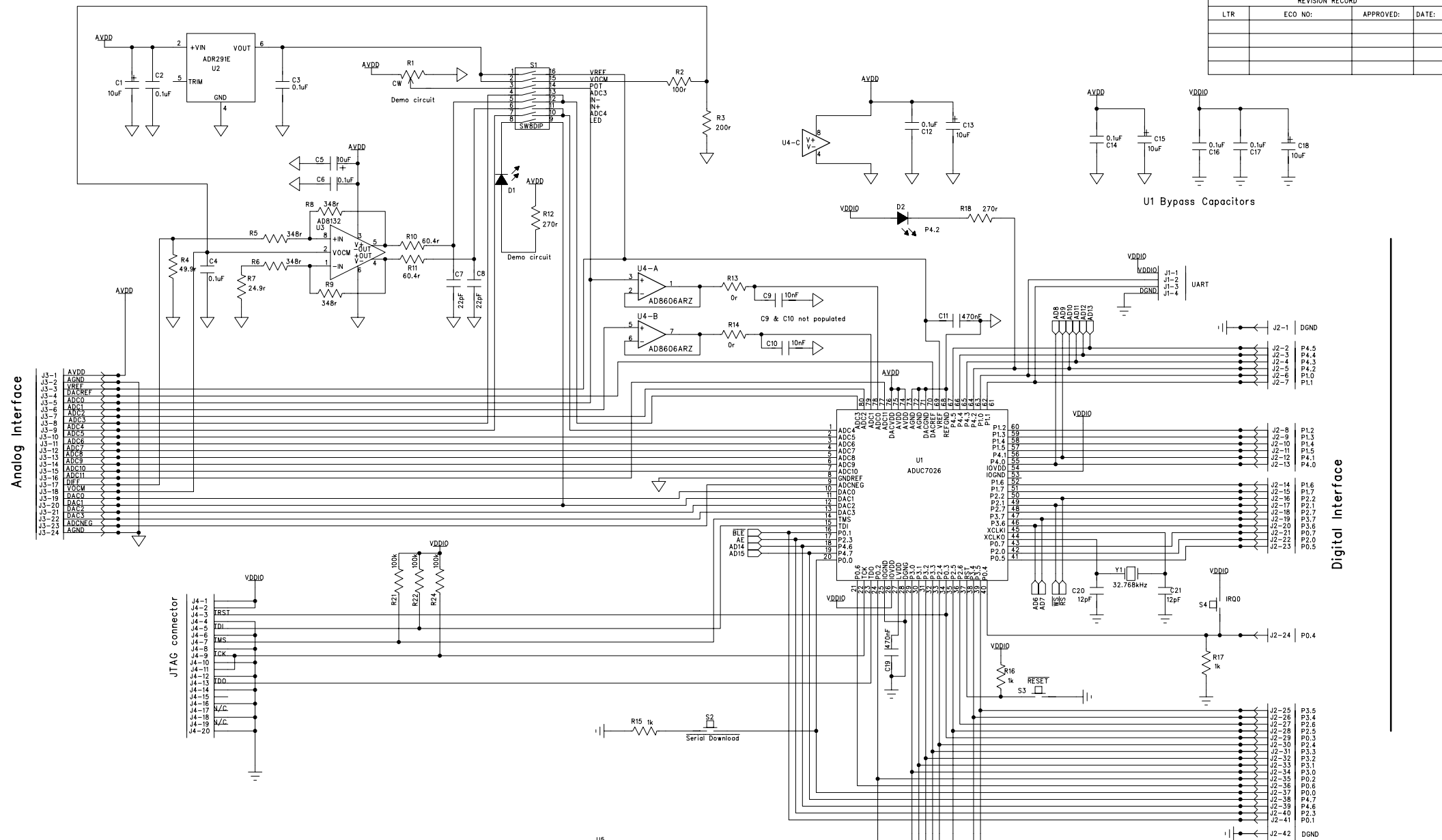


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



Analog Interface

Digital Interface

JTAG connector

Serial Download

COMPANY:		Analog Devices Inc.			
TITLE:		ADuC7026 Development System			
DRAWN:	DATED:	CODE:	SIZE:	DRAWING NO:	REV:
Pat Sheahan	Sept '04				
CHECKED:	DATED:				
Aude Richard	Sept '04				
QUALITY CONTROL:	DATED:				
RELEASED:	DATED:				
SCALE:				SHEET: 1 OF 2	

D

C

B

A

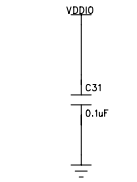
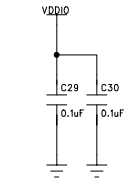
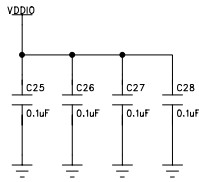
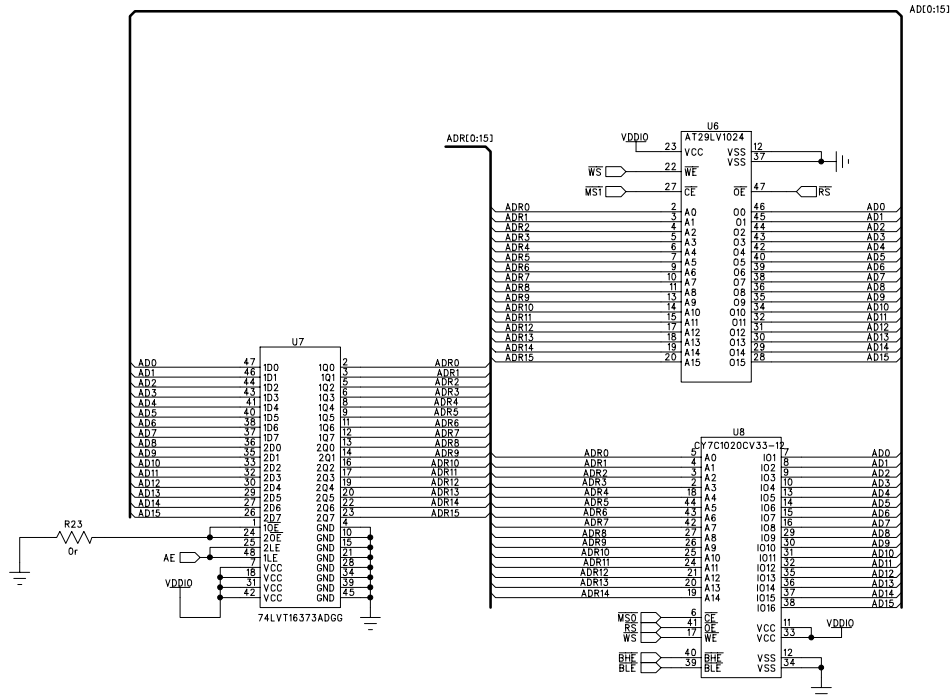
D

C

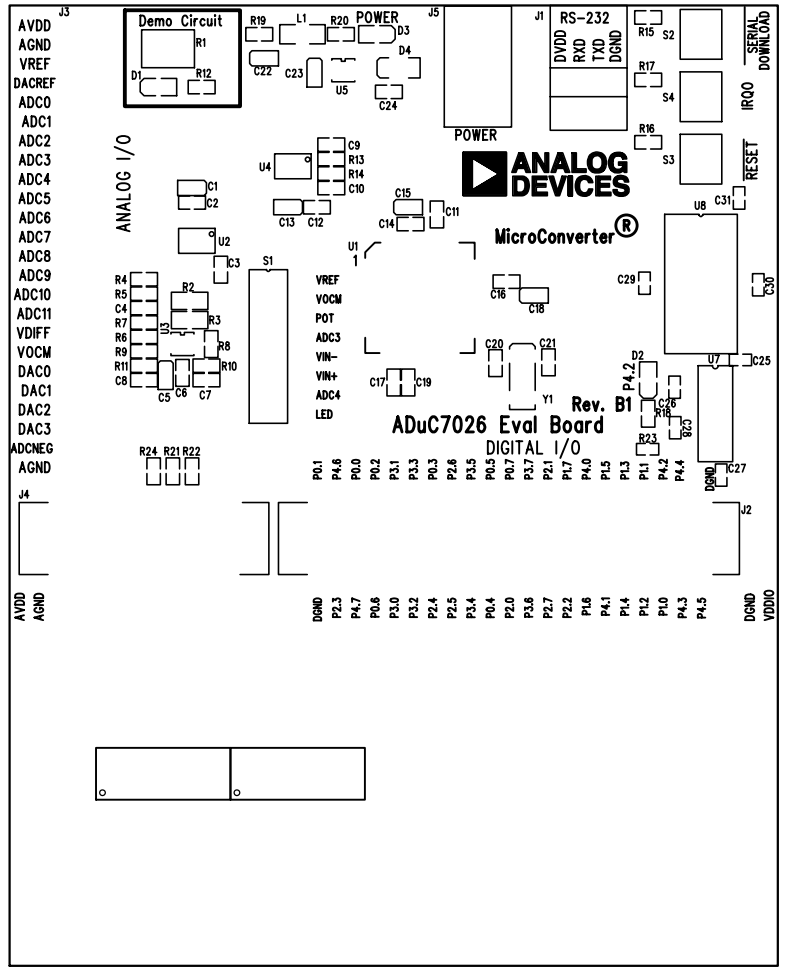
B

A

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

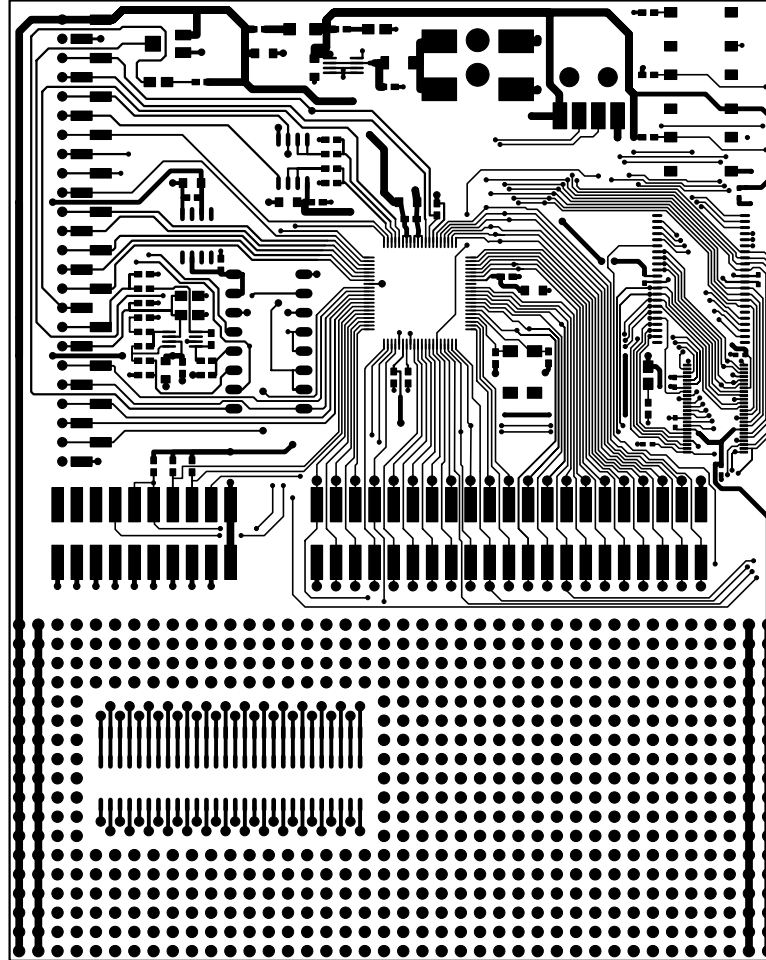


COMPANY:				Analog Devices Inc.			
TITLE:				ADuC7026 Development System			
DRAWN:	DATED:	CODE:	SIZE:	DRAWING NO:		REV:	
Pat Sheahan	Sept '04			EVAL-ADuC7026QS		B	
CHECKED:	DATED:	SCALE:		SHEET: 2 OF 2			
Aude Richard	Sept '04						
QUALITY CONTROL:	DATED:						
RELEASED:	DATED:						



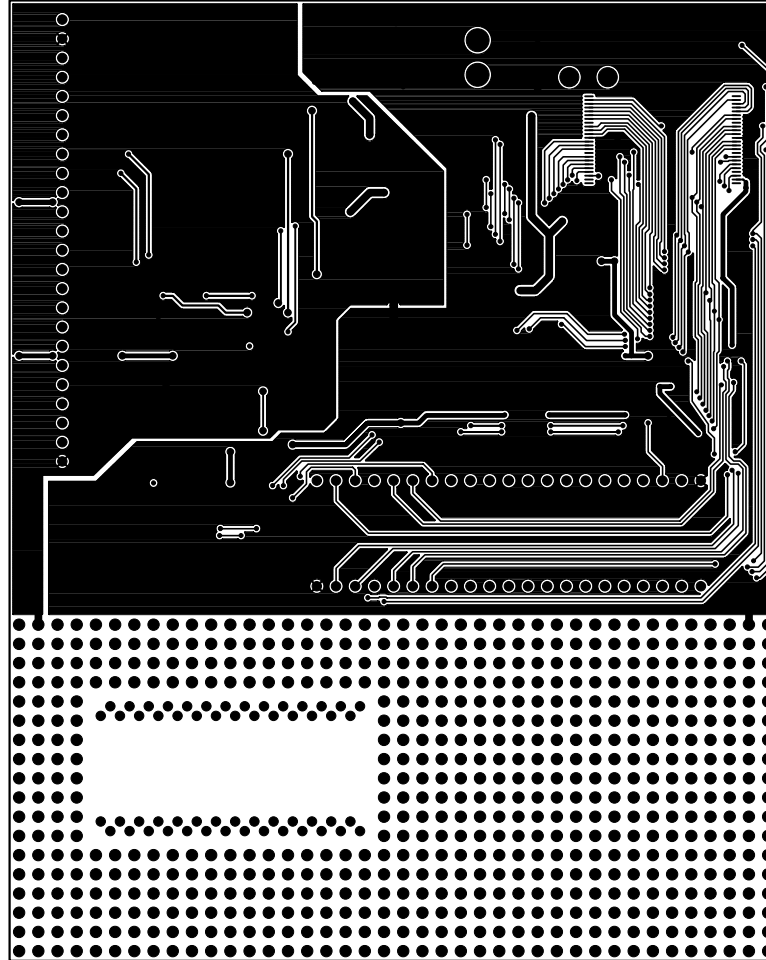
ADuC7026 EVALUATION BOARD Rev. B1 – Component Side View

Silkscreen Top



ADuC7026 EVALUATION BOARD Rev. B1 - Component Side View

Component Side



AduC7026 EVALUATION BOARD Rev. B1 - Component Side View

Solder Side