

## C0805T103G3GCLTU

## Aliases (C0805T103G3GCL7800)

SMD COTS COG, Ceramic, 0.01 uF, 2%, 25 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0805



Click here for the 3D model.

Dimensions	
Chip Size	0805
L	2mm +/-0.2mm
W	1.25mm +/-0.2mm
Т	0.78mm +/-0.10mm
S	0.75mm MIN
В	0.5mm +/-0.25mm

## **Packaging Specifications**

Packaging Packaging Quantity

T&R, 180mm, Paper Tape 4000

General Informat	ion
Series	SMD COTS COG
Style	SMD Chip
Description	SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	No
Prop 65	A WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov.
Termination	Lead (SnPb)
Marking	No
Failure Rate	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA- 469, Humidity per MIL-STD-202, Method 103, Condition A
AEC-Q200	No
Component Weight	11 mg
Shelf Life	78 Weeks
MSL	1

Specifications			
Capacitance	0.01 uF		
Measurement Condition	1 kHz 1.0Vrms		
Capacitance Tolerance	2%		
Voltage DC	25 VDC		
Dielectric Withstanding Voltage	62.5 VDC		
Temperature Range	-55/+125°C		
Temperature Coefficient	COG		
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1kHz 1.0Vrms		
Dissipation Factor	0.1% 1 kHz 1.0Vrms		
Aging Rate	0% Loss/Decade Hour		
Insulation Resistance	100 GOhms		

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