

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _c = +25°C
100V	222mΩ @ V _{GS} = 10V	8.0A
	270mΩ @ V _{GS} = 4.5V	7.3A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Load Switch

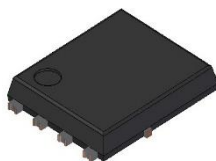
Features

- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Wettable Flank for Improved Optical Inspections
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

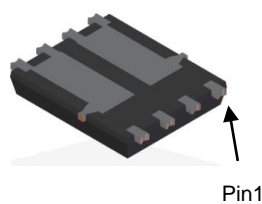
Mechanical Data

- Case: PowerDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.097 grams (Approximate)

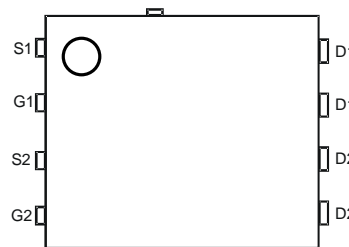
PowerDI5060-8 (SWP) (Type R)



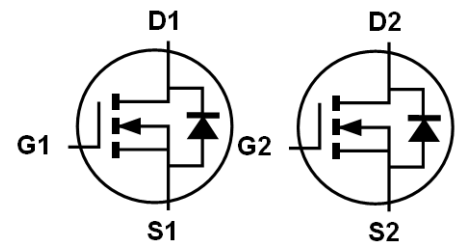
Top View



Bottom View



Pin out
Top View



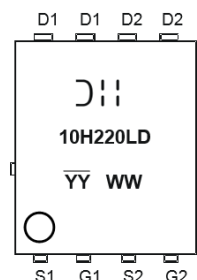
Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN10H220LPDW-13	PowerDI5060-8 (SWP) (Type R)	2500 / Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



D1 D1 D2 D2
 ◯
 10H220LD
 YY WW
 S1 G1 S2 G2

◯ = Manufacturer's Marking
 10H220LD = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Year (ex: 19 = 2019)
 WW = Week (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	100	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	I _D	T _C = +25°C	8.0
		T _C = +70°C	6.4
Maximum Body Diode Forward Current (Note 6)	I _S	8.0	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	32	A
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%)	I _{SM}	32	A
Avalanche Current (Note 7)	L = 0.1mH I _{AS}	4.7	A
Avalanche Energy (Note 7)	L = 0.1mH E _{AS}	1.1	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	2.2	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	57	°C/W
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	5.8	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	100	—	—	V	V _{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	µA	V _{DS} = 100V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	1	—	2.5	V	V _{DS} = V _{GS} , I _D = 250µA
Static Drain-Source On-Resistance	R _{DSON}	—	168	222	mΩ	V _{GS} = 10V, I _D = 2A
		—	208	270	mΩ	V _{GS} = 4.5V, I _D = 1A
Diode Forward Voltage	V _{SD}	—	0.8	1.3	V	V _{GS} = 0V, I _S = 2A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{ISS}	—	384	—	pF	V _{DS} = 25V, f = 1MHz, V _{GS} = 0V
Output Capacitance	C _{OSS}	—	23	—		
Reverse Transfer Capacitance	C _{RSS}	—	17	—		
Gate Resistance	R _G	—	2.4	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	3.7	—	nC	V _{DD} = 50V, I _D = 1.6A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	6.7	—		
Gate-Source Charge	Q _{gs}	—	1.3	—		
Gate-Drain Charge	Q _{gd}	—	2	—		
Turn-On Delay Time	t _{D(ON)}	—	6.2	—	ns	V _{DD} = 50V, V _{GS} = 4.5V, R _G = 6.8Ω, I _D = 1.0A
Turn-On Rise Time	t _R	—	8.7	—		
Turn-Off Delay Time	t _{D(OFF)}	—	7.4	—		
Turn-Off Fall Time	t _F	—	4.2	—		
Body Diode Reverse Recovery Time	t _{RR}	—	20	—	ns	I _S = 1.1A, dI/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q _{RR}	—	11	—	nC	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 - Thermal resistance from junction to soldering point (on the exposed drain pad).
 - I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

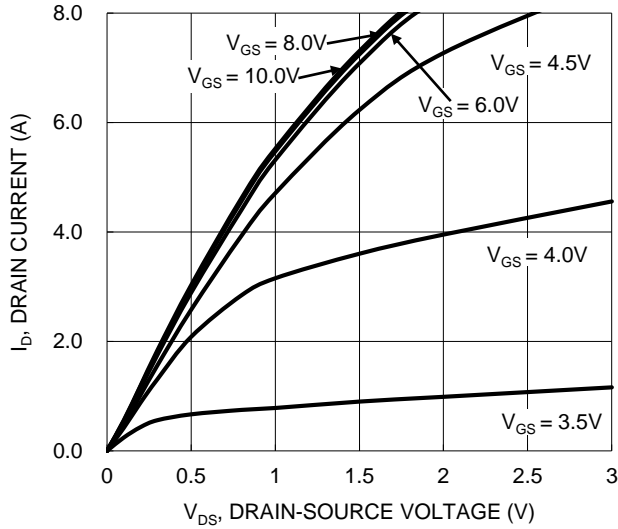


Figure 1. Typical Output Characteristic

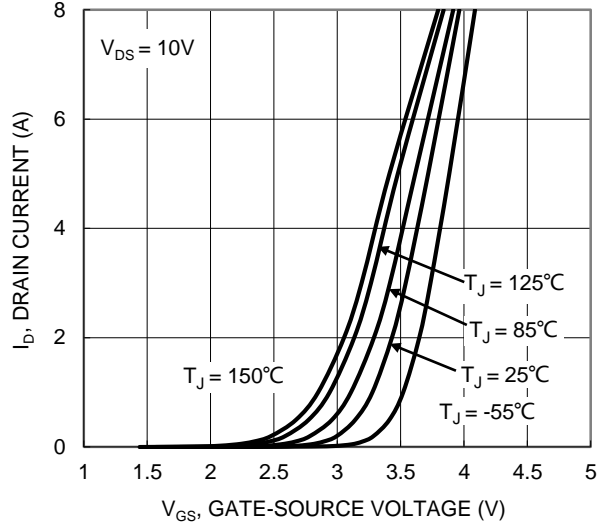


Figure 2. Typical Transfer Characteristic

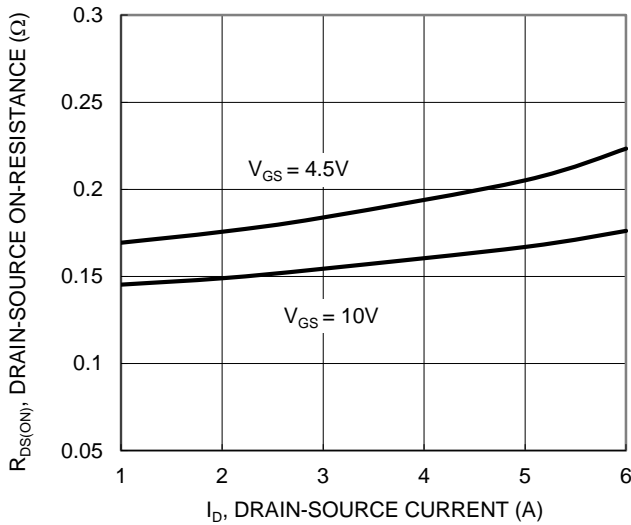


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

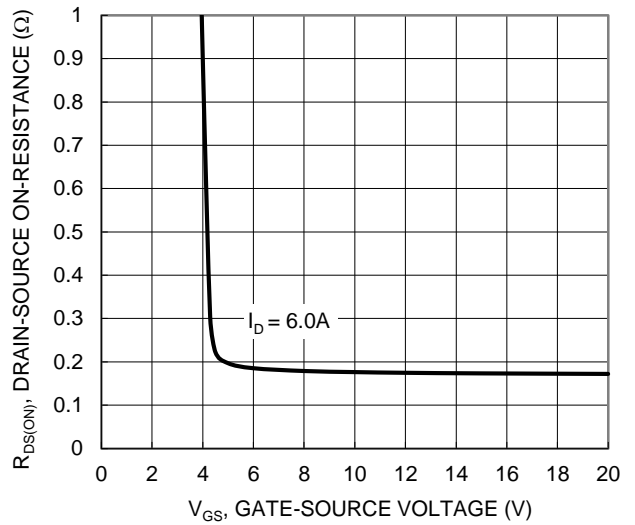


Figure 4. Typical Transfer Characteristic

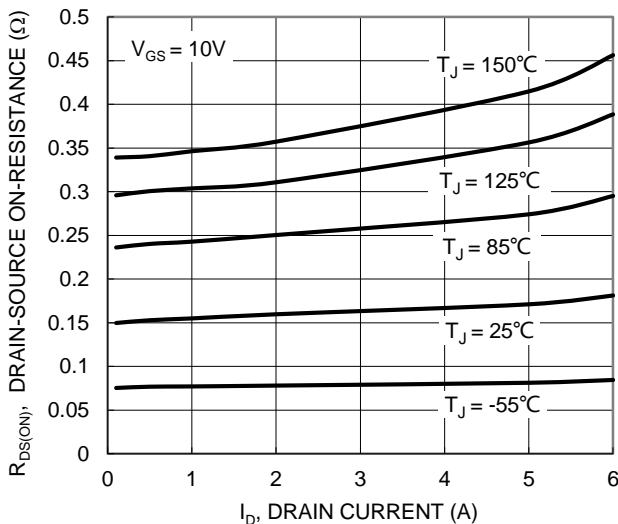


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

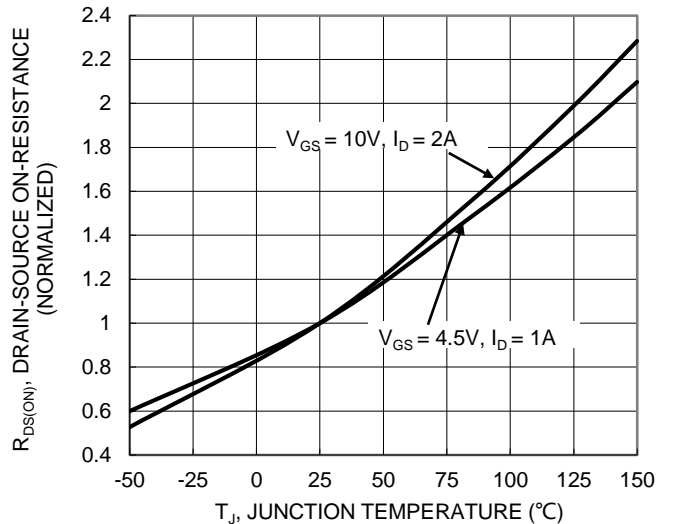


Figure 6. On-Resistance Variation with Junction Temperature

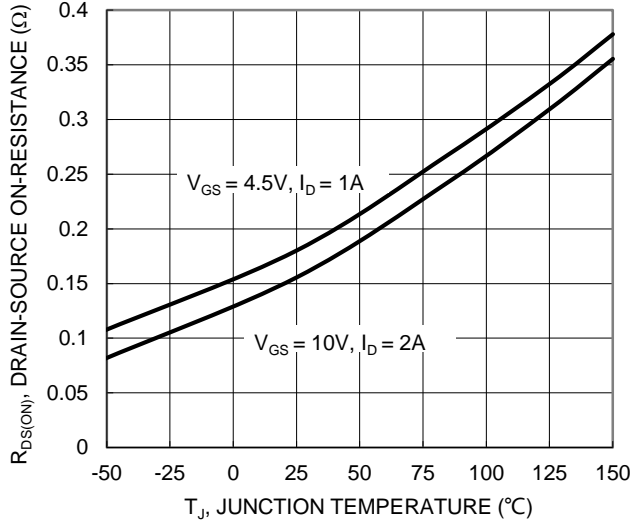


Figure 7. On-Resistance Variation with Junction Temperature

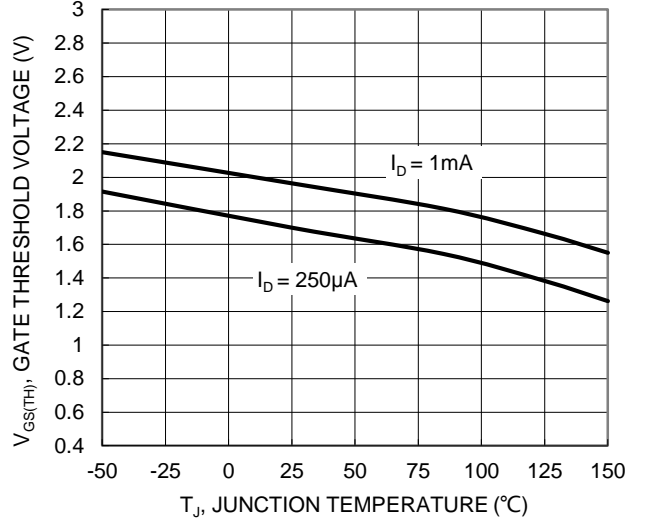


Figure 8. Gate Threshold Variation vs. Junction Temperature

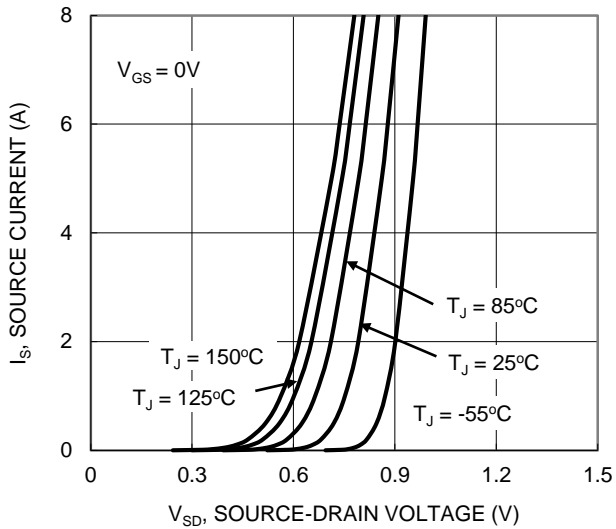


Figure 9. Diode Forward Voltage vs. Current

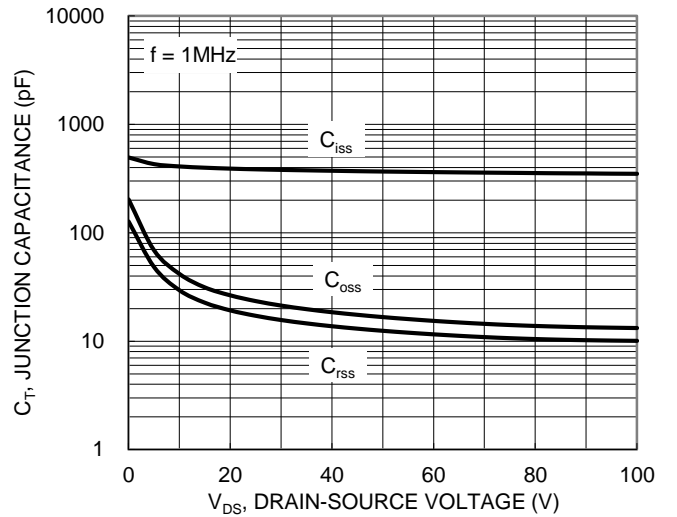


Figure 10. Typical Junction Capacitance

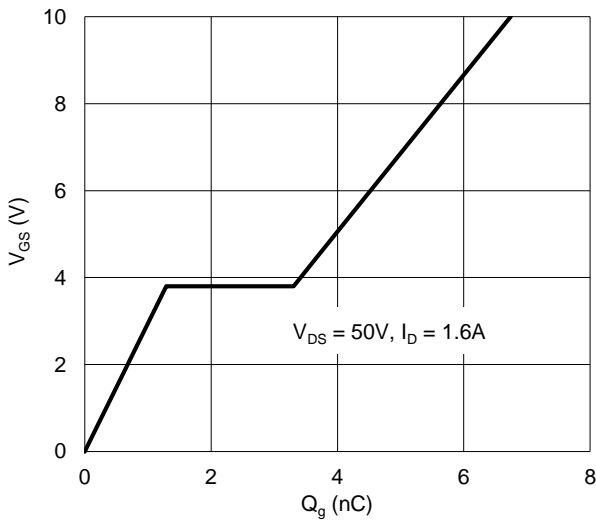


Figure 11. Gate Charge

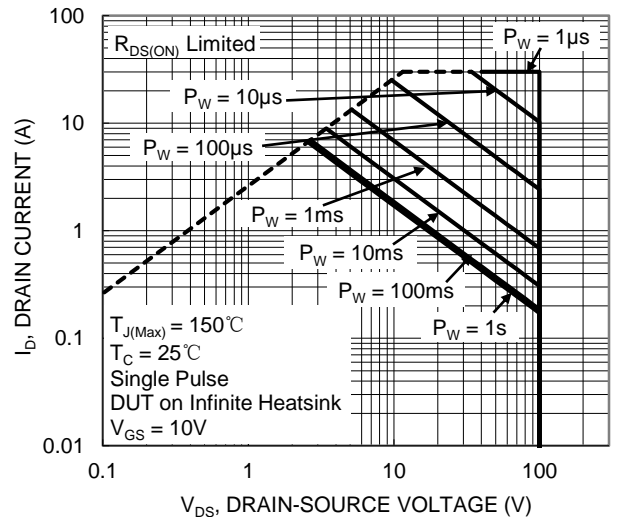


Figure 12. SOA, Safe Operation Area

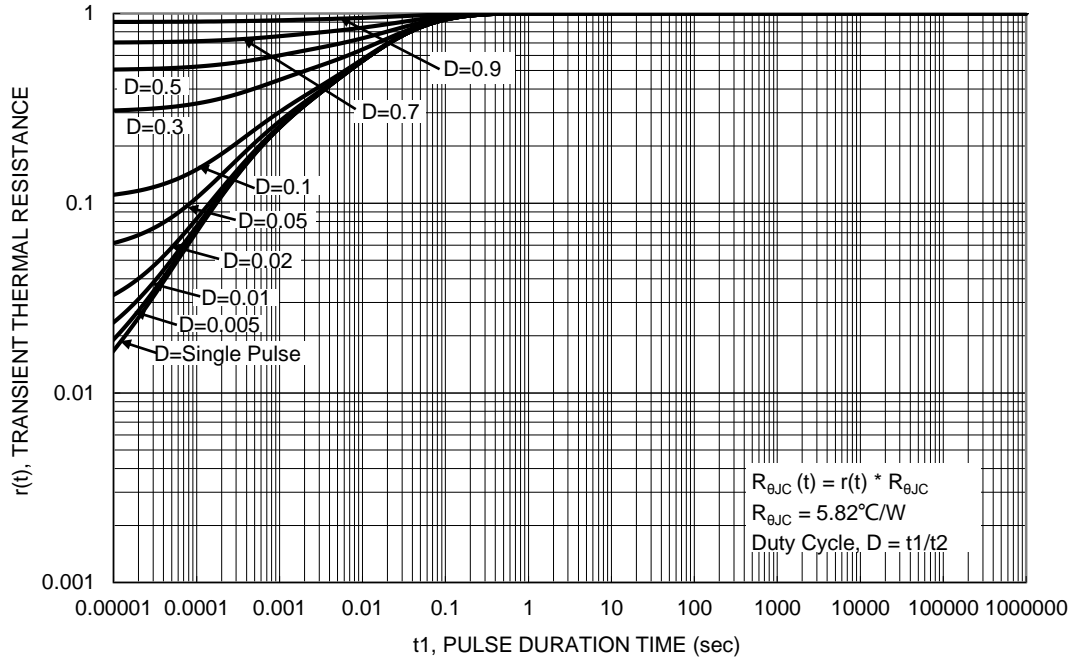
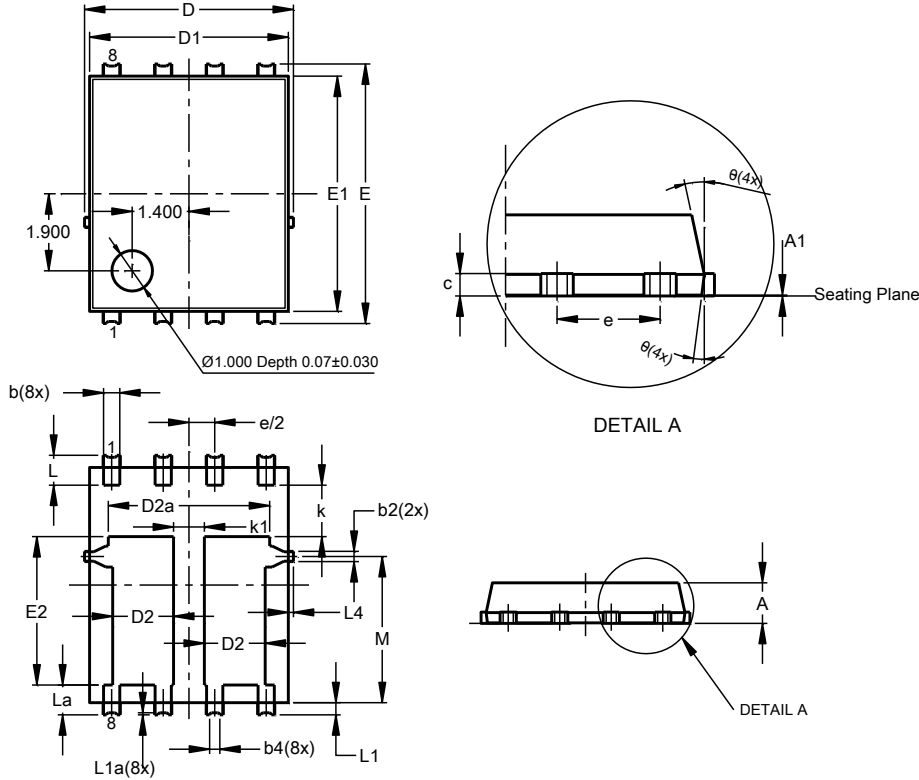


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8 (SWP) (Type R)

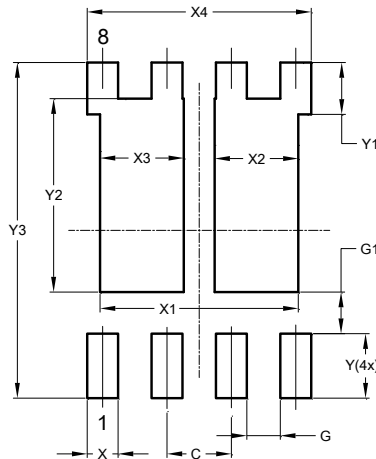


PowerDI5060-8 (SWP) (Type R)			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0	0.05	--
b	0.30	0.50	0.41
b2	0.20	0.35	0.25
b4	0.25REF		
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	1.40	1.60	1.50
D2a	3.78	4.18	3.98
E	6.40 BSC		
E1	5.60	6.00	5.80
E2	3.46	3.86	3.66
e	1.27BSC		
k	1.05	--	--
k1	0.56	--	--
L	0.635	0.835	0.735
La	0.635	0.835	0.735
L1	0.200	0.400	0.300
L1a	0.050REF		
L4	0.025	0.225	0.125
M	3.205	4.005	3.605
θ	10°	12°	11°
θ1	6°	8°	7°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8 (SWP) (Type R)



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	3.910
X2	1.650
X3	1.650
X4	4.420
Y	1.270
Y1	1.020
Y2	3.810
Y3	6.610

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