



Click [here](#) for the 3D model.

### Dimensions

|           |                 |
|-----------|-----------------|
| Chip Size | 0402            |
| L         | 1mm +/-0.05mm   |
| W         | 0.5mm +/-0.05mm |
| T         | 0.5mm +/-0.05mm |
| S         | 0.3mm MIN       |
| B         | 0.3mm +/-0.1mm  |

### Packaging Specifications

|                    |                        |
|--------------------|------------------------|
| Packaging          | T&R, 180mm, Paper Tape |
| Packaging Quantity | 10000                  |

### General Information

|                  |  |
|------------------|--|
| Series           | SMD COTS COG   |
| Style            | SMD Chip   |
| Description      | SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I   |
| Features         | Ultra-Stable, Low Loss, Class I  |
| RoHS             | No   |
| Prop 65          | <b>⚠ WARNING:</b> Cancer and reproductive harm - <a href="http://www.p65warnings.ca.gov">http://www.p65warnings.ca.gov</a> . |
| Termination      | Lead (SnPb)  |
| Marking          | No   |
| Failure Rate     | Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A                         |
| AEC-Q200         | No   |
| Component Weight | 1.06 mg  |
| Shelf Life       | 78 Weeks   |
| MSL              | 1  |

### Specifications

|  |                        |
|--|------------------------|
| Capacitance  | 220 pF                 |
| Measurement Condition  | 1 MHz 1.0Vrms          |
| Capacitance Tolerance  | 1%                     |
| Voltage DC   | 50 VDC                 |
| Dielectric Withstanding Voltage                                    | 125 VDC                |
| Temperature Range  | -55/+125°C             |
| Temperature Coefficient  | COG                    |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 30 ppm/C, 1MHz 1.0Vrms |
| Dissipation Factor   | 0.1% 1 MHz 1.0Vrms     |
| Aging Rate   | 0% Loss/Decade Hour    |
| Insulation Resistance  | 100 GOhms              |