

C1210T685K3RBLTU

Aliases (C1210T685K3RBL7800)

SMD COTS X7R, Ceramic, 6.8 uF, 10%, 25 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 1210



Click here for the 3D model.

Dimensions	
Chip Size	1210
L	3.2mm +/-0.3mm
W	2.5mm +/-0.22mm
Т	1.7mm +/-0.20mm
В	0.5mm +/-0.25mm

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	2000

General Informatic	n
Series	SMD COTS X7R
Style	SMD Chip
Description	SMD, MLCC, COTS, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	No
Prop 65	A WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov.
Termination	Lead (SnPb)
Marking	No
Failure Rate	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA- 469
AEC-Q200	No
Component Weight	85 mg
Shelf Life	78 Weeks
MSL	1

Specifications	
Capacitance	6.8 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	10%
Voltage DC	25 VDC
Dielectric Withstanding Voltage	62.5 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	3.5%1kHz1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	73.5 MOhms

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