



Click [here](#) for the 3D model.

| Dimensions | |
|------------|--------------------|
| Chip Size | 6560 |
| L | 16.5mm +/-0.76mm |
| W | 15.2mm +/-0.76mm |
| T | 5.59mm NOM |
| B | 1.143mm +/-1.524mm |

| Packaging Specifications | |
|--------------------------|----|
| Packaging Quantity | 50 |

| General Information | |
|---------------------|--|
| Series | SMD Mil-Equiv X7R HV 49467 |
| Style | SMD Chip |
| Description | Surface Mount Ceramic Capacitor |
| RoHS | No |
| Prop 65 | ⚠ WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov . |
| SCIP Number | f6abf01f-fbb0-4a02-bb15-583285915f66 |
| Termination | Palladium Silver |
| AEC-Q200 | No |

| Specifications | |
|--|---------------------|
| Capacitance | 2.2 uF |
| Capacitance Tolerance | 10% |
| Voltage DC | 500 VDC |
| Dielectric Withstanding Voltage | 750 VDC |
| Temperature Range | -55/+125°C |
| Temperature Coefficient | X7R |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 30 ppm/C |
| Dissipation Factor | 2.5% |
| Aging Rate | 2% Loss/Decade Hour |
| Insulation Resistance | 455 MOhms |