



180525309 Addendum to 180417271 Si53019-A01A and Si53019-A02A Datasheet Update

Bulletin Issue Date: 5/25/2018

Effective Date: 5/25/2018

Description of Change

This PCN is being issued as an addendum to PCN 180417272 to correct a typographical error. The revision 1.3 datasheet displays the corrected Block Write Protocol diagram showing Resiter# to write 2x7 bit =0, however the Change Reason section of PCN 180417272 stated it as 2x7 = 1. The Change Reason section of the PCN has been updated.

Silicon Labs is pleased to announce revision1.6 of the Si53019-A01A datasheet, and revision1.3 of the Si53019-A02A datasheet.

Reason for Change

The following updates were made to the Si53019-A01A and Si53019-A02A datasheets to correct errors:

- 1) Device ID of 11011011 (DB) in the Byte6 Device ID Control Register has been updated to 11100111(E7)
- 2) Vendor ID of 0001 in the Byte5 Vendor/Revision Identification Control Register has been updated to 1000
- 3) Figure 11 Block Write Protocol diagram has been updated to show Register# to write 2x7 bit = 0

Product Identification

Si53019-A01AGM
Si53019-A01AGMR
Si53019-A02AGM
Si53019-A02AGMR

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

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Customer Actions Needed:

None.

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