



Product Change Notification / SYST-30CLJX590

Date:

02-Jul-2021

Product Category:

32-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

Data Sheet - PIC32MZ Graphics (DA) Family Data sheet Revision

Affected CPNs:

[SYST-30CLJX590_Affected_CPN_07022021.pdf](#)

[SYST-30CLJX590_Affected_CPN_07022021.csv](#)

Notification Text:

SYST-30CLJX590

Microchip has released a new Product Documents for the PIC32MZ Graphics (DA) Family Data sheet of devices. If you are using one of these devices please read the document located at [PIC32MZ Graphics \(DA\) Family Data sheet](#).

Notification Status: Final

Description of Change: This revision includes the following major changes:

1) General

1.1) Throughout the document, References to both "Master" and "Slave" were revised to read "Host" and "Client" where applicable

1.2) Updated "Operating Conditions" to reflect correct temperature ranges

2) 2.0 "Guidelines for Getting Started with 32-bit Microcontrollers" - Updated 2.9.2 "5V Tolerant Input Pins", correcting VDD to VDDIO, and updated VDDIO specifications in the text

3) 8.0 "Oscillator Configuration"

3.1) Removed erroneous information within parenthesis on Note 6 for Table 8-1: "System and Peripheral clock Distribution"

3.2) Updated Register 8-4: "REFOxCON: Reference Oscillator Control Register ('x' = 1-5)" to properly display x=1-5, and added a note number 4

4) 17.0 "Deadman Timer (DMT)" - Updated to note for Register 17-1: "DMTCON: Deadman Timer Control Register"

5) 21.0 "Serial Peripheral Interface (SPI) and Inter-ICSound (I2S)" - Updated FIGURE 21-1: "SPI/I2S Module Block Diagram" to read "SPI Select" instead of "Slave Select"

- 6) 25.0 "Parallel HOST Port (PMP)"
 - 6.1) Added a new note to the IRQM bit of Register 25-2: "PMMODE: Parallel Port Mode Register"
 - 6.2) Added a new note to the IBOV bit of Register 25-7: "PMSTAT: Parallel Port Status Register (Client modes only)"
- 7) 35.0 "Charge Time Measurement Unit (CTMU)" - Added AN5 to AN39 to the first bulleted item on page 585
- 8) 36.0 "Graphics LCD (GLCD) Controller" - Corrected typographical errors in bitfield 5-0 for Register 36-2: "GLCDCLKCON: Graphics LCD Controller Clock Control Register"
- 9) 38.0 "DDR2 SDRAM Controller"
 - 9.1) Added a note to bit 28 of Register 38-24: "DDRSCLSTART: DDL SELF CALIBRATION LOGIC START REGISTER"
 - 9.2) Corrected a typographical error in the DLYSTVAL bitfield of Register 38-29: "DDRPHYDLLR: DDR PHY DLL Recalibrate Register"
- 10) 40.0 "Power-Saving Features"
 - 10.1) Updated FIGURE 40-1: "XLP Device Block Diagram" to properly display VDDCORE
 - 10.2) A new note was added to 40.4 "Peripheral Module Disable"
- 11) 41.0 "Special Features"
 - 11.1) Added a new note to the FDMTEN bitfield for Register 41-4: "DEVCFG1/ADEVCFG1: Device Configuration Word 1"
 - 11.2) Corrected typographical errors in 41.4 "On-Chip Voltage Regulator"
- 12) 44.0 "Electrical Characteristics"
 - 12.1) Updated Table 44-24: "System Timing Requirements" with new conditions for the Reference Clock frequency
 - 12.2) Updated Table 44-26: "MPLL Clock Timing Requirements" with a new maximum value for the MP12 Parameter
 - 12.3) Updated the characteristics for Table 44-55: "GLCD Controller Timing Specifications" with the addition of GLCD pin verbiage
 - 12.4) Updated TABLE 44-56: "Internal DDR2 SDRAM Timing Specifications" with a correction to note 5
- 13) 46.0 "Packaging Information" - Updated the Descriptions for the 169-Ball Low Profile Ball Grid Array
- 14) "Product Identification System" - Updated the Package description for the 169 Lead LFBGA

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 02 July 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[PIC32MZ Graphics \(DA\) Family Data sheet](#)

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Affected Catalog Part Numbers (CPN)

PIC32MZ1025DAG176-I/2J
PIC32MZ1025DAH176-I/2J
PIC32MZ1064DAG176-I/2J
PIC32MZ1064DAH176-I/2J
PIC32MZ2025DAG176-I/2J
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