



Cypress Semiconductor Corporation, 198 Champion Court, San Jose, CA 95134-1709 (408)943-2600

PRODUCT INFORMATION NOTIFICATION

PIN: PIN171002

Date: March 14, 2017

Subject: Qualification of ChipMOS, Taiwan as an Additional Finish Site for 28-Lead TSOP I Products

Change Type: Minor

Description of Change:

Cypress announces the qualification of ChipMOS Technologies Inc as an additional Finish site for 28-Lead TSOP I products. ChipMOS is located at Hsinchu, Taiwan and it is already a qualified strategic Finish site for other Cypress products. They have achieved ISO9001 and ISO14001 certification.

ChipMOS is a leading independent provider of total semiconductor testing and packaging solutions. This qualification will enable ChipMOS to do Test and Finish manufacturing for the 28-Lead TSOP I products and leverage ChipMOS's manufacturing expertise and quality focus. This change is part of the flexible manufacturing initiative which allows Cypress to meet its delivery commitments in dynamic, changing market conditions.

There is no change to the existing Finish manufacturing flows and packing materials.

Benefit of Change:

This change will provide additional Finish manufacturing capacity and reduce lead-times.

Affected Part Numbers: 30

Affected Parts: See the attached 'Affected Parts List' file for a list of all part numbers affected by this notification. Note that any new parts that are introduced after the publication of this PIN will include all changes outlined in this PIN.

Qualification Status:

The changes listed in this PIN have been qualified through a series of tests documented in the Qualification Test Plan (QTP) report 170201. The report can be found as an attachment to this notification.

Approximate Implementation Date:

This change will be implemented from the date of this notification.

Anticipated Impact:

Products Finish processed at ChipMOS are completely compatible with existing product from a functional, parametric, and quality performance perspective. No adverse impact is expected.

Cypress also recommends that customers take this opportunity to review these changes against current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

Method of Identification:

Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

Response Required:

This is an information only announcement. No response is required

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at pcn_adm@cypress.com.

Sincerely,

Cypress PCN Administration

Item	Marketing Part Number
1	CY62256NLL-55ZXI
2	CY62256NLL-55ZXIT
3	CY62256VNLL-70ZXC
4	CY62256VNLL-70ZXCT
5	CY62256VNLL-70ZXI
6	CY62256VNLL-70ZXIT
7	CY7C1399BN-12ZXC
8	CY7C1399BN-12ZXCT
9	CY7C1399BN-15ZXI
10	CY7C1399BN-15ZXIT
11	CY7C1399BNL-12ZXC
12	CY7C1399BNL-12ZXCT
13	CY7C199D-10ZXI
14	CY7C199D-10ZXIT
15	CY7C199NL-15ZXC
16	CY7C199NL-15ZXCT
17	CG7880AA
18	CG7880AAT
19	CG8188AA
20	CG8188AAT
21	CG8213AA
22	CG8213AAT
23	CG8248AA
24	CG8248AAT
25	CG8260AA
26	CG8260AAT
27	CG8261AA
28	CG8261AAT
29	CG8310AA
30	CG8310AAT