



Cypress Semiconductor Corporation, 198 Champion Court, San Jose, CA 95134. Tel: (408) 943-2600

PRODUCT INFORMATION NOTIFICATION

PIN: PIN155102

Date: December 15, 2015

Subject: Qualification of Tera Probe (Kumamoto, Japan) as an Additional Wafer-Level Test Location.

To: PCN ADMIN
CYPRESS
pcn_adm@cypress.com

Change Type: Major

Description of Change:

Cypress announces the qualification of wafer-level test operations at Tera Probe (largest wafer-level automotive certified test house) in Kumamoto, Japan for the 300 mm Cypress products that are currently being tested by Fujitsu Semiconductor Limited (FSL). Tera Probe is already a qualified subcontractor for Fujitsu Semiconductor Limited and was originally qualified in 2013 for both automotive and commercial customers. The change being reported in this notification is an operational change where Cypress will directly handle wafer test operations at Tera Probe instead of Fujitsu Semiconductor Limited. There will be no changes to the tester platforms, test program coverage or test methodologies.

Benefit of Change:

This change will allow Cypress to consolidate and expand 300 mm wafer-level test capacity to meet Cypress wafer volume demands and ensure on-time delivery to customers. In addition, Cypress will be able to exercise Tera Probe's expertise in wafer test operations along with Cypress quality control systems to continue to meet its customers' IC testing needs as well as delivery commitments in dynamic, changing market conditions. Tera Probe is certified on several international quality standards: ISO / TS16949, ISO 9001, ISO 14001 and OHSAS 18001.

Affected Part Numbers: 1373

Affected Parts: Please see the attached file for the list of parts affected by this notice.

Qualification Status:

Tera Probe has been qualified through a series of tests documented in the Qualification Test Plan (QTP) 154901. The qualification report can be found as an attachment to this notification or by visiting www.cypress.com, and typing the QTP number in the keyword search window.

Approximate Implementation Date:

Cypress will begin implementing this change starting from the date of this notification. For consumer/commercial devices, wafer-level testing will commence immediately. For automotive devices, wafer-level testing will begin migration to Tera Probe effective 02/15/2015. Devices currently being tested at Tera Probe through Fujitsu operation will continue to test in this location during this time.

Anticipated Impact:

Products that will be tested at Tera Probe will be completely compatible with existing products from a functional, parametric, and quality performance perspective.

Cypress also recommends that customers take this opportunity to review these changes against their current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

Method of Identification:

Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

Response Required:

This is an information announcement only. No response is required

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at pcn_adm@cypress.com.

Sincerely,

Cypress PCN Administration

Item	Marketing Part Number
1	8611200172
2	A2C00568600
3	A2C01835300
4	A2C01835400
5	A2C01976700
6	A2C01976701
7	A2C01976800
8	A2C01976900
9	A2C01977100
10	A2C02234900
11	A2C02235000
12	MB86C36PMC-G-BNDE1
13	MB86C36PMC-G-JNE1
14	MB86C36PMC-G-JN-YE1
15	MB8AA4521WQN-GS-JKERE1
16	MB91F057PMC-GSE1
17	MB91F058ASPMC-GSE1
18	MB91F058BSPMC-GSE1
19	MB91F058BSPMC-GSE2
20	MB91F060ASPMC-GSE1
21	MB91F060BSPMC-GSE1
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261	MB9AF111KPMC-G-JNEFE2
262	MB9AF111KPMC-G-JNERE2
263	MB9AF111KQN-G-AVE2
264	MB9AF111KQN-G-AVEFE2
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268	MB9AF111LAPMC1-G-106JNE2
269	MB9AF111LAPMC1-GE1
270	MB9AF111LAPMC1-G-JNE2
271	MB9AF111LAPMC-GE1
272	MB9AF111LAPMC-G-JNE2
273	MB9AF111LAQN-G-AVE2
274	MB9AF111LPMC1-GE1
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277	MB9AF111LPMC-G-JNE1
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279	MB9AF111MAPMC-GE1
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