



Product Discontinuation Notice For Development Systems Product

XCN12019 (v1.0) August 27, 2012

Product Discontinuation Notice

Overview

The purpose of this notification is to communicate that Xilinx is discontinuing certain Development Systems products.

Description

As part of standard Xilinx product review activities, the following products have been identified for obsolescence. This notice should be used in conjunction with previous discontinuance notices which can be found at http://www.xilinx.com/support/documentation/customer_notices.htm.

Products Affected

1. [Xilinx® Development Systems Kits and Boards Products on While Supplies Last](#)

Table 1: Xilinx Development Systems Products on While Supplies Last

Part Number	Description	Suggested Replacement
HW-SPAR3E-SK-UNI-G	Spartan®-3E Starter Kit	EK-S6-SP601-G
HW-SPAR3E-SK-UNI-G-J	Spartan-3E Starter Kit, Japan Specific	EK-S6-SP601-G-J
SK-CR11-L-G	CoolRunner™-II Low Power/Cost Starter Kit	www.digilentinc.com/coolrunner2
HW-V4-ML401-UNI-G	Virtex®-4 General Purpose/FPGA Evaluation Board	EK-V6-ML605-G
HW-V4-ML401-UNI-G-J	Virtex-4 General Purpose/FPGA Evaluation Board, Japan Specific	EK-V6-ML605-G-J
HW-V4-ML403-UNI-G	Virtex-4 Processor and General Purpose/FPGA Evaluation Board (Lead-Free)	EK-V6-ML605-G
HW-V4-ML403-UNI-G-J	Virtex-4 ML403 Embedded Platform, Japan Specific	EK-V6-ML605-G-J
HW-V5-ML525-UNI-G	Virtex®-5 LXT ML525 RocketIO™ Characterization Platform	CK-V6-ML623-G
HW-V5-ML525-UNI-G-J	Virtex-5 LXT FPGA ML525 RocketIO Characterization Platform, Japan Specific	CK-V6-ML623-G-J
HW-V4-ML410-UNI-G	Virtex-4 Embedded System Development Board (Lead-Free)	HW-V5-ML510-G
HW-V4-ML410-UNI-G-J	Virtex-4 Embedded System Development Board, Japan Specific (Lead-Free)	HW-V5-ML510-G-J
HW-MLX10-RACK-G	Xilinx 19-inch 1U Rack-Mount Chassis	No Xilinx Suggested Replacement
DO-V5-DSP-DK-SG-UNI-G	XtremeDSP™ Development Kit – Virtex-5	AES-V6DSP2-LX240T-G
DO-V5-DSP-DK-SG-UNI-G-J	XtremeDSP™ Development Kit – Virtex-5 (Japan)	AES-V6DSP2-LX240T-G-J

NOTE:

1. Reference designs provided with all the above Development Systems are intended to be used as is.
2. The last version of design environment to support the reference designs is ISE® 11.4.

Key Dates and Ordering Information

Multiple Xilinx Development Systems Kits and Boards

Available While Supplies Last: The products listed are in stock, but will NOT be replenished when inventory has been depleted. When this occurs the product will be discontinued and no longer available for order. If the suggested replacement product is not acceptable, please contact Xilinx Customer Service and they can refer you to the Xilinx contract manufacturer and you may possibly be able to purchase the board directly from the contract manufacturer.

Six months after the obsolescence process begins no technical support will be available for the product.

Technical Support

For Technical Support on these products refer to Solution Center [Xilinx Answer Record 40174](#). Support for these products will be limited to tools usage and existing documentation.

Response

Customers are requested to place last-time-buy orders in a timely manner prior to the key dates listed above to avoid product availability issues. If you anticipate that your last time buy requirements will exceed the current rate, please provide your Xilinx Sales Representative with a forecast of the requirements. All orders will be supported on a first-come, first-served basis. All orders placed within 60 days of the last time buy date will be non-cancellable and non-returnable (NCNR). For additional information or questions, contact your local Xilinx Sales representative.

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the Support website (<http://www.xilinx.com/support>). Register today and personalize your "Documentation and Design Advisory Alerts" area to include Customer Notifications. Xilinx Support provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to [Xilinx Answer Record 18683](#).

Revision History

The following table shows the revision history for this document:

Date	Version	Description of Revisions
08/27/12	1.0	Initial release.

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