

Instruction Manual for Evaluation Board of MTO-EV002(Toshiba TC78B002FTG)

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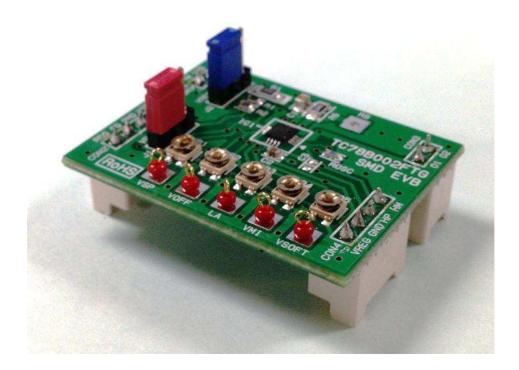


Outline

TC78B002FTG is a full-wave driver for single phase fan motor. It has a DMOS device in an output transistor.

A highly effective drive is possible by adopting a DMOS output driver with low ON resistance and a PWM drive system.

This evaluation board mounts necessary components to evaluate the IC. Please sense various motor drive mode.



Note

In using, please be careful about thermal condition sufficiently.

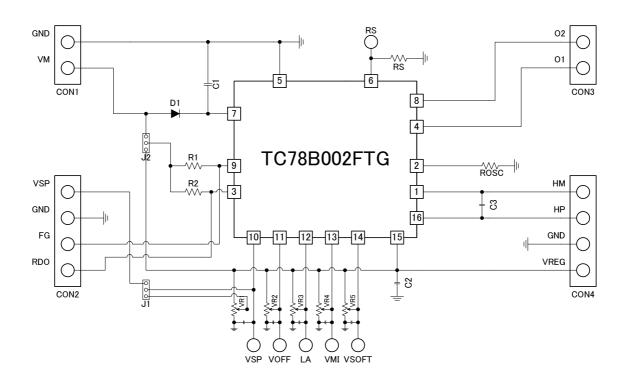
As for each control signal, please refer to the IC specification by accessing to the below URL.

 $\underline{https://toshiba.semicon\text{-}storage.com/ap\text{-}en/product/linear/detail.TC78B002FTG.html}$

Further, the application of this evaluation board is limited to the purpose of evaluating and learning the motor control. Please do not ship them to a market.



Circuit Diagram



Parts List

i aits List							
Parts number		Value(Open=no mounted)	Supplier	Product name	Package (EIA)	Remarks	
C 1		25V 10µF	MURATA	GRM219B31E106KA12#	0805		
С	2	50V 0.1μF	MURATA	GRM188B31H104KA92#	0603		
С	3	Open			0603	0.01μF~0.1μF	
R	1	51kΩ			0603		
R	2	51kΩ			0603		
R	S	0.47Ω			1206		
R	osc	24kΩ			0603		
VR	1	100kΩ	MURATA	PVZ3A104C01R00			
VR	2	100kΩ	MURATA	PVZ3A104C01R00			
VR	3	100kΩ	MURATA	PVZ3A104C01R00			
VR	4	100kΩ	MURATA	PVZ3A104C01R00			
VR	5	100kΩ	MURATA	PVZ3A104C01R00			
D	1	Short			SOD-123	Anti-reverse diode is not mounted	
CON	1	Connector	JST	B02B-XASK-1(LF)(SN)			
CON	2	Connector	JST	B04B-XASK-1(LF)(SN)			
CON	3	Connector	JST	B02B-XASK-1(LF)(SN)			
CON	4	Connector	JST	B04B-XASK-1(LF)(SN)			



Operating Ranges

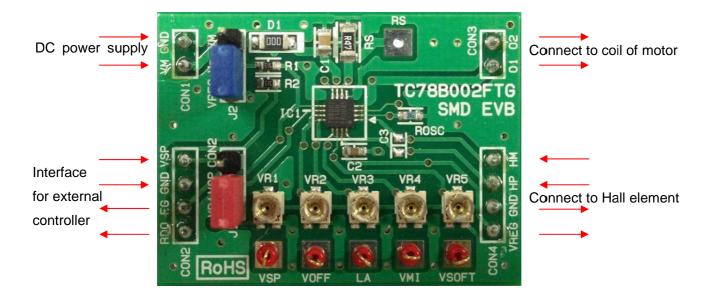
Characteristics	Symbol	Min	Тур.	Max	Unit
Power supply voltage	V _{Mopr1}	5.5	12	1 6	V
Power supply for low voltage operation	VM _{opr2}	3.5	4.5	5.5	V
VSP input voltage	V _{IN}	0		5 .5	V

Electrical Characteristics

Characteristics		Symbol	Test conditions	Min	Тур.	Max	Unit	
Hall signal input	Common mode input voltage range	V _{CMRH}			_	V _{REG} -1.5	V	
	Input voltage swing	VH		40	_	_	mV	
	Input current	lH	V _{HP} -V _{HM} ≧100mV	_		1	μA	
	Hysteresis+Voltage	V _{HHYS+}	(Design target value)	5	10	15	mV	
	Hysteresis - Voltage	V _{HHYS} -	(Design target value)	-15	-10	-5	mV	
VREG pin voltage		V_{REG}	VREG pin output source current=10mA	4.5	5.0	5.5	٧	
VSP threshold		V _{AD (L)}	Threshold voltage of stopping output	0.5	0.55	_	V	
		V _{AD (H)}	Threshold voltage of full output	_	3.9	4.3		
PWM frequency		f _{PWM}	R _{OSC} =24kΩ	20	25	30	kHz	
Output ON resistance		Ron(H+L)	I _{OUT} = 0.2A	_	1.6	2.5	Ω	
FG RDO	Output low voltage	V _{OUT} (L)	I _{FG/RDO} =5mA	_	_	0.3	V	
pin	Output leakage current	I _{OUT} (H)	V _{FG/RDO} =5V	_	_	1	μA	
Current limit detecting voltage for RS pin		V _{RS}		0.27	0.3	0.33	V	
Operating current of over current protection		I _{LIM}	(Design target value)	_	2.5	_	Α	
Operating temperature of thermal shutdown circuit		T _{SD}	Junction temperature (Design target value)	_	170	_	°C	
Hysteresis of thermal shutdown circuit		ΔT _{SD}	(Design target value) (Note 1)	_	40	_	°C	
ON time of lock detection		T _{ON}	R _{OSC} =24kΩ (Design target value)	0.32	0.4	0.48	S	
OFF time of lock detection		T _{OFF}	R _{OSC} =24kΩ (Design target value)	3.2	4	4.8	s	
Detecting voltage for low voltage		VUVLO	Operation voltage (Design target value)	2.6	2.9	3.2	V	
		VPORRL	Recovery voltage (Design target value)	2.9	3.2	3.5	V	



Explanation of evaluation board



CON1: Input connector for external power supply

In usual, please apply 12V DC voltage.

The operating voltage range of this IC is from 3.5V to 14.0V.

Please use this evaluation board within this voltage range.

CON2: Interface of external controller (Input signal / Output signal)

The rotation speed command voltage can be inputted from VSP pin of CON2.

In such case, please short the VSP pin and CON2 pin together on J1 jumper.

The motor speed signal is output from the FG pin.

The RDO pin is the lock detection signal output.

The pull-up voltage of the FG and RDO pins can be set with J2.

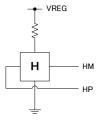
When shorts Vp pin and VREG pin together on J2 jumper, the pull-up voltage is 5V. When shorts Vp pin and VM pin together on J2 jumper pins, the pull-up voltage is VM voltage. Leave J2 jumper open, it is possible to pull up from external.

CON3: Connector for motor drive outputs

Please connect it to a coil of motor.

CON4: Connector for hall signals

Please connect it to a hall element. The power of hall element is able to supply from VREG pin. Please use current limit resistor for limit of flowing current on a hall element. Also, if necessary, please attach noise suppression capacitor to C3. 0.01 μF to 0.1 μF is recommended capacitance value.





VRx: Potentiometer

The 1 pin of potentiometer is connected to the IC's VREG pin. 3 pin is connected to GND. The default shipping position of the rotor is at the middle point. When rotating in the counterclockwise direction, the voltage of pin 2 is increased, and when it rotates in the clockwise direction, the voltage of pin 2 is decreased. Also, please be careful to use within the effective rotation angle when adjusting the potentiometer.

About each control signals, please refer to the datasheet of IC. https://toshiba.semicon-storage.com/ap-en/product/linear/detail.TC78B002FTG.html

The voltage of rotation speed command is able to generate using a variable resistor VR1. When using the divided voltage from VR1 as the command voltage, please short the VSP pin and VR1 pin together on J1 jumper.

RS: Current limit resistor

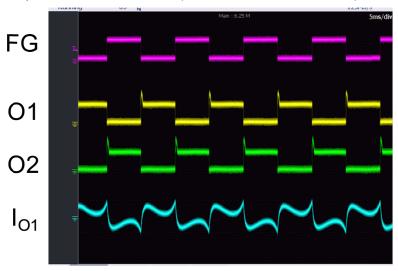
The relation of output current limit value and RS resistance becomes as below. $I_{Limit} = 0.3 V \ / \ RS$

A 0,.47 Ω RS resistor is mounted at shipment. So, the current limit is set to about 0.6 A. Since a large current is flowing through the current limiting resistor, please select resistor with enough power rating when replacing with other resistance value,

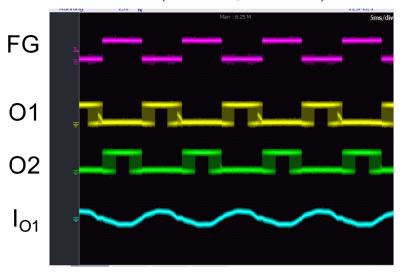


Example of operating waveform

1. Square wave current drive (VSOFT=GND, VOFF=GND)



2. Sine wave current drive (VSOFT=5V, VOFF=GND)



3. OFF period (VSOFT=GND, VOFF=1.5V)

