

**4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY**
**Features**

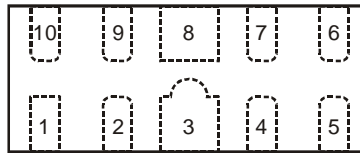
- Clamping Voltage: 9V at 10A 100ns TLP; 9V at 6A 8 $\mu$ s/20 $\mu$ s
- IEC 61000-4-2 (ESD): Air – +20/-18kV, Contact – +20/-16kV
- IEC 61000-4-5 (Lightning):  $\pm$ 6A (8/20 $\mu$ s)
- 4 Channels of ESD Protection
- Low Channel Input Capacitance of 0.5pF Typical
- TLP Dynamic Resistance: 0.25 $\Omega$
- Typically Used for High Speed Ports Such as USB 2.0, DVI™, HDMI™, Ethernet Port, IEEE, MDDI, PCI Express®, SATA/eSATA
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.**  
<https://www.diodes.com/quality/product-definitions/>
- **An automotive-compliant part is available under separate datasheet (DT1140-04LPQ)**

**Mechanical Data**

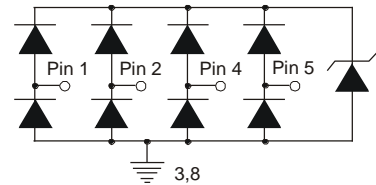
- Package: U-DFN2510-10
- Package Material: Molded Plastic, “Green” Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals:  
Sites 1 and 2: NiPdAu over Copper Leadframe (Lead-Free Plating) Solderable per MIL-STD-202, Method 208 (E4)  
Site 3: Matte Tin over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.038 grams (Approximate)

Sites 1 and 2: U-DFN2510-10  
Site 3: U-DFN2510-10 (Type CJ)

Pin #	Description
1, 2, 4, 5	I/O
6, 7, 9, 10	No Connection
3, 8	Vss



Pin Description (Top View)



Device Schematic

**Ordering Information** (Note 4)

Part Number	Package	Marking	Reel Size (inches)	Tape Width (mm)	Packing	
					Qty.	Carrier
DT1140-04LP-7	U-DFN2510-10	BC2	7	8	3,000	Tape & Reel
DT1140-04LP-7	U-DFN2510-10 (Type CJ)	BC2	7	8	3,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information

Sites 1 and 3



BC2 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: K = 2023)  
 M = Month (ex: 9 = September)

Site 2



BC2 = Product Type Marking Code  
 YWX = Date Code Marking  
 Y = Year (ex: 3 = 2023)  
 W = Week (ex: a=Week 27; z Represents Week 52 and 53)  
 X = Internal Code (ex: U = Monday)

Date Code Key for YM

Year	2013	...	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032
Code	A	...	K	L	M	N	O	P	R	S	T	U

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Date Code Key for YWX

Year	2013	...	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032
Code	3	...	3	4	5	6	7	8	9	0	1	2

Week	1-26	27-52	53
Code	A-Z	a-z	z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	T	U	V	W	X	Y	Z

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current, per IEC 61000-4-5	I <sub>PP</sub>	6	A	I/O to V <sub>SS</sub> , 8/20μs
Peak Pulse Power, per IEC 61000-4-5	P <sub>PP</sub>	60	W	I/O to V <sub>SS</sub> , 8/20μs
Operating Voltage (DC)	V <sub>DC</sub>	6	V	I/O to V <sub>SS</sub>
ESD Protection – Contact Discharge, per IEC 61000-4-2	V <sub>ESD_CONTACT</sub>	+20/-16	kV	I/O to V <sub>SS</sub>
ESD Protection – Air Discharge, per IEC 61000-4-2	V <sub>ESD_AIR</sub>	+20/-18	kV	I/O to V <sub>SS</sub>
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	—

## Thermal Characteristics

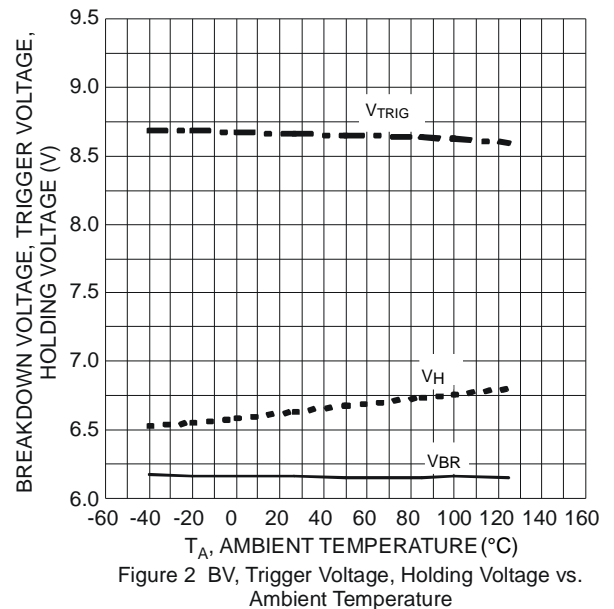
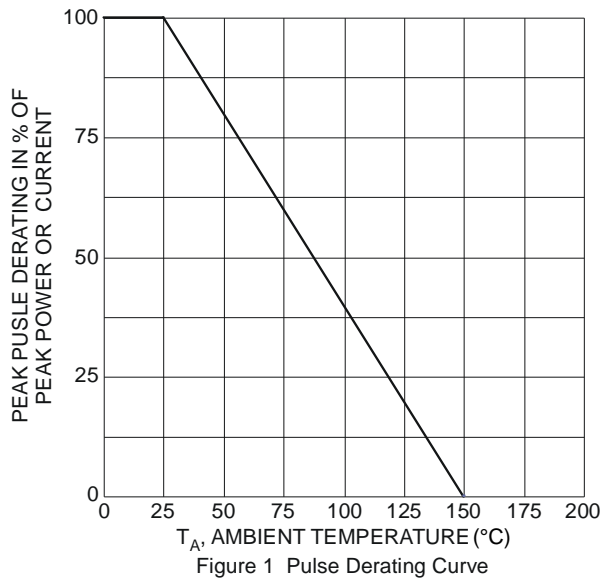
Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	P <sub>D</sub>	350	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	R <sub>θJA</sub>	360	°C/W

Note: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Working Voltage	$V_{RWM}$	—	—	5.5	V	—
Reverse Current (Note 6)	$I_R$	—	—	50	nA	$V_R = 5\text{V}$ , I/O to $V_{SS}$
Reverse Breakdown Voltage	$V_{BR}$	6	—	—	V	$I_R = 1\text{mA}$ , I/O to $V_{SS}$
Forward Clamping Voltage	$V_F$	-1.0	-0.85	—	V	$I_F = -15\text{mA}$ , I/O to $V_{SS}$
Holding Voltage	$V_H$	5.5	—	—	V	—
Reverse Clamping Voltage (Note 7)	$V_C$	—	6.4	—	V	$I_{PP} = 1\text{A}$ , I/O to $V_{SS}$ , 8/20 $\mu\text{s}$
Reverse Clamping Voltage (Note 7)	$V_C$	—	9	10	V	$I_{PP} = 6\text{A}$ , I/O to $V_{SS}$ , 8/20 $\mu\text{s}$
Trigger Voltage	$V_{TRIG}$	—	—	9.5	V	—
ESD Clamping Voltage	$V_{ESD}$	—	9	—	V	TLP, 10A, $t_P = 100\text{ns}$ , I/O to $V_{SS}$
Dynamic Reverse Resistance	$R_{DIF-R}$	—	0.25	—	$\Omega$	TLP, 10A, $t_P = 100\text{ns}$ , I/O to $V_{SS}$
Dynamic Forward Resistance	$R_{DIF-F}$	—	0.25	—	$\Omega$	TLP, 10A, $t_P = 100\text{ns}$ , $V_{SS}$ to I/O
Channel Input Capacitance	$C_{I/O}$	—	0.5	0.65	pF	$V_{I/O} = 2.5\text{V}$ , $V_{SS} = 0\text{V}$ , $f = 1\text{MHz}$

- Notes:
- 6. Short duration pulse test used to minimize self-heating effect.
  - 7. Clamping voltage value is based on an 8x20 $\mu\text{s}$  peak pulse current ( $I_{pp}$ ) waveform.



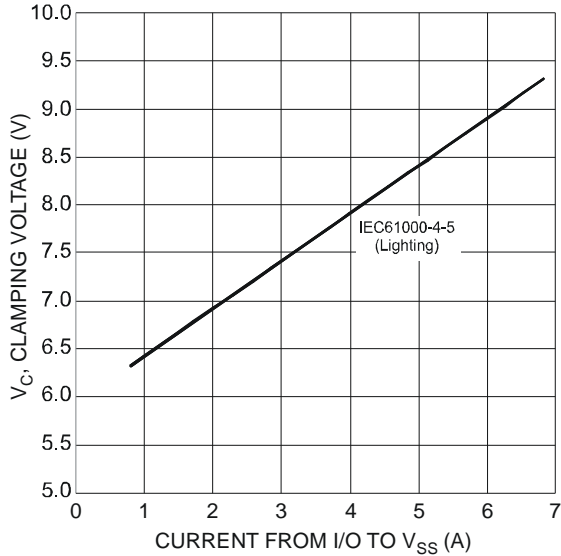


Figure 3 Clamping Voltage Characteristic

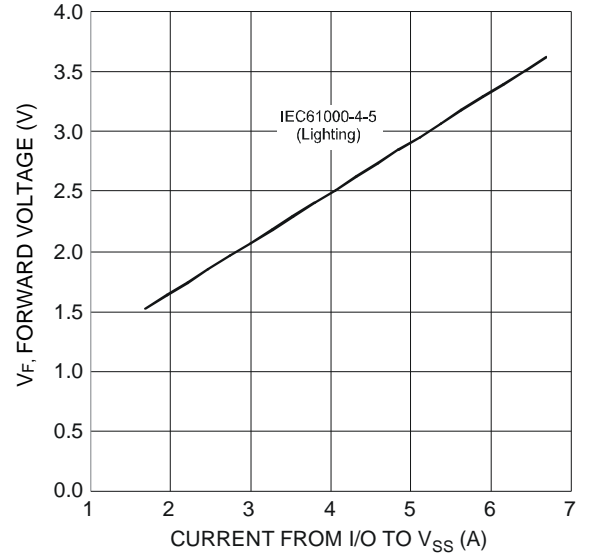


Figure 4 Forward Voltage Characteristic

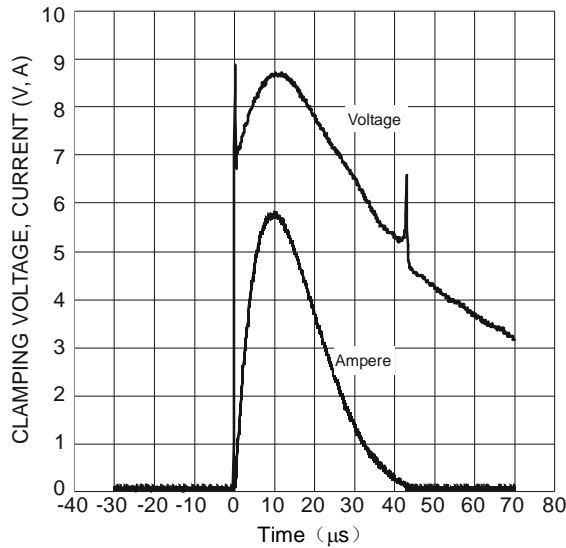


Figure 5 Waveform of Clamping Voltage, Current vs. Time (8/20μs, I/O to V<sub>SS</sub>)

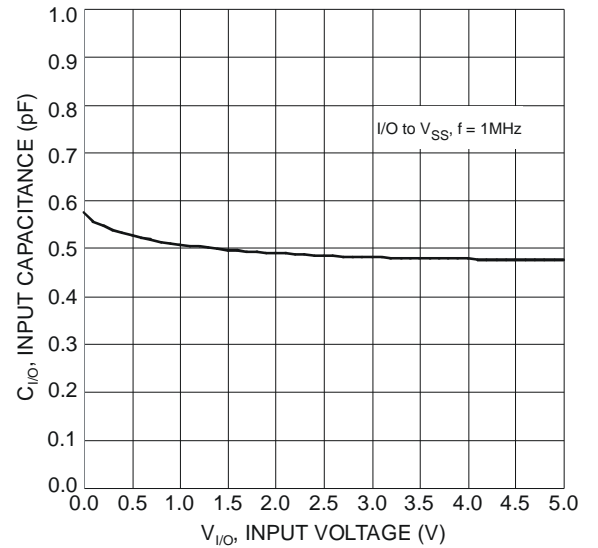


Figure 6 Input Capacitance vs. Input Voltage

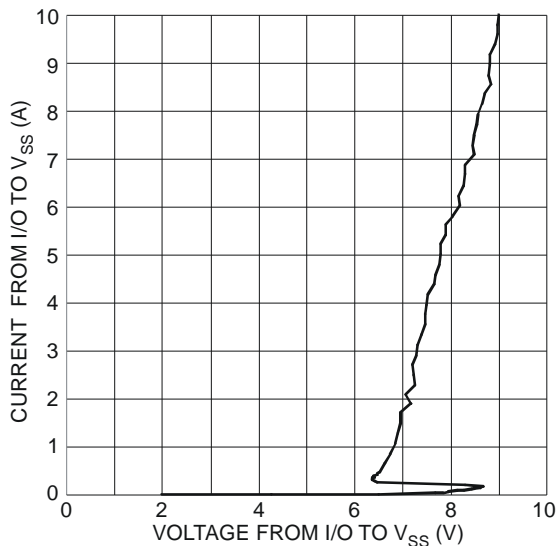
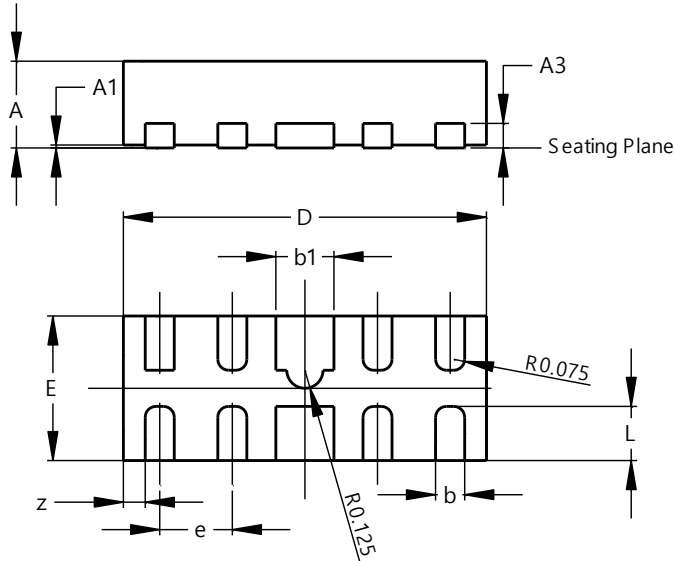


Figure 7 Current vs. Voltage

## Package Outline Dimensions

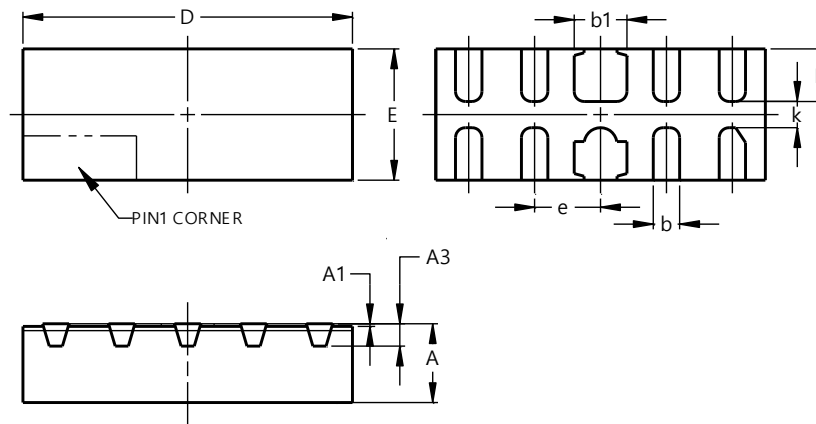
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### Sites 1 and 2: U-DFN2510-10



U-DFN2510-10			
Dim	Min	Max	Typ
A	0.545	0.605	0.575
A1	0.00	0.05	0.03
A3	-	-	0.13
b	0.15	0.25	0.20
b1	0.35	0.45	0.40
D	2.450	2.575	2.500
e	-	-	0.50
E	0.950	1.075	1.000
L	0.325	0.425	0.375
z	-	-	0.150
All Dimensions in mm			

### Site 3: U-DFN2510-10 (Type CJ)

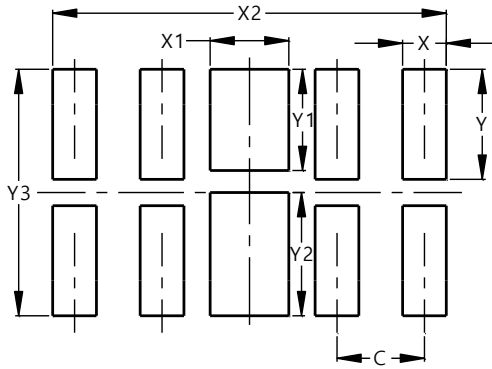


U-DFN2510-10 (Type CJ)			
Dim	Min	Max	Typ
A	0.545	0.605	--
A1	0.00	0.05	--
A3	0.152REF		
b	0.150	0.250	--
b1	0.350	0.450	--
D	2.450	2.575	--
E	0.950	1.075	--
e	--	--	0.500
E	0.950	1.075	1.000
L	0.350	0.450	--
k	0.200REF		
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

All Sites: U-DFN2510-10 and U-DFN2510-10 (Type CJ)



Dimensions	Value (in mm)
<b>C</b>	0.500
<b>X</b>	0.250
<b>X1</b>	0.450
<b>X2</b>	2.250
<b>Y</b>	0.625
<b>Y1</b>	0.575
<b>Y2</b>	0.700
<b>Y3</b>	1.400

**IMPORTANT NOTICE**

1. DIODES INCORPORATED (Diodes) AND ITS SUBSIDIARIES MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes' products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes' products. Diodes' products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of Diodes' products for their intended applications, (c) ensuring their applications, which incorporate Diodes' products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
5. Diodes' products are provided subject to Diodes' Standard Terms and Conditions of Sale (<https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/>) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
6. Diodes' products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes' products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.
9. This Notice may be periodically updated with the most recent version available at <https://www.diodes.com/about/company/terms-and-conditions/important-notice>

The Diodes logo is a registered trademark of Diodes Incorporated in the United States and other countries.  
All other trademarks are the property of their respective owners.  
© 2023 Diodes Incorporated. All Rights Reserved.

[www.diodes.com](http://www.diodes.com)