



\* Cin1 is an optional capacitor. It is inserted on the DC833 to dampen the (possible) ringing voltage due to the use of long input leads. On a normal, typical PCB, with the short traces, Cin1 is not needed. Also, Rsw and Rsd are optional parts on the DC833. They're inserted in case the jumper shunts accidentally fall out.

\*\* 1.14V is the minimum input voltage that the LTC3026 will operate at. The minimum input voltage for a specific regulator circuit depends on the output voltage (plus the dropout voltage). 3.5V is the maximum input voltage with the internal boost converter enabled. With an external voltage supply on the boost pin, the maximum input voltage is 5.5V. See the table below.

JP6	Vin	Vboost
Ext	1.14V-5.5V	4.5V-5.5V
Boost	1.14V-3.5V	_____

\*\*\* The maximum output voltage is 2.6V.

### CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS	DATE
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TITLE		SIZE	CAGE CODE	DWG NO	REV
LTC3026EDD, Low Input Voltage VLDO Linear Regulator				DC833A	A
SCALE:	FILENAME:	SHEET	1	OF	1