

Product / Process Change Notification



N° 2018-025-A

Dear Customer,

Please find attached our INFINEON Technologies PCN:

Silicon improvement, package process harmonization and data sheet update affecting product TLF50281EL

Important information for your attention:

- Please respond to this PCN by indicating your decision on the approval form, sign it and return to your sales partner before **29. June 2018**.
- Infineon aligns with the widely-recognized JEDEC STANDARD "JESD46", which stipulates: **"Lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change."**
- This PCN includes a **Product Discontinuation Notice** (JEDEC STANDARD "JESD48") on page 4/4.

Your prompt reply will help Infineon Technologies to assure a smooth and well executed transition. If Infineon does not hear from your side by the due date, we will assume your full acceptance to this proposed change and its implementation.

Your attention and response to this matter is greatly appreciated.

Disclaimer:

If we do not receive any response by the date in the PCN above we consider this as the acceptance of the PCN. After the last order date as stated herein, purchase orders related to the unchanged product(s) cannot be accepted.

In case the customer rejects this PCN this PCN shall be considered a product discontinuation notice (PD).

Infineon Technologies AG
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Chairman of the Supervisory Board: Dr. Eckart Sünner
Management Board: Dr. Reinhard Ploss (CEO), Dominik Asam, Dr. Helmut Gassel, Jochen Hanebeck
Registered Office: Neubiberg Commercial Register
Amtsgericht München HRB 126492

Product / Process Change Notification



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► **Products affected:** Please refer to attached affected product list 1_cip18025_a

► **Detailed Change Information:**

Subject: DeQuMa ID: SEM-DS-01; SEM-DE-02; SEM-PW-03.
Silicon improvement, package process harmonization, and data sheet update affecting product TLF50281EL

Reason: Device robustness was improved at very fast load step behavior (e.g. load step 100µA -> 300mA or higher, in less than 1µs).
Wafer thickness of TLF50281EL was changed from 380µm to 220µm to harmonize it with latest product manufacturing within the product family.
SYNC parameter (min high/low time) within the datasheet was updated to reflect the most relevant application conditions.

Description:	Old	New
Silicon Redesign	■ Triggered by a load step (i.e. increase of output current) the product may stay in PFM mode with consequence of decreasing output voltage.	■ The redesign now ensures that after a load step the device will switch into the PWM mode as expected.
Wafer thickness	■ 380µm	■ 220µm
SYNC min high/low time	■ 25µs	■ 50µs
Data Sheet	■ Rev. 1.01	■ Rev. 1.1

► **Product Identification:** Traceability is ensured by lot number.
SP number and ordering part number are different

► **Impact of Change:** No changes in quality, reliability, and manufacturability of the products.
Change in function is considered as uncritical and does not affect data sheet parameters.
Datasheet update and wafer thickness change not considered to be application relevant.

► **Attachments:** 1_cip18025_A: affected product list
4_cip18025_A: DS update

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► Time Schedule:

■ Final qualification report:	Available on request
■ First samples available:	Available on request
■ Intended start of delivery:	August 2018 or earlier after customer release

Refer to page 4/4:

■ Last order date of unchanged product:	01-January-2019
■ Last delivery date of unchanged product:	01-July-2019

If you have any questions, please do not hesitate to contact your local Sales office.

PRODUCT DISCONTINUATION



Referring to PCN N° 2018-025-A

■ Last order date of unchanged product:	2019-01-01
■ Last delivery date of unchanged product:	2019-07-01

DISCONTINUED				NEW (REPLACEMENT)			
Sales name	SP number	OPN	Package	Sales name	SP number	OPN	Package
TLF50281EL	SP000819928	TLF50281ELXUMA1	PG-SSOP-14	TLF50281EL	SP001721758	TLF50281ELXUMA3	PG-SSOP-14