

Features

- Split Gate Trench Mosfet Technology
- Excellent Package for Heat Dissipation
- High Density Cell Design for Low $R_{DS(on)}$
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 3
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

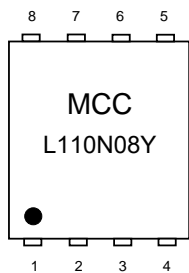
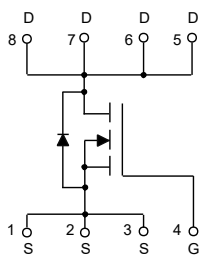
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 50°C/W Junction to Ambient
- Thermal Resistance: 1°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	80	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	$T_C=25^\circ C$	110
		$T_C=100^\circ C$	69
Pulsed Drain Current (Note 2)	I_{DM}	400	A
Avalanche Energy (Note 3)	E_{AS}	937	mJ
Total Power Dissipation	P_D	125	W

Note:

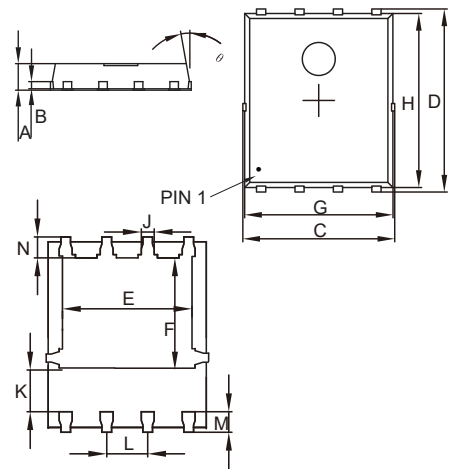
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
3. $T_J=25^\circ C$, $V_{DD}=50V$, $V_G=10V$, $L=3mH$, $R_G=25\Omega$, $I_{AS}=25A$

Internal Structure and Marking Code



N-CHANNEL MOSFET

DFN5060



DIM	DIMENSIONS				NOTE
	INCHES		MM		
	MIN	MAX	MIN	MAX	
A	0.031	0.047	0.80	1.20	
B	0.010		0.254		TYP.
C	0.193	0.222	4.90	5.64	
D	0.232	0.250	5.90	6.35	
E	0.148	0.167	3.75	4.25	
F	0.126	0.154	3.20	3.92	
G	0.189	0.213	4.80	5.40	
H	0.222	0.239	5.65	6.06	
K	0.045	0.059	1.15	1.50	
J	0.012	0.020	0.30	0.50	
L	0.046	0.054	1.17	1.37	
M	0.012	0.028	0.30	0.71	
N	0.016	0.028	0.40	0.71	

Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	80			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=80V, V_{GS}=0V$			1	μA
		$V_{DS}=80V, V_{GS}=0V, T_j=150^\circ C$			100	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	3	4	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=55A$		2.9	3.9	m Ω
		$V_{GS}=6V, I_D=20A$		4.5	6	
Gate resistance	R_G	f=1MHz, Open drain		2		Ω
Diode Characteristics						
Continuous Body Diode Current	I_S				110	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=55A$		0.9	1.2	V
Reverse Recovery Time	t_{rr}	$I_S=55A, di/dt=420A/\mu s$		36		ns
Reverse Recovery Charge	Q_{rr}			73		nC
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=40V, V_{GS}=0V, f=1MHz$		5290		μF
Output Capacitance	C_{oss}			870		
Reverse Transfer Capacitance	C_{rss}			445		
Total Gate Charge	Q_g	$V_{DS}=40V, V_{GS}=10V, I_D=55A$		68		nC
Gate-Source Charge	Q_{gs}			20		
Gate-Drain Charge	Q_{gd}			13		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=10V, V_{DD}=40V, I_D=55A, R_{GEN}=2.2\Omega$		22		ns
Turn-On Rise Time	t_r			160		
Turn-Off Delay Time	$t_{d(off)}$			34		
Turn-Off Fall Time	t_f			10		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

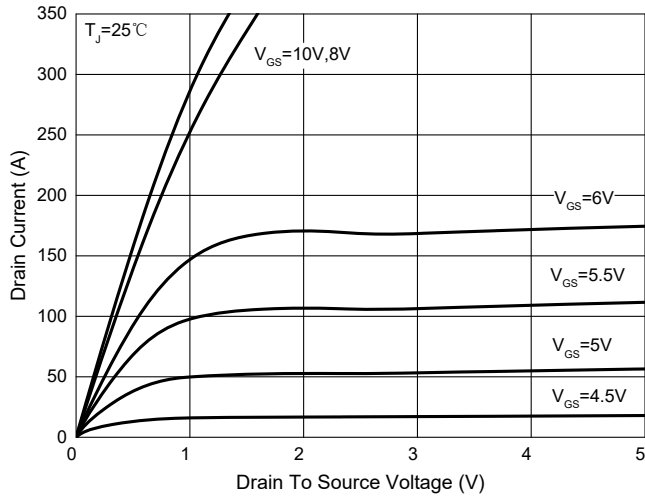


Fig. 2 - Transfer Characteristics

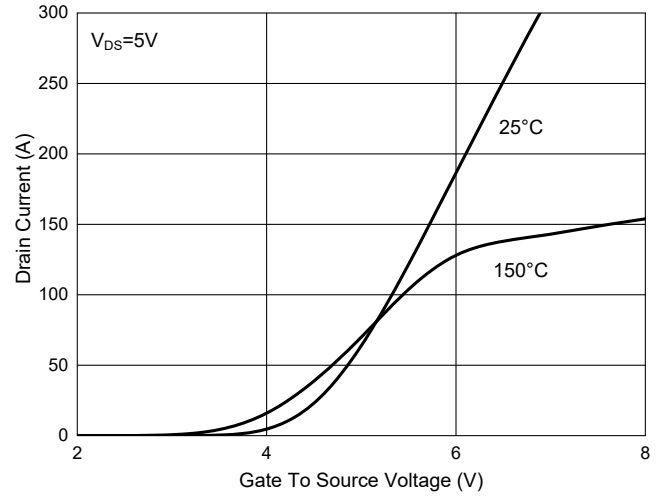


Fig. 3 - $R_{DS(ON)} - V_{GS}$

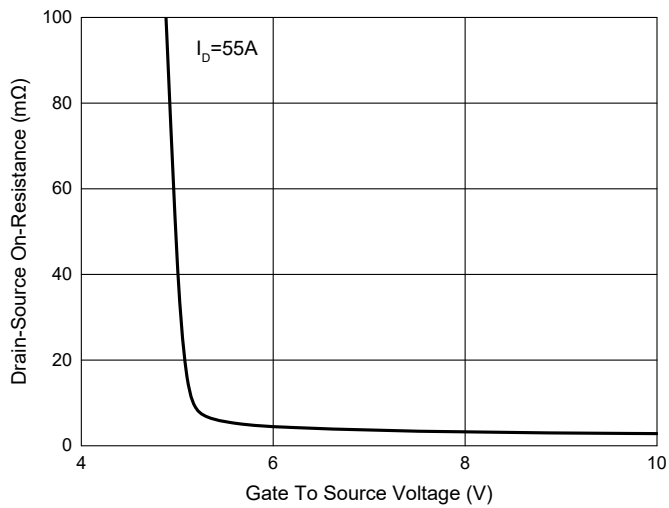


Fig. 4 - $R_{DS(ON)} - I_D$

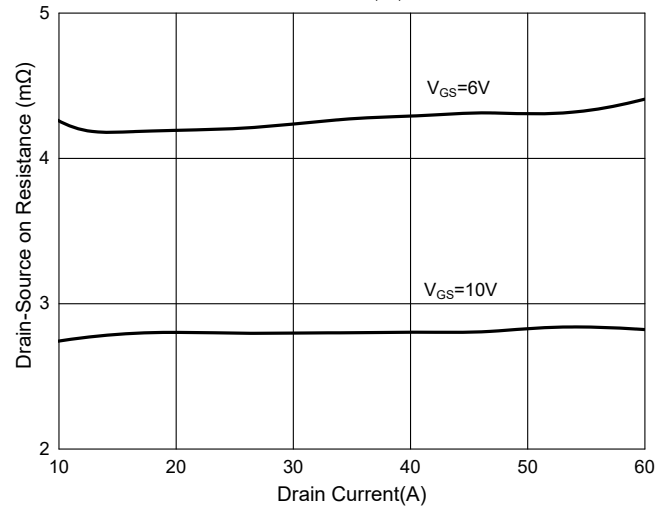


Fig. 5 - Gate Charge

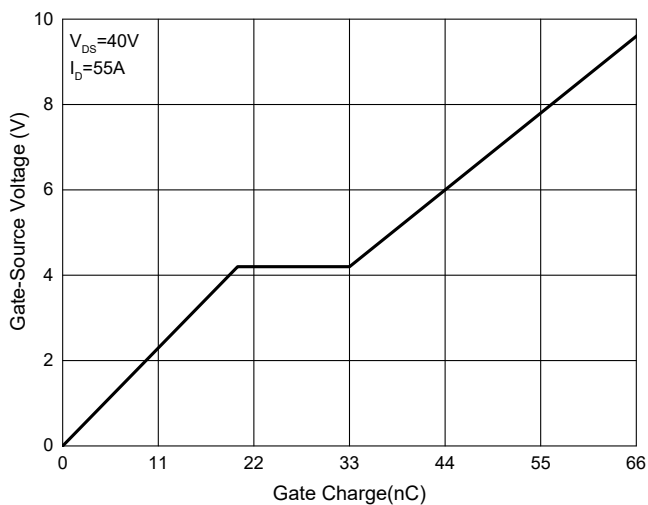
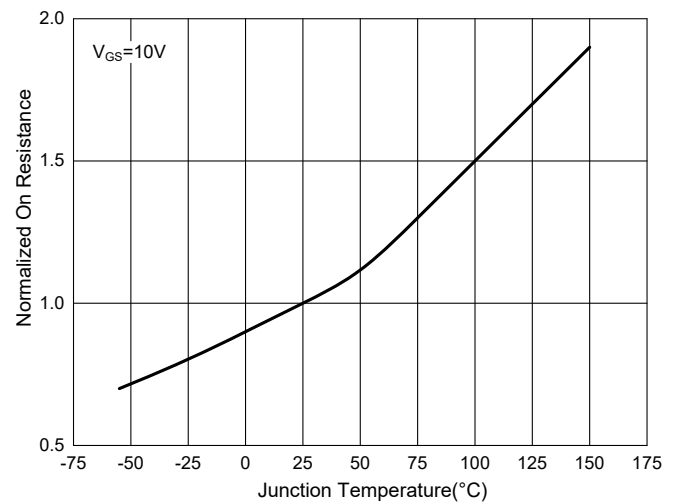


Fig. 6 - Normalized On Resistance Characteristics



Curve Characteristics

Fig. 7 - Capacitance Characteristics

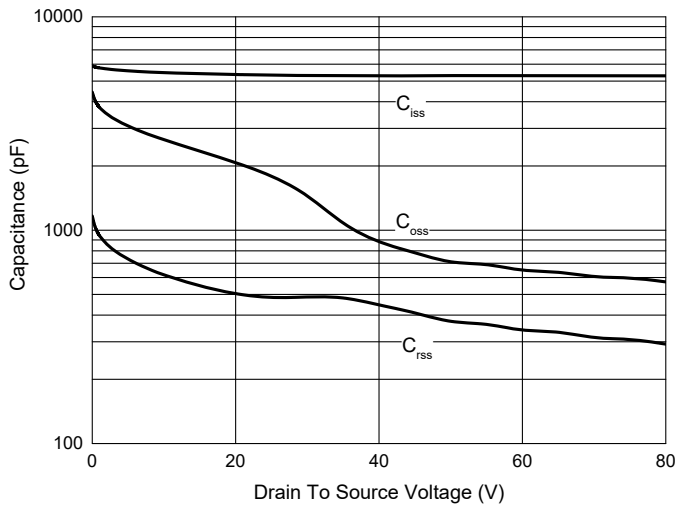


Fig. 8 - $I_s - V_{SD}$

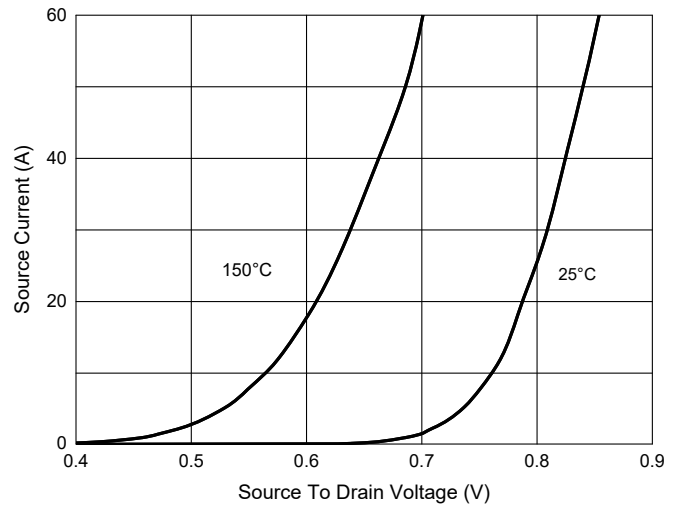


Fig. 9 - Power dissipation

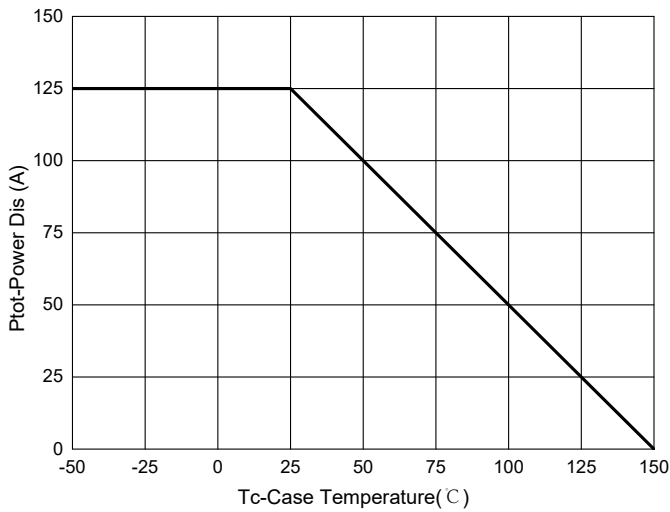


Fig. 10 - Safe Operation Area

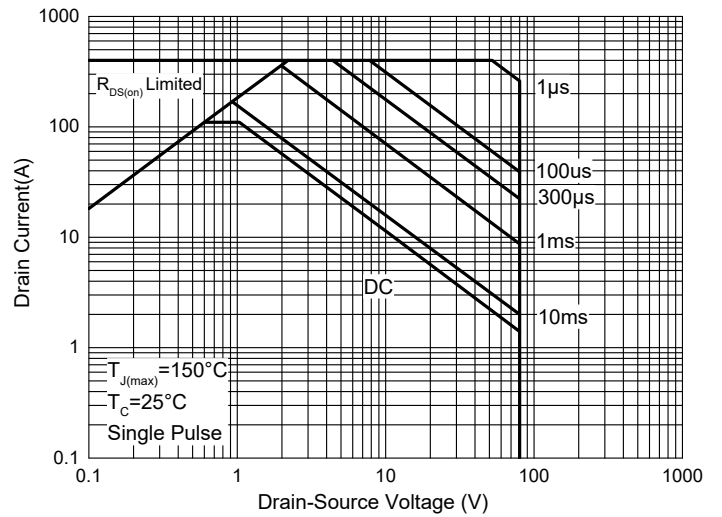
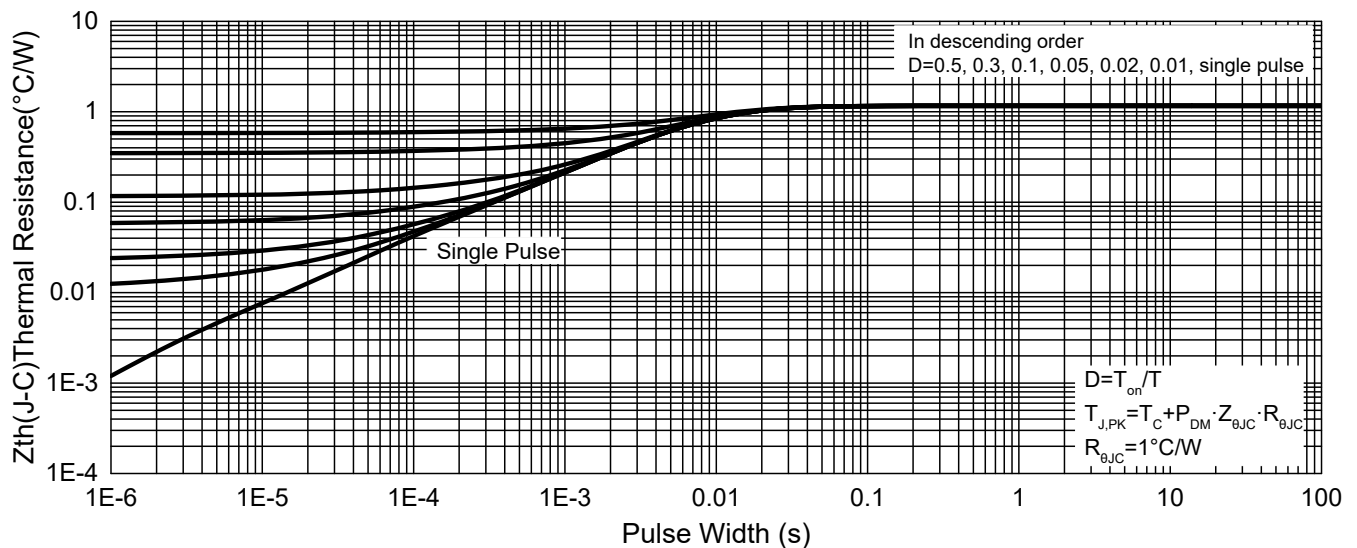


Fig. 11 - Maximum Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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