



Cypress Semiconductor Corporation, 198, Champion Court, San Jose, CA 95134. Tel: (408) 943-2600

PRODUCT INFORMATION NOTIFICATION

PIN: PIN145280

Date: March 17, 2014

Subject: Qualification of Manufacturing and Design Changes for PSoC 3 Products

To: PCN ADMINISTRATOR
CYPRESS PCN ADMIN
pcn_adm@cypress.com

Change Type: Minor

Description of Change:

Cypress announces a new revision of masks for devices in the PSoC 3 family. These new devices improve HBM Electro-Static Discharge performance from 750 V to 2,000 V and improve Single-Event Latchup to <10 FIT.

These new devices are drop-in replacements which are form, fit, and function compatible with existing PSoC 3 devices. Product data sheets have been updated to reflect the changed specifications.

Additionally, Cypress is implementing a manufacturing change that will result in a temperature restriction on the IMO 3MHz spec to +/- 1.5% below 0 deg C and above 70 deg C. This spec remains at +/- 1% at all other operational temperatures.

Benefits of the Change:

The PSoC 3 manufacturing and design changes provide improved specifications and performance for all PSoC 3 products.

Affected Part Numbers: 28

Affected Parts: Please refer to the attached 'Affected part list' file.

Customer Part Numbers Affected: Not Applicable

Affected Parts: Not Applicable

Qualification Status:

These new parts have been qualified through a series of tests identified in Qualification Test Plan (QTP) No. 101101 section 133003. The QTP report can be found in the attachment to this notification or by visiting www.cypress.com and typing the QTP number 101101 in the Keyword Search window.

Approximate Implementation Date:

Effective with the date of this notification, Cypress will start to transition to shipments of the affected part numbers with the manufacturing and design changes.

Anticipated Impact:

Products manufactured are completely compatible with existing products from a functional, parametric, and quality performance perspective.

Cypress also recommends that customers take this opportunity to review these changes against current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

Method of Identification:

Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

Response Required:

This is an information only announcement. No response is required.

For additional information regarding this change, contact your local sales representative or contact the Cypress PCN Administrator at pcn_adm@cypress.com.

Sincerely,

Cypress PCN Administration

Item	Marketing Part Number
1	CY8C3244PVI-133
2	CY8C3244PVI-133T
3	CY8C3245PVI-134
4	CY8C3245PVI-134T
5	CY8C3245PVI-150
6	CY8C3245PVI-150T
7	CY8C3246PVI-122
8	CY8C3246PVI-122T
9	CY8C3246PVI-147
10	CY8C3246PVI-147T
11	CY8C3444PVI-100
12	CY8C3444PVI-100T
13	CY8C3445PVI-090
14	CY8C3445PVI-090T
15	CY8C3445PVI-094
16	CY8C3445PVI-094T
17	CY8C3446PVI-076
18	CY8C3446PVI-076T
19	CY8C3446PVI-102
20	CY8C3446PVI-102T
21	CY8C3665PVI-008
22	CY8C3665PVI-008T
23	CY8C3866PVI-021
24	CY8C3866PVI-021T
25	CY8C3866PVI-070
26	CY8C3866PVI-070T
27	CS7642AT
28	CS7642ATT