

## C1206T104K1RBLTU

Aliases (C1206T104K1RBL7800)

SMD COTS X7R, Ceramic, 0.1 uF, 10%, 100 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 1206



Click here for the 3D model.

Dimensions	
Chip Size	1206
L	3.2mm +/-0.2mm
W	1.6mm +/-0.2mm
Т	0.78mm +/-0.10mm
В	0.5mm +/-0.25mm

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	4000

SMD COTS X7R SMD Chip
SMD Chip
SMD, MLCC, COTS, Temperature Stable, Class II
Temperature Stable, Class II
No
▲ WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov.
2d771165-5336-48a3-96fa-3663929fd828
Lead (SnPb)
No
Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469
No
17 mg
78 Weeks
1

Specifications	
Capacitance	0.1 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	10%
Voltage DC	100 VDC
Dielectric Withstanding Voltage	250 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	10 GOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.