Product Change Notification / SYST-30ACAP600

Date:

02-Aug-2021

Product Category:

16-Bit - Microcontrollers and Digital Signal Controllers

PCN Type:

Document Change

Notification Subject:

Data Sheet - dsPIC33CK64MC105 Family Data Sheet Revision

Affected CPNs:

SYST-30ACAP600_Affected_CPN_08022021.pdf
SYST-30ACAP600_Affected_CPN_08022021.csv

Notification Text:

SYST-30ACAP600

Microchip has released a new Product Documents for the dsPIC33CK64MC105 Family Data Sheet of devices. If you are using one of these devices please read the document located at dsPIC33CK64MC105 Family Data Sheet.

Notification Status: Final

Description of Change: This revision incorporates the following updates:
1. Change in title from "High-Speed PWM" to "High-Resolution PWM"; changed throughout document
2. Sections:
    2.1 "Microcontroller Features", 28-Pin UQFN and 36-Pin UQFN in "Pin Diagrams", Section 4.3 BIST Overview", Section 4.3.1 "BIST at Start-up", Section 5.4 "Error Correcting Code (ECC)", Section 8.5.4 "Input Mapping", Section 9.2 "CPU Clocking", Section 12.0 "High-Speed, 12-Bit Analog-to-Digital Converter (ADC)", Section 16.0 "Serial Peripheral Interface (SPI)", Section 20.1 "Time Base Generator", Section 27.2.1 "Sleep Mode", Section 27.2.2 "Idle Mode" and Section 28.6 "Dual Watchdog Timer (WDT)".
    2.2 Adds Section 9.5 "Low-Power RC (LPRC) Oscillator" and Section 12.4 "Differential-Mode"
3. Registers: Register 5-1, Register 7-5, Register 9-1, Register 9-6, Register 21-3, Register 28-4, Register 28-6, Register 28-18 and Register 28-19
4. Tables:
    4.1 Table 4-10, Table 4-11, Table 9-1, Table 12-1, Table 22-3, Table 31-12, Table 31-23, Table 31-24, Table 31-33, Table 31-36, Table 32-7 and Table 32-17
    4.2 Adds Table 32-16

4.3 Removes Table 32-17: ADC Module Accuracy
5. Figures: - Figure 9-1, Figure 9-2, Figure 9-5, Figure 28-1 and Figure 28-2
6. Adds new terminology cross references table (Table 6) that provides updated terminology for depreciated naming conventions.

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 02 August 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

dsPIC33CK64MC105 Family Data Sheet

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

DSPIC33CK64MC105-E/M4
DSPIC33CK64MC105-E/M4VAO
DSPIC33CK64MC105-E/PT
DSPIC33CK64MC105-I/M4
DSPIC33CK64MC105-I/PT
DSPIC33CK64MC105-H/M4
DSPIC33CK64MC105-H/M4VAO
DSPIC33CK64MC105-H/PT
DSPIC33CK64MC105T-I/M4
DSPIC33CK64MC105T-I/PT
DSPIC33CK64MC105T-E/M4
DSPIC33CK64MC105T-E/PT