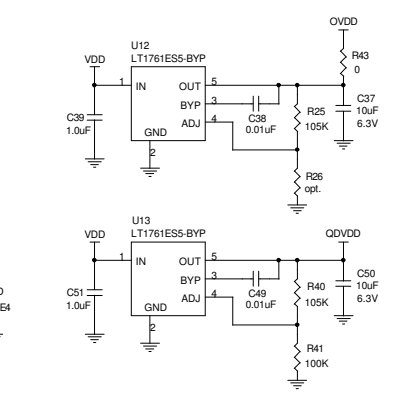
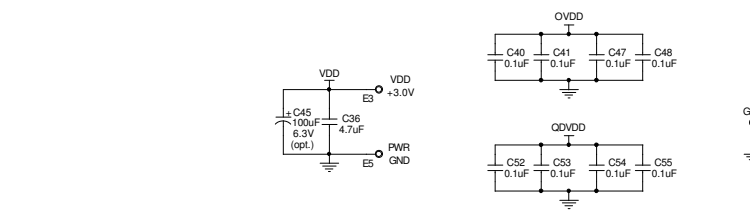


*** VERSION TABLE**

| ASSEMBLY TYPE | U1 | Bits | Mbps | R5, R9, R18, R24 | C6, C31 | T1, T2 | INPUT FREQUENCY |
|---------------|------------|------|------|------------------|---------|--------------------|----------------------|
| DC1098A-A | LTC2281IUP | 10 | 125 | 24.9 ohm | 12pF | MABAES0060 | 1MHz < Ain < 70MHz |
| DC1098A-B | LTC2283IUP | 12 | 125 | 24.9 ohm | 12pF | MABAES0060 | 1MHz < Ain < 70MHz |
| DC1098A-C | LTC2285IUP | 14 | 125 | 24.9 ohm | 12pF | MABAES0060 | 1MHz < Ain < 70MHz |
| DC1098A-D | LTC2281IUP | 10 | 125 | 12.4 ohm | 8.2pF | MABA-007159-000000 | 70MHz < Ain < 140MHz |
| DC1098A-E | LTC2283IUP | 12 | 125 | 12.4 ohm | 8.2pF | MABA-007159-000000 | 70MHz < Ain < 140MHz |
| DC1098A-F | LTC2285IUP | 14 | 125 | 12.4 ohm | 8.2pF | MABA-007159-000000 | 70MHz < Ain < 140MHz |



| APPROVALS | | DATE | TITLE |
|-----------|-------------|---------|---------------------------------------|
| DRAWN | June Wu | 4/24/08 | LTC2285 FAMILY HIGH SPEED DUAL ADC |
| CHECKED | | | |
| APPROVED | | | |
| ENGINEER | Mark Thoren | 4/24/08 | |
| DESIGNER | | | |

CUSTOMER NOTICE
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.
 THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

| | | | | |
|-----------------------|--------|-----------|---------|-----|
| CONTRACT NO. | SCALE: | FILENAME: | DWG NO. | REV |
| Friday, June 30, 2008 | | | | A |

SHEET 1 OF 1