



CUSTOMER ADVISORY

ADV2213

Intel® Agilex™ & Intel® Stratix® 10 Pin-out Document Update

Description:

Intel® is notifying customers of an update to the pin-out documents for selected Intel® Agilex™ and Intel® Stratix® 10 Devices.

Product Family	Change Details
Agilex™	- Added Pin List R31C. - Removed SDMMC_PWR_EN in HPS_IOB_11 pin on Pin List R24C.
Stratix® 10	- Added SDMMC_PWR_EN signal in HPS Pin HPS_IOA_11 and HPS Pin HPS_IOB_23.

Product Family	Link to Pin-out File
Agilex™	AGFB019
	AGFB023
	AGID019
	AGIB019
	AGID023
	AGIB023
Stratix® 10	ISD110
	ISX280
	ISX250
	ISX210

ISX165
ISX110
ISX085
ISX065
ISX040
IST280
IST250
IST210
ISTI65
ISTI10
ISTO85
ISTO40

Recommended Action:

Customers are requested to review the change and determine the impact on their designs. Refer to the revision history of the pin-out documents for complete list and history of updates.

Products Affected:

The list of affected part numbers (OPNs) can be downloaded in Excel form:
<https://cdrdv2.intel.com/v1/dl/getContent/758565>

Change Implementation:

Table 1:

Milestones	Availability
Availability of Intel® Agilex™ and Intel® Stratix® 10 Devices Family Pin-out Guidelines Update	Now

Contact:

For more information, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via:
<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Customer Notifications Subscription:

If you would like to receive customer notifications by email, please follow the instructions in [ADV 2209](#)

Intel references J-STD-046 guidelines for PCN.

In accordance with J-STD-046, this change is deemed acceptable to the customer if no acknowledgement is received within 30 days from date of notification.

Revision History:

Date	Rev	Description
11/17/2022	1.0.0	Initial Release

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