

Customer Part:



Description

- 5 x 3.2 crystal oscillator
Ceramic package with a seam sealed metal lid, hermetically sealed
- Model: CFPS-12
- Model Issue number: 7

Frequency Parameters

- Frequency: 16.0MHz
- Frequency Stability: $\pm 50.00\text{ppm}$
- Operating Temperature Range: -40.00 to 85.00°C
- Ageing: $\pm 3\text{ppm}$ max per year @ 25°C

Electrical Parameters

- Supply Voltage: $5.0\text{V} \pm 0.5\text{V}$
- Current Draw: 45.000mA

Output Details

- Output Compatibility: CMOS
- Drive Capability: 15pF max
- Rise and Fall Time: 10.0ns max
- Duty Cycle: $40/60\%$

Output Control

- Enable/Disable Operation:
Logic '1' ($>70\%$ VS) to pad 1 enables oscillator output
Logic '0' ($<30\%$ VS) to pad 1 disables oscillator output; when disabled the oscillator output goes to the high impedance state
No connection to pad 1 enables oscillator output
Standby Current: $10\mu\text{A}$ max

Environmental Parameters

- Storage Temperature Range: -55 to 125°C
- Shock: MIL-STD-202F, Method 213B: 1000G , 0.5ms , $1/2$ sine wave
- Vibration: MIL-STD-202F, Method 204D, Test Condition D: 20G (10Hz - 2000Hz), 4hrs in 3 mutually perpendicular planes (total 12hrs)

Manufacturing Details

- RoHS Terminations: NiAu
- RoHS Reflow Temp: 260degC 10s

Compliance

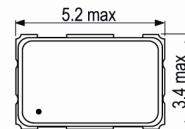
- RoHS Status (2015/863/EU): Compliant
- REACH Status: Compliant
- MSL Rating (JDEC-STD-033): Not Applicable

Packaging Details

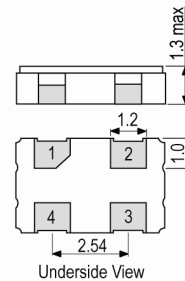
- Pack Style: Reel Tape & reel in accordance with EIA-481-D
Pack Size: 1,000
- Alternative packing option available



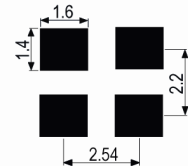
Outline (mm)



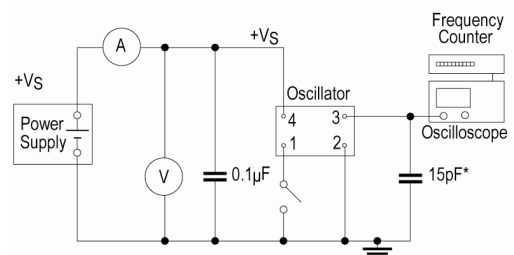
- Pad Connections
1. Enable/Disable
 2. GND
 3. Output
 4. +Vs



Solder Pad Layout



Test Circuit



* Inclusive of jigging and equipment capacitance

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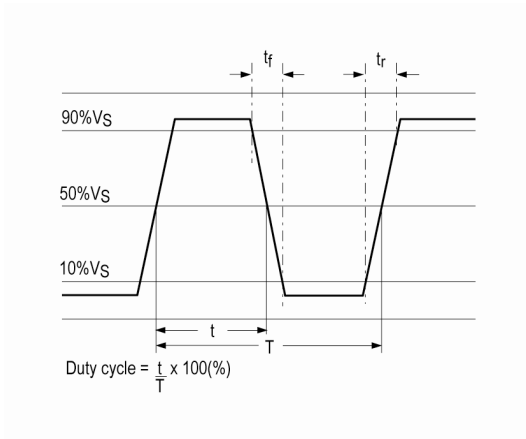
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Customer Part:

Wave Form



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