PCI	Number:	2023021	7000.2 F			PCN Da	ate: February 17, 2023			
Titl		Qualification of new CDAT as an additional Assembly and Test site with a new die								
	revision	for some of	of th	f the devices						
Cus	tomer Contact	:	<u>PC</u>	PCN Manager I		Dept:		Quality Services		
Proposed 1 st Ship Date:				Aug 16, 2023 Sample request accepted until:			Mar 17, 2023*			
*Sa	*Sample requests received after Mar 17, 2023 will not be supported.									
Change Type:										
\boxtimes	Assembly Site			Assembly Process		\square	Assembly Materials			
\boxtimes	Design			Electrical Specification			Mech	anical Specification		
☐ Test Site				Packing/Shipping/Labeling			Test Process			
□ Wafer Bump Site				Wafer Bump Material			Wafer Bump Process			
Wafer Fab Site				Wafer Fab Materials			Wafe	r Fab Process		
			Part number change							
	PCN Details									

Description of Change:

Texas Instruments is pleased to announce the qualification of a Assembly and Test site (CDAT) including a new die revision for some of the devices. Construction differences are noted below:

Construction differences are as follows:

	UTL1	CDAT
Mount Compound	SID#PZ0035	4207123
Bond wire composition, diameter	Cu, 1.3 mil	Cu, 0.96 mil
Lead finish	Matte Sn	NiPdAu
ECAT	G3	G4

A minor metal change was performed to avoid overstress during OVST

Test coverage, insertions, conditions will remain consistent with current testing and verified with test MQ

Upon expiry of this PCN, there will be a transition period where TI will combine lead free solutions in a single <u>standard part number</u> For example; <u>TPS6503320BRGERQ1</u> – can ship with both Matte Sn and NiPdAu.

Example:

- Customer order for 7500 units of TPS6503320BRGERQ1 with 2500 units SPQ (Standard Pack Quantity per Reel).
- TI can satisfy the above order in one of the following ways.
 - I. 3 Reels of NiPdAu finish.
 - II. 3 Reels of Matte Sn finish
 - III. 2 Reels of Matte Sn and 1 reel of NiPdAu finish.
 - IV. 2 Reels of NiPdAu and 1 reel of Matte Sn finish.

Reason for Change:

Supply Continuity

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative): None

Impact on Environmental Ratings

Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.

No Change	🛛 No Change	🛛 No Change	🛛 No Change
anges to produc			
anges to produ			
	ct identification resulti	ng from this PCN:	
			Г
Assembly Site	Assembly Site Origin (22L)	Assembly Country Code (23L)	Assembly City
UTL1	NSE	THA	Bangkok
CDAT	CDA	CHN	Chengdu
TEXAS NSTRUMENTS ADE IN: Malaysia DC: 20: SL 2 /260C/1 YEAR S SL 1 /235C/UNLIM 0		(1P) SN74LS07NSR (Q) 2000 (D) 0336 (31T)LOT: 3959047MLA (4W) TKY (1T) 75234B3S12 (P) (2P) REV: (V) 0033317 (201) 599 SWE (211) 559 WS4	

Product Affected:								
Group 1 devices (Assembly/Test Site (CDAT) qualification only):								
TPS6503200CRGERQ1	TPS6503320AARGERQ1	TPS65033300QRGERQ1	TPS65033104RGERQ1					
TPS65033201RGETQ1	TPS65033000QRGETQ1	TPS6503320DRGERQ1	TPS6503320FRGERQ1					
TPS65033203RGERQ1	TPS65033304RGERQ1	TPS6503320GRGERQ1	TPS65033007RGETQ1					
TPS65033000QRGERQ1	TPS6503320BRGERQ1	TPS65033007RGERQ1	TPS6503200BRGERQ1					
TPS65033000RGERQ1	TPS65032006RGERQ1	TPS6503320HARGERQ1	TPS6503320CRGERQ1					
TPS65032008QRGERQ1	TPS65033207RGERQ1	TPS65032002QRGERQ1	TPS65033302RGERQ1					

Group 2 device: (Assembly/Test Site (CDAT) & Die revision qualification):

TPS65033205QRGERQ1	TPS6503200AQRGETQ1	TPS6503300IQRGETQ1	TPS6503300DRGERQ1
TPS65033209QRGETQ1	TPS65032018ARGERQ1	TPS65033208RGERQ1	TPS65032018ARGETQ1
TPS65033209QRGERQ1	TPS6503300ERGERQ1	TPS6503300IQRGERQ1	TPS65033206RGERQ1
TPS65033201RGERQ1	TPS6503200AQRGERQ1	TPS65033303RGERQ1	TPS65033205QRGETQ1



Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines)

TPS65033xxx (Fedora): Automotive Grade 1 Q100 Camera PMIC P2.1 Approved 21-Oct-2021

Product Attributes

Attributes	Qual Device: <u>TP \$6503300IQRGERQ1</u>	QBS Product Reference: <u>.TP \$65033000QRGERQ1</u>
Automotive Grade Level	Grade 1	Grade 1
Operating Temp Range	-40 to +125 C	-40 to +125 C
Product Function	Power Management	Power Management
Wafer Fab Supplier	RFAB	RFAB
Die Revision	B1	B0
Assembly Site	UTL1	UTL1
Package Type	QFN	QFN
Package Designator	RGE	RGE
Ball/Lead Count	24	24

Qual By Similarity
Qual Device TPS6503300IQRGERQ1 is qualified at LEVEL3-260C

Qualification Results Data Displayed as: Number of lots / Total sample size / Total failed

т	уре	#	Test Spec	Spec Min Lot Qty SS/Lot		Test Name / Condition	Duration	Qual Device: TPS65033001QRGERQ1	QBS Product Reference: . <u>TP \$65033000QRGERQ1</u>
			Test Gr						
	PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	(MSL 3 / 260C)	-	No fails
Н	AST	A2	JEDEC JESD22-A110	3	77	Biased HAST, 130C/85%RH	96 Hours	-	3/231/0
	AC	A3	JEDEC JESD22-A102	3	77	Autoclave 121C	96 hours	-	3/231/0
	тс	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle, -65/150C	500 cycles	-	3/231/0
F	тс	A5	JEDEC JESD22-A105	1	45	Power Temperature Cycle, -40/125C	1000 cycles	-	1/45/0
н	TSL	A6	JEDEC JESD22-A103	1	45	High Temp Storage Bake 175C	1000 hours	-	3/135/0
			Test Gr	oup B – Aco	celerated	Lifetime Simulation Tests			
Н	TOL	B1	JEDEC JESD22-A108	3	77	Life Test, 125C	1000 hours	-	3/231/0
E	LFR	B2	AEC Q100-008	3	800	Early Life Failure Rate, 125C	48 hours	1/800/0	3/2400/0
E	DR	B3	AEC Q100-005 3 77 NVM Endurance, Data Retention, and Operational Life -		-	(1)			
			Test	Group C – P	ackage A	ssembly Integrity Tests			
V	VBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Cpk>1.67	-	3/90/0
٧	VBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Cpk>1.67	-	3/90/0
	SD	C3	JEDEC JESD22-B102	1	15	Surface Mount Solderability	Pb Free Solder (Post-Bake)	-	1/15/0
	SD	C3	JEDEC JESD22-B102	1	15	Surface Mount Solderability	Pb Solder (Post- Bake)	-	1/15/0
	PD	C4	JEDEC JESD22-B100 and B108	3	10	Physical Dimensions	Cpk>1.67	-	3/30/0
	LI	C6	JEDEC JESD22-B105	1	50	Lead Integrity	-	-	-

				Te	st Gr	oup D – Die Fabrication Reliability Tests	;		
	EM	D1	JESD61	-	-	Electromigration	-	Completed Per Process Technology Requirements	-
	TDDB	D2	JESD35	-	-	Time Dependant Dielectric Breakdown	Dependant Dielectric Breakdown - Completed Per Process Technology Requirements		
	HCI	D3	JESD60 & 28	-	-	Hot Injection Carrier	-	Completed Per Process Technology Requirements	-
	NBTI	D4	-	-	-	Negative Bias Temperature Instability	gative Bias Temperature Instability - Completed Per Process Technology Required		-
	SM	D5	-	-	-	Stress Migration	Stress Migration - Completed Per Process Technology Requirement		-
	Test Group E – Electrical Verification Tests								
	HBM	E2	AEC Q100-002	1	3	ESD - HBM - Q100	4000 V	1/3/0	1/3/0
	CDM	E3	AEC Q100-011	1	3	ESD - CDM - Q100	1000 V	1/3/0	1/3/0
	LU	E4	AEC Q100-004	1	6	Latch-up	(Per AEC-Q100-004)	1/6/0	1/6/0
	ED	E5	AEC Q100-009	3	30	Electrical Distributions	Electrical Distributions Cpk>1.67 Room, hot, and cold test 1/30/0		3/90/0
						Additional Tests			
	YLD			-	-	Yield Evaluation	(per mfg. Site specification)	1/Pass	-
41 (P	PC): Pre	condi	tioning:			•			

Performed for THB, Biased HAST, AC, uHAST, TC & PTC samples, as applicable

Ambient Operating Temperature by Automotive Grade Level: Grade 0 (or E): -40°C to +150°C Grade 1 (or Q): -40°C to +125°C Grade 2 (or T): -40°C to +105°C Grade 3 (or 1): -40°C to +85°C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold: HTOL, ED Room/Hot: THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU Room: AC/uHAST

TI Qualification ID: 20210107-137686

Affected ZVEI IDs: SEM- DE-02, SEM- PA-04, SEM- PA-07, SEM- PA-08, SEM- PA-18, SEM- TF-01

For questions regarding this notice, e-mails can be sent to the contacts shown below or your local Field Sales Representative.

Location	E-Mail			
WW Change Management Team	PCN ww admin team@list.ti.com			

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