


LAN9252-DIG-IO-EVB

LAN9252(Config 4-DIG-I/O)

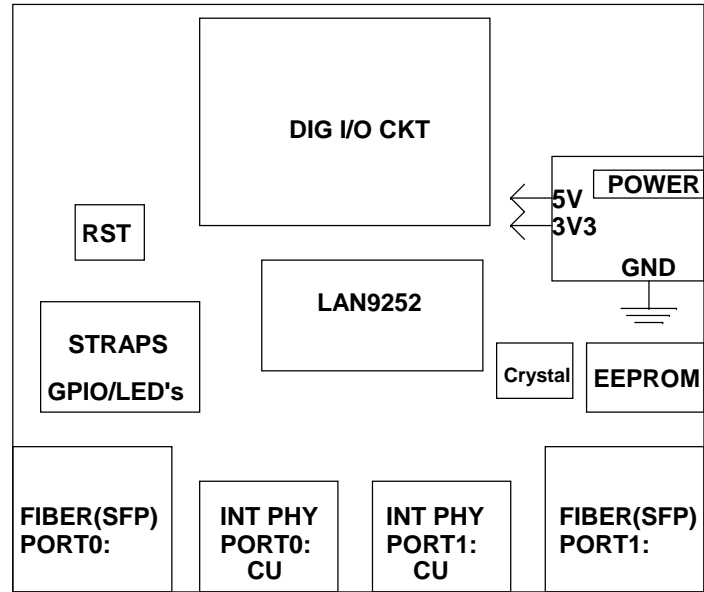
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4	LAN9252 (part1)
5	Copper Mode Interface
6	SFP Interface
7	STRAP,GPIO,I2C & FXLOS
8	LAN9252 (part2)
9	DIG-I/O




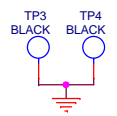
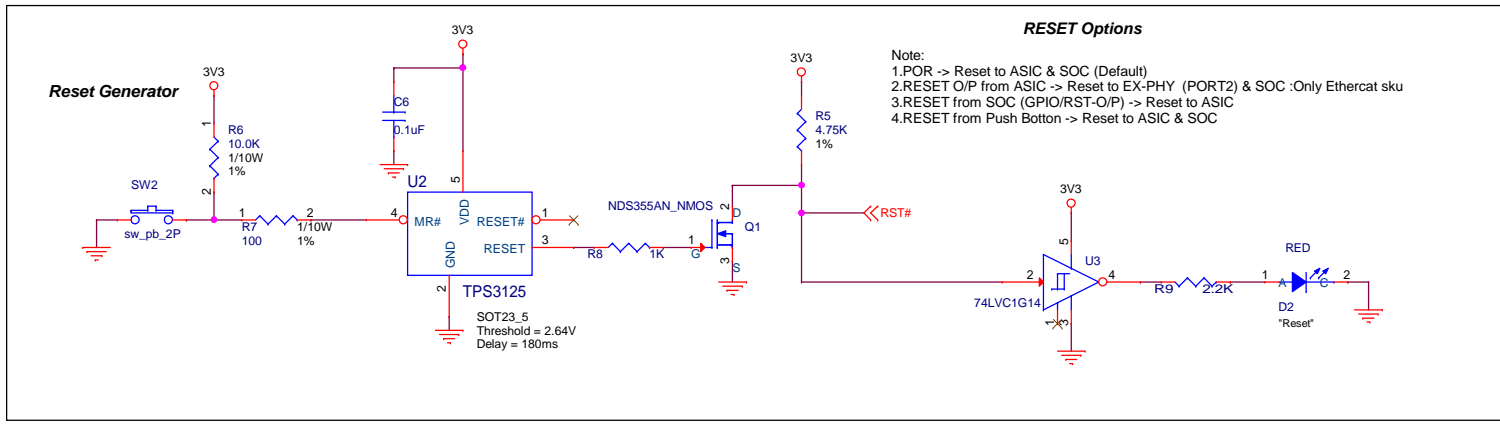
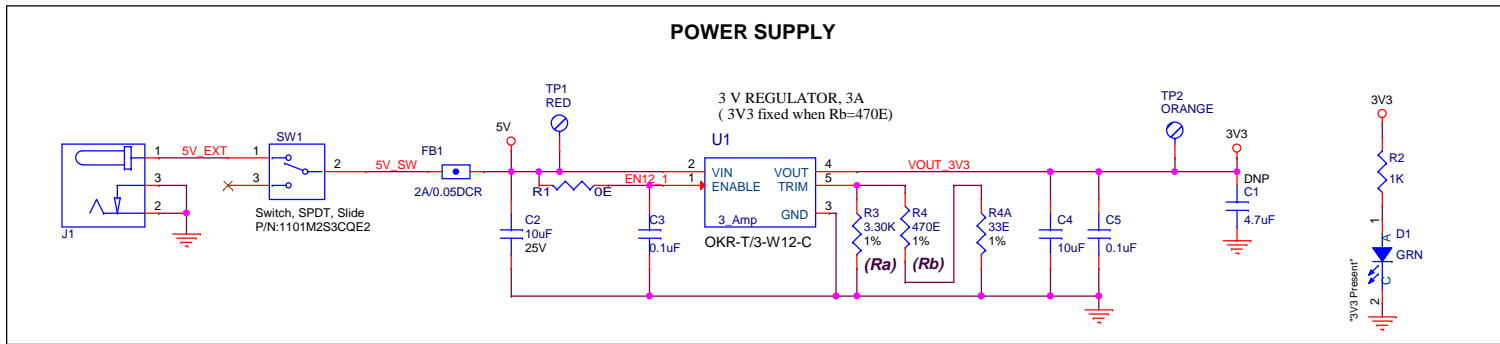
MICROCHIP Chennai India

Part Number:	LAN9252-DIG-IO-EVB	Page:	TITLE				
Size:	B	Project Name:	LAN9252-DIG-IO-EVB	Board Name:	EVB3-9252-DIG-I/O-RevB	Rev	B
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LAN9252-DIG-IO-EVB BLOCK DIAGRAM



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Part Number: LAN9252-DIG-IO-EVB	Project Name: LAN9252-DIG-IO-EVB	Page: Block Diagram
Size: B	Board Name: LAN9252-DIG-IO-EVB	Rev: B
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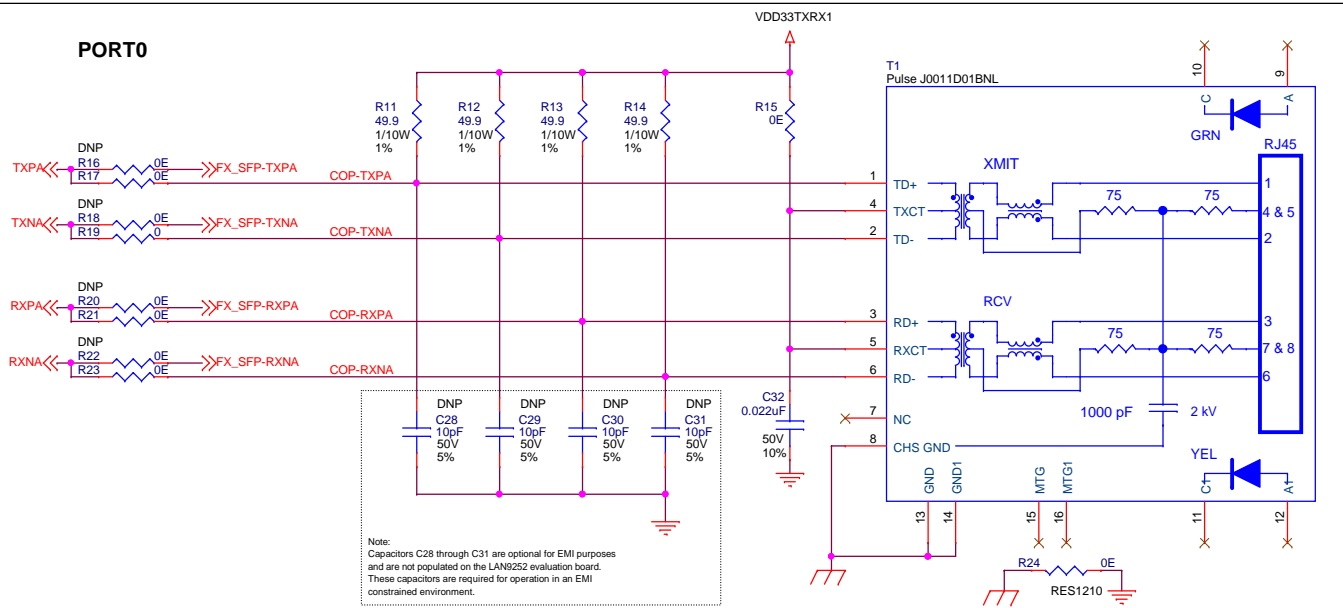


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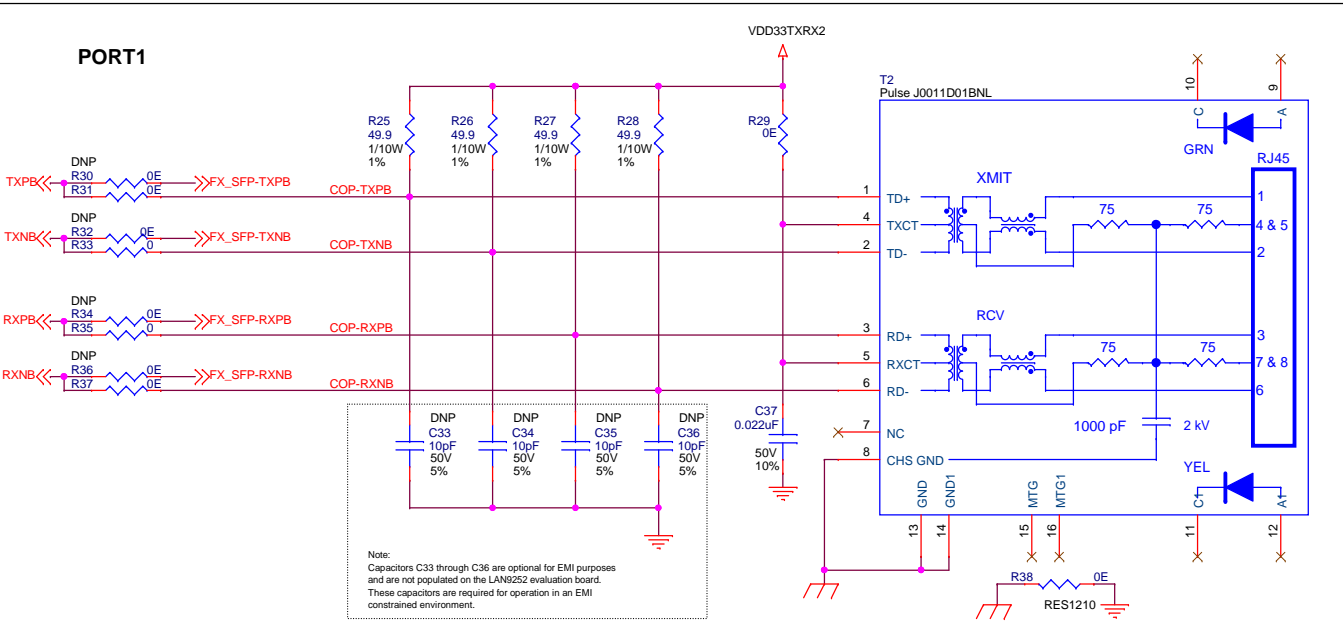
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India

Part Number:	LAN9252-DIG-IO-EVB	Page:	Power Supply & RST
Size:	B	Board Name:	EV3-9252-DIG-I/O-RevB
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PORT0

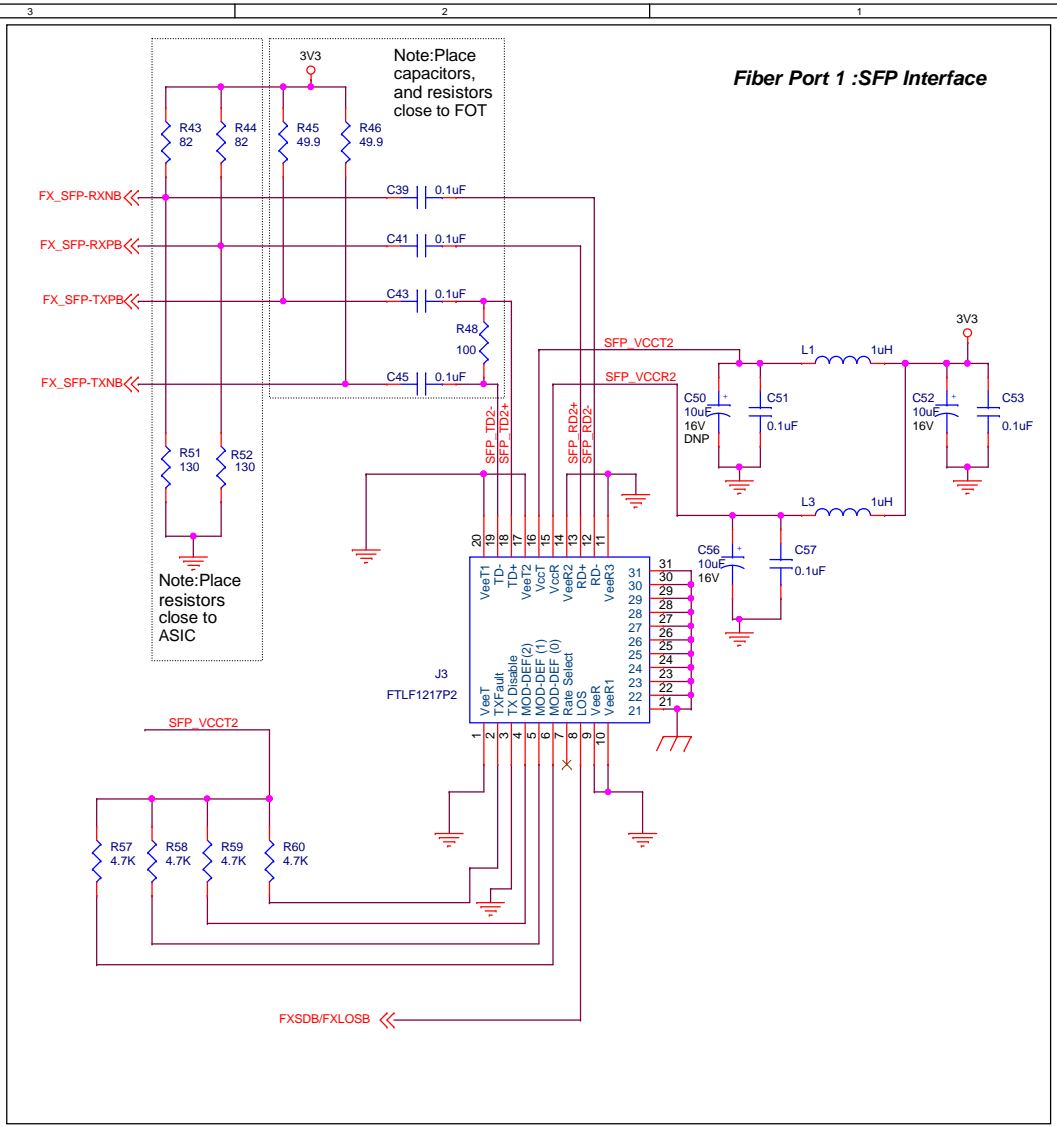
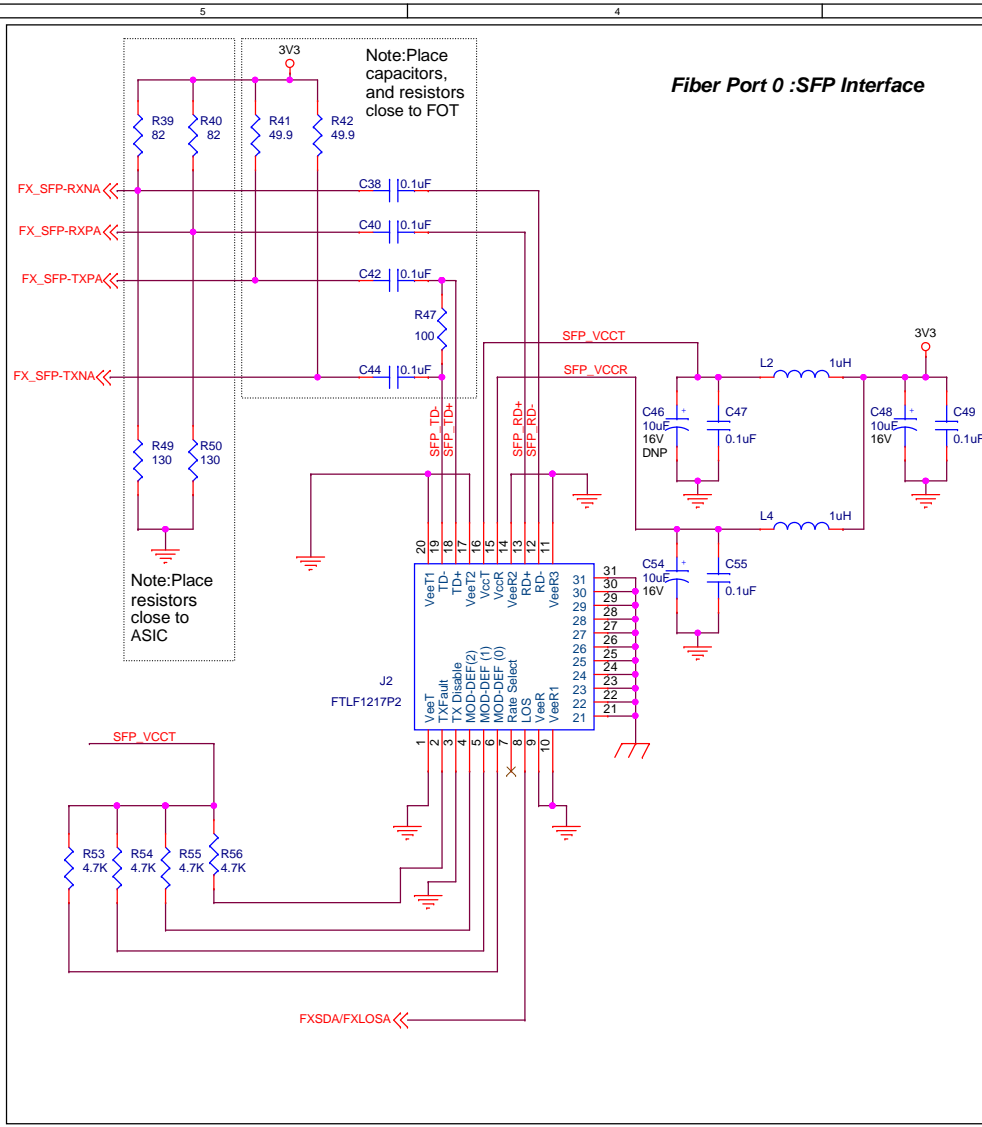


PORT1



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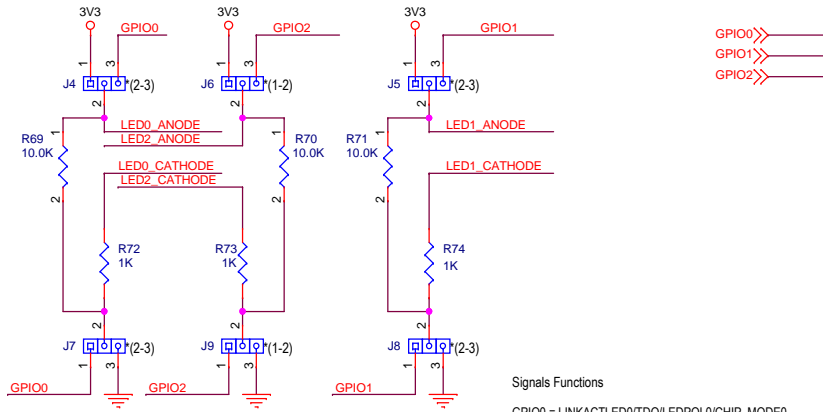
Part Number:	LAN9252-DIG-IO-EVB	Page:	Copper Mode Interface
Size:	B	Project Name:	LAN9252-DIG-IO-EVB
Date:	Tuesday, October 21, 2014	Board Name:	EVB3-9252-DIG-IO-RevB
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Part Number:	LAN9252-DIG-IO-EVB	Page:	SFP Interface
Size:	B	Project Name:	LAN9252-DIG-IO-EVB
Date:	Tuesday, October 21, 2014	Board Name:	EVB3-9252-DIG-IO-RevB
		Rev:	B
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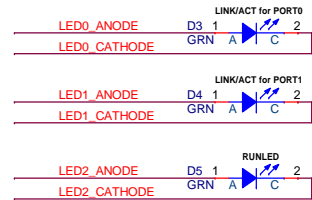
GPIO [0:2] & LED_POL_Strip



GPIO0
GPIO1
GPIO2

Signals Functions

GPIO0 = LINKACTLED0/TD0/LEDPOL0/CHIP_MODE0
 GPIO1 = LINKACTLED1/TD1/LEDPOL1/CHIP_MODE1
 GPIO2 = RUNLED/LEDPOL2/E2PSIZE



CHIP_MODE[1:0] Strap Details

CHIP_MODE[1:0]	Port Description	MODE
00[Default]	Port 0 = PHY A, Port 1 = PHY B	2 PORT MODE
01	RESERVED	RESERVED
10	Port 0 = PHY A, Port 1 = PHY B, Port 2 = Mil	3 PORT DOWNSTREAM MODE
11	Port 0 = Mil, Port 1 = PHY B, Port 2 = PHY A	3 PORT UPSTREAM MODE

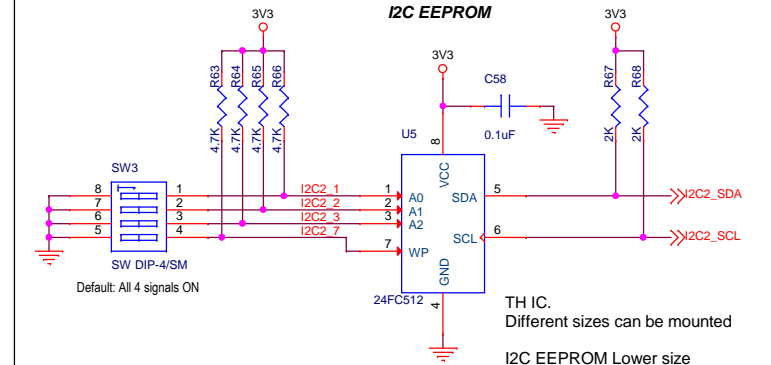
Strap Details

Signal Name	Logic	Connector	LED Polarity Strap
CHIP_MODE0	0	J4,J7 (2&3) Default	The LED is set as active high.
	1	J4,J7 (1&2)	The LED is set as active low,
CHIP_MODE1	0	J5,J8 (2&3) Default	The LED is set as active high.
	1	J5,J8 (1&2)	The LED is set as active low,
E2PSIZE	0	J6,J9 (2&3)	The LED is set as active high. EEPROM Size=1K bits (128 x 8) through 16K bits (2K x 8)
	1	J6,J9 (1&2) Default	The LED is set as active low, EEPROM Size=32K bits (4K x 8) through 512K bits (64K x 8) or 4Mbits (512K x 8) (LAN9252 only) [Default]

Note:

--To use GPIOs as LED
 * Short 2-3 of both jumpers (ex. for GPIO0 short 2-3 of J4 & J7)

I2C EEPROM

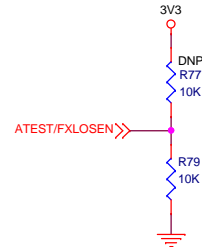


TH IC.
 Different sizes can be mounted

I2C EEPROM Lower size
 Below 16K(2K X 8) (24FC04)

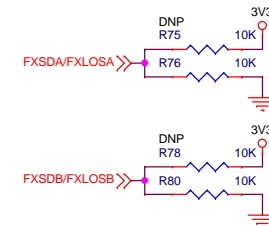
I2C EEPROM Higher size
 Above 16K(2K X 8) (24FC512)

FX_Los_Strip_1 & 2



R77	R79	Ref.Voltage	Function
Populate	DNP	3V3	Above 2 V selects FX-LOS for ports 0 and 1
Populate	Populate	1V5	Level of 1.5 V selects FX-LOS for port 0 and FX-SD/copper twisted pair for port 1 further determined by FXSDB
DNP (Default)	Populate (Default)	0 (Default)	Level of 0V Selects FX-SD / copper twisted pair for ports A and B further determined by FXSDB and FXSDB.

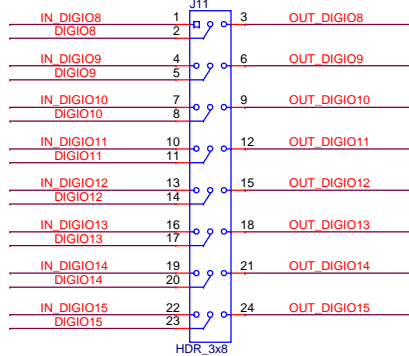
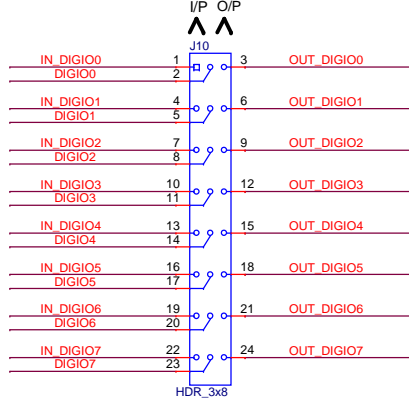
FX_Mode_Strip_1 & 2



PORT	MODE	Populate	DNP
PORT0	Copper (Default)	R76	R75
	Fiber	R75	R76
PORT1	Copper (Default)	R80	R78
	Fiber	R78	R80

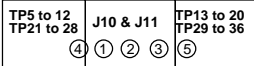


1-2 side short of J10 = Input Mode (Default)
 2-3 side short of J10 = Output Mode



1-2 side short of J11 = Input Mode (Default)
 2-3 side short of J11 = Output Mode

DIG-IO JUMPER OPTIONS



Short 1-2 = Input Mode

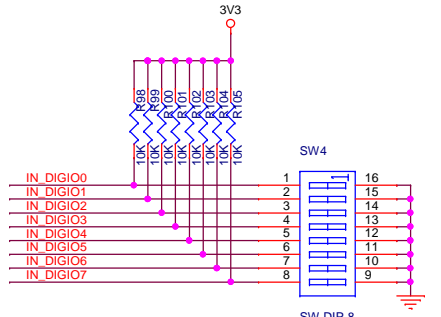
Short 2-3 = Output Mode

Short 1-4 & 3-5 = Bidirectional Mode

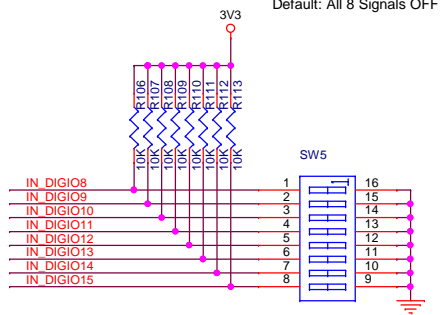
Digital INPUTS

Default
 SW4 & SW5 are in OFF position (Pullup active)

For Pull Down, move SW4 & SW5 to ON position

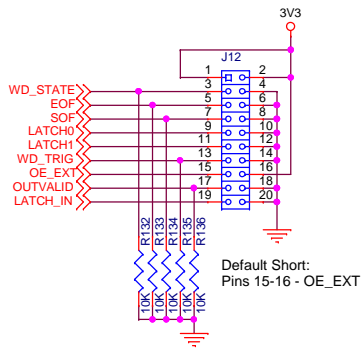
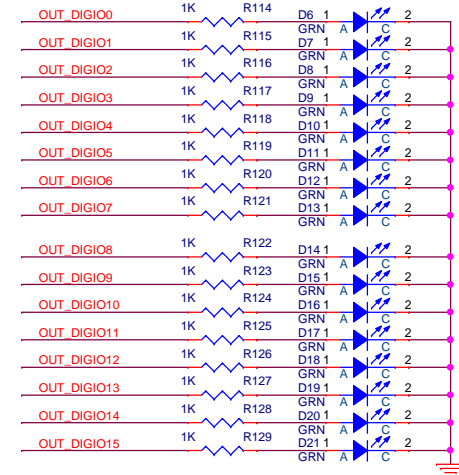


SW DIP-8
 Default: All 8 Signals OFF

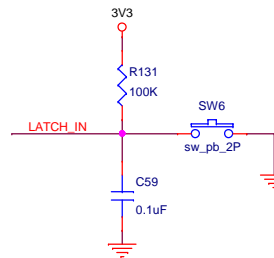
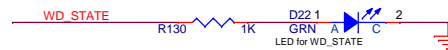


SW DIP-8
 Default: All 8 Signals OFF

Digital OUTPUTS



Default Short:
 Pins 15-16 - OE_EXT



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 Size: B Project Name: LAN9252-DIG-IO-EVB Board Name: EVB3-9252-DIG-I/O-RevB Rev: B
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