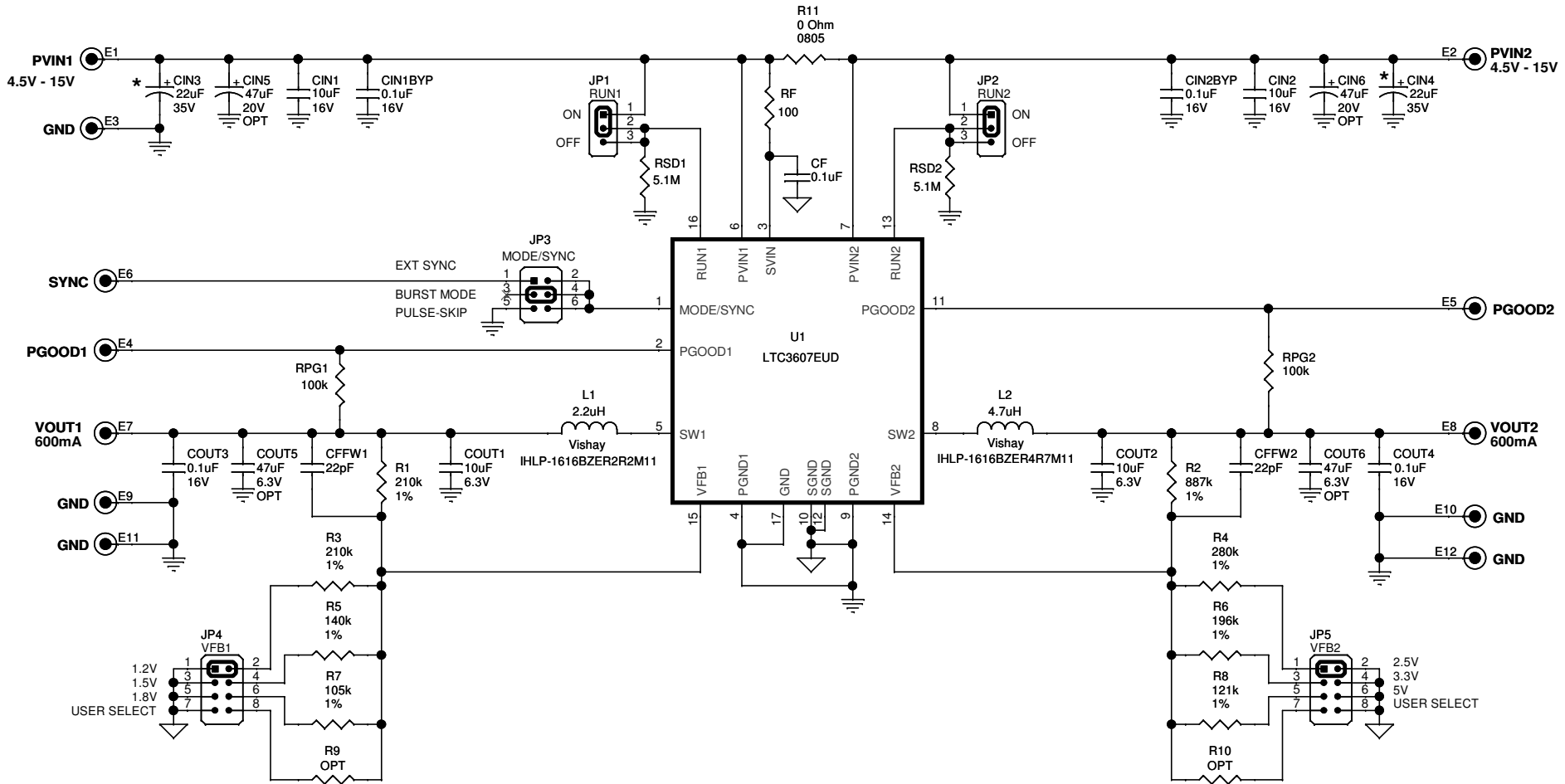


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	2	PRODUCTION	TOM G.	3-15-13



[*] CIN3 and CIN4 ARE INSERTED ON DC1596A TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO THE USE OF LONG INPUT LEADS. ON A NORMAL, TYPICAL PCB, WITH SHORT TRACES, CIN3 AND CIN4 ARE NOT NEEDED.

CUSTOMER NOTICE
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES.	HZ
APP ENG.	TG

SCALE = NONE



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TITLE: SCHEMATIC
 DUAL 600mA MONOLITHIC, SYNCHRONOUS STEP-DOWN REGULATOR

SIZE	IC NO.	LTC3607EUD	REV.
N/A		DEMO CIRCUIT 1596A	2

DATE: Monday, May 13, 2013 SHEET 1 OF 1