# **Triac couplers—basic properties and application design**

## <span id="page-0-0"></span>**Introduction**

Triac couplers are photocouplers that have a triac output. They are commonly used for AC powered load switching applications. Triac couplers operate on AC power and differ from transistor couplers and IC couplers in several ways. This document explains the implications of differences in performance characteristics for circuit design.

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## **TOSHIBA**

#### Triac couplers-basic properties and application design

## **Application Notes**



## <span id="page-3-0"></span>**1. Typical uses of triac couplers**

**Triac couplers are commonly used for on/off switching of AC loads such as AC-powered motors, lamps, heaters and electromagnetic valves. For small loads, a single triac coupler will usually suffice. For larger loads, a triac coupler is often used in combination with the main triac element, which has direct control over the load and uses the triac coupler as the trigger device.**

Although mechanical relays are sometimes used for AC load control, triac couplers are generally considered superior, due to the many limitations of mechanical relays such as finite service life of contacts, slow switching speed and audible sound during operation. Triac couplers can also be used for phase control of AC loads. Triac couplers have similar operating characteristics to semiconductors, and as such are quite different from mechanical relays. Circuit design should take these characteristics into consideration.

## <span id="page-3-1"></span>**2. Operating principles**

A triac coupler comprises an LED element on the input side and a triac element on the output side. The triac element consists of antiparallel connected P-N-P-N junction thyristor elements residing on a single chip. Normally a triac element switches on in response to current at the gate terminal. In a triac coupler, light from the LED is converted to photoelectric current at the PN junction surface, providing gate current that switches on the triac.

While thyristor elements can perform on/off switching for current in one direction only, the triac element has multiple thyristor elements connected in antiparallel configuration, thus allowing on/off control of current moving in both directions (typically AC power).



## <span id="page-4-0"></span>**3. Construction of triac couplers**

A triac coupler comprises an LED on the input side and a phototriac element on the output side. Normally the LED and triac elements are on opposite sides, to allow light from the LED to reach the photosensitive surface of the triac PN junction.



Figure 3.1.1 Cutaway view of triac coupler



Figure 3.1.2 Cross-section view of triac coupler



Figure 3.1.3 Equivalent circuit diagram for triac coupler

#### <span id="page-5-0"></span>**4. Characteristics of triac couplers**

The table below compares the key characteristics of MOSFET output semiconductor photorelays and mechanical relays with physical contacts.



#### Table 4.1.1 Triac couplers vs. other relay types

## <span id="page-6-0"></span>**5. Triac couplers vs. mechanical relays**

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A triac coupler is a type of semiconductor relay. It is considered superior to mechanical relays in several key respects.

- 1. Triac couplers have a compact mounting footprint, with minimum design dimensions of around 3.7 x 7.0 x 2.1 mm.
- 2. Due to the lack of mechanical contacts, triac couplers are more reliable and have a longer service life.
- 3. Triac couplers help to reduce the overall power requirements of the circuit, since the input LED requires only 5 to 10 mA for switching.
- 4. Triac couplers offer faster switching than mechanical relays as well as silent operation with minimal electrical noise.
- 5. Being semiconductor switches, triac couplers are compatible with hot switches.
- 6. Triac couplers are not subject to chattering, a common problem with mechanical relays.
- 7. Triac couplers can be used for phase control and power control of AC loads.





## <span id="page-7-0"></span>**6. Basic application circuit of triac coupler**

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Triac couplers can be used in isolation for AC load control, provided that the load is relatively small (about 50 mA or less). For larger currents (up to several amps), the triac coupler is combined with the main triac. The diagram below shows a basic circuit configuration. Rs and Cs are elements of a snubber circuit that prevents triac malfunction by shielding it from noise, while a varistor absorbs any surge voltage from the power line.



Figure 6.1.1 AC load control using triac coupler only



Figure 6.1.2 AC load control using triac coupler combined with main triac

## <span id="page-8-0"></span>**7. How to Use triac couplers**

The way the triac operates depends on the type of AC load. In this section, we consider the operating voltage and current for resistance load and inductive load and show the corresponding non-zero-cross operating waveforms. Zero cross triac couplers will be discussed later in Section 10.1.

#### <span id="page-8-1"></span>**7.1 Basic operation with resistance load**





Control circuit and operating waveforms for triac coupler with direct AC load

#### 1. Switching on

The output side triac switches on in response to input LED current. The load operates and load current is generated. On-state voltage between T1 and T2 of the triac is in the range 1 to 2 V. 2. Switching off

The triac does not switch off when the LED turns off; it waits until the AC power supply is in the vicinity of 0 V and below the holding current (see Section 10.11). At this point the AC load also switches off.

## <span id="page-9-0"></span>**7.2 Basic operation with inductive load**



Figure 7.2.1 Triac coupler operating waveforms (non-zero-cross)



Control circuit and operating waveforms for triac coupler with direct AC load

For inductive load, current is 90° phase behind voltage.

#### 1. Switching on

The output triac switches on in response to input LED current. The load operates and load current is generated. During the on state, the on-state voltage of the triac between T1 and T2 is around 1 to 2 V.

#### 2. Switching off

The triac does not switch off when the LED turns off; it waits until the AC power is in the vicinity of 0 V and below the holding current (see Section 10.11). At this point the AC load also switches off.

#### <span id="page-10-0"></span>**8. Key parameters on triac coupler data sheet**

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The key items of interest on data sheets for triac couplers are described below. Section 10 also describes implications for circuit design.

> Input forward current The maximum current that the input LED can absorb without failing when ambient temperature is 25°C. This should be used as the maximum value in the design.

Off-state output terminal voltage The maximum voltage that can safely be applied to the triac output terminals. This governs the power supply rating.

#### 7. Absolute Maximum Ratings (Note) (Unless otherwise specified,  $T_a = 25 \degree C$ )



Isolation voltage The maximum voltage that can be applied between the input and output terminals for one minute without causing insulation failure.

#### R.M.S on-state current

The maximum load current that can be sent to the output side triac when an electrical connection has been established (note: can be affected by ambient temperature). Circuit design should prevent load current from exceeding the maximum value.



#### Table 8.1.2 Data sheet sample 2

Peak off-state current The leak current when the output triac is off. The circuit designs should ensure that the load is not impacted by leak current.

Peak on-state voltage The voltage at either side of the output triac in the on state.

#### 9. Electrical Characteristics (Unless otherwise specified,  $T_a = 25 \degree C$ )



#### 10. Coupled Electrical Characteristics (Unless otherwise specified,  $T_a = 25 \degree C$ )



The minimum value of LED input current to switch on output triac. The circuit design should be considered the LED deterioration over time, the temperature characteristics, and the circuit margin. LED input current should be greater than  $I_{FT}(MAX)$  (5mA in this case). Refer to Section 9.1

## <span id="page-12-0"></span>**9. Triac coupler circuit design**

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In describing basic application circuits for triac couplers, we will begin by looking at designing constants for peripheral circuits. Since this is governed by the characteristics of the main triac, we will consider circuits that incorporate the main triac. Although the discussion below is for non-zero-cross triac couplers, it applies equally to zero-cross triac couplers.



Figure 9.1.1 Basic application circuit for triac coupler

#### <span id="page-12-1"></span>**9.1 LED Input current IF**

The output triac requires current to the input LED of the triac coupler in order to switch on. The current must be larger than the maximum  $I_{FT}$  value on the data sheet. It is normally determined as follows:

I<sub>F</sub> design value = I<sub>FT</sub> (max.) × a1 × a2 × a3

#### where

α1 is the coefficient of LED deterioration over time, taken from the graph of average value (X) (X)ー3σ (see Figure 1). Note that the coefficient increases in line with both ambient temperature and  $I_F$  value, and is also affected by the LED type.

a2 is the coefficient for change in ambient temperature of  $I_{FT}$ , based on the  $I_{FT}-T_a$  curve in the relevant technical data (see Figure 9.1.3).

α3 is the drive coefficient, a design margin that takes into account factors such as supply power variance and tolerance requirements









In an operating environment where the temperature can drop as low as – 40°C, I<sub>FT</sub> increases by 30% so:  $a2 = 1.3$ 

#### Figure 9.1.3  $I_{FT}$  temperature curve

The TLP265J circuit design is shown below as an example.

Trigger LED currents for the triac coupler  $I_{FT}$  (Max) are ranked for different product types. The table below shows the TLP265J values: no rank = 10 mA,  $(IFT7) = 7$  mA.

Table 9.1.1 Trigger LED current  $(I_{FT})$  categories on data sheet





Sample calculation 1

This example assumes a no rank product.

For a maximum I<sub>FT</sub> product rating of 10 mA with T<sub>a</sub> = 40°C, expected product life = 100,000 hours and a design margin of  $a3 = 1.2$ , we have:

 $I_F$  design value =  $I_{FT}$ (Max.) x a1 x a2 x a3  $= 10$  mA x 1.43 x 1.3 x 1.2  $= 22.3$  mA

So the circuit should be designed with  $I_F$  current of at least 22.3 mA.

Sample calculation 2 This example assumes an (IFT7) product.

> I<sub>F</sub> design value = I<sub>FT</sub>(Max.) x  $a1 \times a2 \times a3$  $= 7$  mA x 1.43 x 1.3 x 1.2  $= 15.6$  mA

So the circuit should be designed with  $I_F$  current of at least 15.6 mA.

From the above calculations we can see that a product with (lower IFT) rank can be designed with lower input current  $I_F$ .

#### <span id="page-14-0"></span>**9.2 Limiting resistance Rin**

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We use the LED current I<sub>F</sub> derived above to determine the value of the limiting resistance value R<sub>in</sub> connected in series with the LED.  $R_{in}$  is affected by several factors: the voltage drop  $V_F$  associated with the LED, operating temperature dependency ( $V_F$  tends to be higher at low temperatures) and the voltage drop at the signal input (drive) element, denoted here as Tr.  $V_{CE(sat)}$ . The relevant technical documentation will show the maximum values for each of these. It is important to stay below the absolute maximum rated I<sub>F</sub> value for the triac coupler. Also, the power supply ( $V_{cc}$ ) and current to the drive element should have at least the capacity of the design  $I_{F}$ .



Figure 9.2.1 Basic application circuit for triac coupler

The maximum value of  $R_{in}$  based on the I<sub>F</sub> design value is given by:

$$
R_{in} \leq \frac{V_{CC}(MIN) - V_{F}(MAX) - V_{CE}(sat)(MAX)}{Design value of I_{F}}
$$

The minimum value of  $R_{in}$  based on the maximum rated value of  $I_F$  is given by:

$$
R_{in} > \frac{V_{CC}(MAX) - V_{F}(MIN) - V_{CE}(sat)(MIN)}{I_{F}(MAX)}
$$

## <span id="page-14-1"></span>**9.3 Selection of main triac**

The choice of main triac is governed by the power supply voltage and the load current. More information can be found on the data sheet for the main triac.

## <span id="page-14-2"></span>**9.4 Gate resistance R<sub>GK</sub>**

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Gate resistsance  $R_{GK}$  helps to regulate the sensitivity of the main triac and prevents operational malfunction associated with power supply noise and dV/dt.

Gate resistance also diverts noise from the gate to reduce the incidence of malfunction. Normally a resistance in the range 10 to 100  $\Omega$  is sufficient. The design value will depend on the characteristics of the main triac. In some cases an additional capacitor  $(0.1 - 0.01 \mu F$  approx.) may be installed in parallel with  $R_{GK}$  as a noise bypass.

## <span id="page-15-0"></span>**9.5** Limiting resistance R<sub>T</sub>

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The triac coupler acts as the trigger device for the main triac, allowing gate current to pass through while the main triac is off but then shutting off the current when the main triac switches on. The data sheet gives a rated value for  $I_{ONP}$ , the pulse on-state current, which is the current present when the main triac is off. For most triac couplers this will be 2 A. For non-zero-cross triac couplers, the current should normally be no more than about half the rated value. Thus, 100 Ω resistance is recommended for a 100 V AC power supply and 200 Ω for a 200 V AC power supply. Zero-cross triac couplers tend to have lower on-state voltage and limiting resistance is generally not required.

## <span id="page-15-1"></span>**9.6 Snubber circuit,**  $C_s$  **and**  $R_s$

Normally, a snubber circuit is inserted to suppress malfunction caused by dV/dt (Section 10.7), commutation dV/dt (Section 10.8), and impulsive noise (Section 10.10). The capacitor  $C_S$  works effectively to suppress dV/dt, but  $R_S$  must be inserted to prevent the elements from being destroyed by the discharge current from the  $C<sub>S</sub>$  when the triac is turned ON. Generally, about 47  $\Omega$  is used for the AC100V system and about 100  $\Omega$  for the AC200V system. We recommend the following constants for each power supply voltage condition.

AC100V: R<sub>S</sub>=47Ω, C<sub>S</sub>=0.033μF AC200V:  $R_s = 100$ Ω, C<sub>S</sub>=0.1μF

In case of phase control, a 1W class resistor is recommended because the discharge current from the capacitor  $C_S$  flows at every half cycle of the resistor  $R_S$ .

Capacitors  $C_S$  are used with a withstand voltage of 400V for AC100V system and 600V for AC200V system.

Care must also be taken when connecting a snubber circuit, as leakage current through that circuit will also occur.

For example, when AC200V, f=50Hz,  $R_s = 100\Omega$ , and C<sub>s</sub>=0.1µF, the Z<sub>CS</sub> of the snubber circuit is as follows.

$$
Z_{CS} = \sqrt{R_S^2 + \frac{1}{(2\pi f C_S)^2}}
$$
  
= 
$$
\sqrt{100^2 + \frac{1}{(2 \times 3.14 \times 50 \times 0.1 \times 10^{-6})^2}} \approx 32 \text{ k}\Omega
$$

Therefore, the leakage current  $I_{CS}$  flowing through the snubber circuit is as follows.

$$
I_{CS} = \frac{200 \text{ V}}{32 \text{ k}\Omega} \approx 6 \text{ mA}
$$

It is necessary to consider whether this leakage current does not affect the operation of the circuit.

## <span id="page-16-0"></span>**9.7 Surge protection varistor**

TNR surge protection varistor is used to prevent damage to the triac in circuits where the voltage may exceed the maximum rated voltage of the triac. The TNR must have a rapid response time in the event of a surge. For AC 100 V power the TNR element should be in the range of 200 to 300 V; for AC 200 V it should be 400 to 500 V.

## <span id="page-16-1"></span>**10. Design considerations**

Triac couplers are normally combined with the main triac for AC load control. Key design considerations are listed in Sections 10.1 through 10.14. For the purpose of clarity, we will consider triac couplers used in isolation. Where a main triac is connected, commutation dV/dt (Sections 10.8 and 10.9) and holding current  $I_H$  (Section 10.11) characteristics apply on the main triac side.

- 10.1 Zero-cross vs. non-zero-cross
- 10.2 Basic operation by phase control
- 10.3 Zero-cross voltage, inhibit voltage  $V_{\text{IH}}$
- 10.4 Inhibit current  $I_{IH}$
- 10.5  $I_{DRM}$  characteristics
- 10.6 Switching time  $t_{ON}$
- 10.7 dV/dt characteristics
- 10.8 Commutation dV/dt characteristics
- 10.9 Commutation dV/dt triac coupler malfunction—inductive loads
- 10.10 Impulse noise immunity  $V_N$  characteristics
- 10.11 Holding current  $I_H$
- 10.12 RMS On-state current  $I_T$
- 10.13 Pulse on-state current  $I_{TP}$
- 10.14 Peak non-repetitive surge current  $I_{TSM}$

#### <span id="page-17-0"></span>**10.1 Zero-cross vs. non-zero-cross**

Zero-cross triac couplers are designed to remain in the off state during high voltage from the AC power supply, and are often used to protect against excessive rush current and EMI when switched on. Note that zero-cross triac couplers cannot be used for phase control (see Section 10.2).

Zero-cross circuit operation is shown below.

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Figure 10.1.1 Operating waveform for zero-cross triac coupler



Circuit design and operating waveforms for zero cross triac coupler for direct AC load control

#### 1. Switching on

If the AC power supply voltage is greater than the zero-cross voltage when input LED current is registered, the zero cross function prevents the triac from switching on. The triac switches on when the AC power drops down to the vicinity of 0 V (close to the zero cross). During the on state, the on-state voltage of the triac between T1 and T2 is around 1 to 2 V.

#### 2. Switching off

The triac does not switch off when the LED turns off; it waits until the AC power is in the vicinity of 0 V and below the holding current (see Section 10.11). At this point the AC load also switches off.

#### <span id="page-18-0"></span>**10.2 Basic operation by phase control**

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By shifting the phase of the input LED current relative to the AC power supply phase, we can control the triac on-state time and regulate the amount of electric energy reaching the load. A zero-cross triac switches on in the vicinity of 0 V only, which is not suitable for phase control. Phase control operation is shown below; Figure 10.2.1 is for high electric energy and Figure 10.2.2 for low electric energy.









Triac coupler AC load phase control circuit and operating waveforms (not applicable to zero-cross type)

#### 1. Switching on

The output triac switches on in response to input LED current. The load operates and load current is generated. During the on state, the on-state voltage of the triac between T1 and T2 is around 1 to 2 V.

#### 2. Switching off

The triac does not switch off when the LED turns off; it waits until the AC power is in the vicinity of 0 V and below the holding current (see Section 10.11). At this point the AC load also switches off. The load operates once LED current is detected and continues until the AC power is close to 0 V. Load power consumption can be controlled by adjusting the LED current phase angle.

#### 3. Controlling electric energy

The electric energy applied to the load is controlled by the timing that the LED switches on input current relative to the AC power supply phase. Note that this cannot be done for switching off, since the off timing is always in the vicinity of the zero cross.

The input LED pulse current time should be longer than the triac on state time  $t_{on}$ .

Figure 10.2.2 Load current and voltage waveform for phase

control with low electric energy

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## <span id="page-19-0"></span>**10.3 Zero-cross voltage, inhibit voltage V<sub>IH</sub>**

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A zero-cross triac coupler is one where the triac does not come on in response to on-state LED at a voltage greater than the zero-cross voltage. This is because the triac element features a built-in voltage detection circuit that shuts off the gate current if the voltage exceeds a predefined threshold level. Although the zero-cross voltage value varies between products, the maximum guaranteed value is called the zero-cross voltage or inhibit voltage.

Generally speaking, when the zero-cross voltage is low, the triac does not switch on even for high triac voltages. A lower zero-cross voltage can thus help to suppress noise; however it also limits the on-state range within a given AC power cycle. For consistent results, it is important to consider both the level and duration of the LED current. Note that zero-cross voltage (inhibit voltage) is also affected by temperature. The graphs below plot data taken from a typical data sheet.



#### <span id="page-20-0"></span>**10.4 Inhibit current I<sub>IH</sub>**

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Where the zero-cross triac coupler voltage exceeds the zero-cross voltage, the triac element is not switched on by the LED, leading to leakage current in the triac. The leakage current exceeds  $I_{DRM}$  by the amount of on current sent to the LED. The associated inhibit current  $I_{IH}$  can cause load malfunction if the circuit is not designed properly. Note that inhibit current tends to be higher at low temperatures.



#### <span id="page-20-1"></span>10.5 I<sub>DRM</sub> characteristics

If the maximum rated voltage  $V_{DRM}$  is applied to either side of the triac element with the input LED in the off state, the triac switches off, and some leakage current is generated. The leakage current, denoted I<sub>DRM</sub>, increases exponentially with temperature and at high temperatures can even cause a circuit malfunction.

The data sheet provides a graph of  $I_{DRM}$  against  $T_a$ . The sample graph shown below is for a product that guarantees a maximum I<sub>DRM</sub> of 1 µA (MAX) for V<sub>DRM</sub> = 600 V and T<sub>a</sub> = 25°C. From the graph we can see that when  $T_a = 85^{\circ}$ C,  $I_{DRM} = 4 \mu$ A. 100



#### <span id="page-21-0"></span>**10.6 Switching time ton**

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In Section 10.2, we saw how phase control allows us to control the LED on-state time with periodal square wave signal inputs. There is a short lag after the LED current before the triac coupler switches on; this is known as the switching time  $t_{ON}$ . Note that the triac will not switch on if the pulse width of the LED current (the LED on time) is shorter than  $t_{ON}$ .

The higher the LED current, the shorter the  $t_{ON}$  value.



Figure 10.6.1 Triac coupler switching waveform

## <span id="page-22-0"></span>**10.7 dV/dt characteristics**

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When a voltage lower than the maximum rated voltage  $V_{DRM}$  is applied to both sides of the triac element with the input LED off, if the voltage is accompanied by an abrupt rise (dV/dt) this can cause the triac to switch on unexpectedly. The dV/dt threshold value just prior to the triac switching on is known as the dV/dt tolerance, or Rate of Rise of Off-State Voltage on the data sheet.

When the triac element PN junction surface is in reverse bias state and an abrupt rise voltage dV/dt is applied, this causes charging current to flow to the PN junction capacitance, which acts as triac gate current and likewise causes the triac to switch on unexpectedly.

The charging current tends to increase in line with the temperature, so further evaluation is required in this area.

A snubber circuit with  $C_S$  and  $R_S$  in series can be used to prevent dV/dt malfunction by ameliorating the dV/dt inclination. It should be noted however that the snubber circuit is not a panacea for all forms of malfunction.



Figure 10.7.1 dV/dt characteristics





#### <span id="page-23-0"></span>**10.8 Commutation dV/dt characteristics**

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In some cases when a triac coupler is used in isolation for on-off load switching, the self-holding feature can cause the triac to remain in on state on the output side after the LED has switched off. A typical example is commutation dV/dt malfunction.

The self-holding function of the output triac does not allow the triac to switch off as soon as the input LED switches off. It waits until the load current is close to the zero-cross current and below the holding current before switching off the triac.

For purely resistance type loads, since voltage and current are in the same phase, the voltage is close to 0 V when the current is close to the zero-cross current. The voltage is relatively low when the triac switches off, so the change in voltage dV/dt is fairly small.

For inductive loads, however, where current and voltage can be as much as 90° out of phase, the voltage may be quite high at the point where the current is switched off, and this leads to a significant voltage change between the two sides of the triac. This voltage change is called the commutation dV/dt. When the commutation dV/dt is large, minority carriers that accumulate in the triac are transformed into gate current of the triac in the opposite direction connected in antiparallel, causing the triac in the opposite direction to switch on unexpectedly. As a result, the triac is unable to switch off in the vicinity of the zero cross and remains in the on state for the next AC voltage cycle.

This type of malfunction is the product of triac characteristics when two triacs are connected in antiparallel and use a common semiconductor junction surface. Two separate thyristors in an antiparallel configuration would not produce a commutation dV/dt malfunction. A snubber circuit with  $C<sub>S</sub>$  and R<sub>S</sub> in series connected to both sides of the triac can help to prevent commutation dV/dt malfunction .



 $\bullet$  (2) Triac operations (during commutation) 1. Normal operation: switches from ON to OFF state 2. Commutation dV/dt malfunction: remains ON, unable to switch OFF

#### Figure 10.8.1 Commutation dV/dt malfunction



Section 10.9 shows current and voltage waveforms for normal operation and malfunction in inductive load control applications, which tend to have a higher incidence of commutation dV/dt malfunction.



Application Notes

#### <span id="page-24-0"></span>**10.9 Commutation dV/dt triac coupler malfunction—inductive loads**

The waveforms below are for a triac coupler circuit used to control direct AC load. For inductive loads, the current phase lags 90° behind voltage.



#### 1. Switching on

The output triac switches on in response to input LED current. The load operates and load current is generated. During the on state, the on-state voltage of the triac between T1 and T2 is around 1 to 2 V.

#### 2. Switching off normally

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The triac does not switch off when the LED turns off; it waits until the AC power is in the vicinity of 0 V and below the holding current (see Section 10.11). At this point the AC load also switches off.

#### 3. Switching off due to commutation dV/dt malfunction

The triac is unable to switch off due to the influence of dV/dt below the holding current, and it remains in the on state.

#### <span id="page-25-0"></span>Application Notes **10.10 Impulse noise immunity V<sub>N</sub> characteristics**

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If impulse noise is superimposed onto AC power while the input LED is off, the output triac may come on unexpectedly. Impulse noise  $V_N$  denotes the impulse peak voltage  $V_N$  at which a superimposed impulse  $t_N$  (normally of width 1 µs) forces the triac to switch on. Impulse noise malfunction is attributed to the combined impact of breakdown current and charging current in the capacitance portion of PN junction due to dV/dt. It cannot be prevented with a  $(C_s + R_s)$  snubber circuit.



Figure 10.10.1 Impulse noise malfunction





#### <span id="page-26-0"></span>**10.11 Holding current I<sub>H</sub>**

After the triac switches on in response to on-state input LED, the triac self holding feature maintains the on state even if the LED subsequently switches off. If a drop in the peak voltage of the AC halfcycle causes the current to the triac to fall below the holding current threshold  $I_{H}$ , the self hold is released and the triac switches off. Note that  $I_H$  is inversely proportion to temperature.







#### <span id="page-27-0"></span>**10.12 RMS On-state current I**<sub>T</sub>

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On-state current  $I<sub>T</sub>$  to the output side triac is defined in the maximum rating. The rated allowance value is temperature dependent, as per the RMS On-state current curve below.



#### <span id="page-27-1"></span>**10.13** Pulse on-state current I<sub>TP</sub>

Where the triac coupler is used as the trigger device to drive the main triac gate, pulse current flows to the photo-triac until such time as the main triac switches on. The AC power current generated in each half-cycle is defined by a maximum rated value. For example, the TLP265J is rated at  $I_{TP}$  (Max) = 2A for a 100 µs pulse at 120 pps.

## <span id="page-27-2"></span>**10.14** Peak non-repetitive surge current I<sub>TSM</sub>

Where a triac coupler is used to drive inductive and capacitive loads, the initially low impedance can lead to significant rush current when the triac coupler switches on. Since the rush current may exceed the rated on-state current  $I_T$ , it is given a maximum rating known as Peak non-repetitive surge current I<sub>TSM</sub>. The TLP265J is rated at I<sub>TSM</sub> (Max) = 1.2 A for Pw = 10 ms.

## <span id="page-28-0"></span>**11. Typical triac coupler application circuit**

**TOSHIBA** 

This section shows a typical triac coupler basic applied circuit and associated application.



Figure 11.1.1 Basic applied circuit





## <span id="page-29-0"></span>**12. (Reference) Thyristor coupler applications**

#### <span id="page-29-1"></span>**12.1 Uses for thyristor couplers**

Two thyristor couplers in antiparallel configuration are sometimes used instead of a triac coupler for load control. Although this requires more components, it eliminates the problem of commutation dV/dt associated with triac couplers.

## <span id="page-29-2"></span>**12.2 Operating principles of the thyristor coupler**

A thyristor coupler comprises an LED on the input side and a PNPN junction photo-thyristor element on the output side.

The thyristor element normally switches on when current is applied to the gate terminal. Light from the LED is converted to photoelectric current at the PN junction surface, which then becomes gate current that causes the thyristor element to switch on.

A thyristor provides on/off current switching in one direction only. However multiple thyristors can be configured in antiparallel to provide the same functionality as a triac element, including on-off current switching for AC-powered loads in both directions.



Figure 12.2.1 Equivalent circuit for thyristor element

## **12.3 (Reference) Basic application circuit with thyristor coupler**

<span id="page-30-0"></span>

Figure 12.3.1 AC load control using thyristors in antiparallel configuration



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The two thyristors are on the same chip. In the event of reverse current, the minority carrier can cause a commutation dV/dt malfunction



Figure 12.3.2 Triac structure Figure 12.3.3 Thyristors in antiparallel

## Application Notes

## <span id="page-31-0"></span>**13. FAQ**









## Application Notes



## <span id="page-34-0"></span>**14. Revision history**



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#### Application Notes

#### <span id="page-35-0"></span>**RESTRICTIONS ON PRODUCT USE**

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