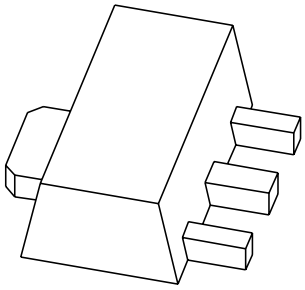
DATA SHEET



BST39; BST40
NPN high-voltage transistors

Product specification
Supersedes data of 2000 Jul 03

2004 Dec 14

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NPN high-voltage transistors BST39; BST40

FEATURES

- Low current (max. 100 mA)
- High voltage (max. 350 V).

APPLICATIONS

- General purpose switching and amplification.

DESCRIPTION

NPN high-voltage transistor in a SOT89 plastic package.
PNP complements: BST15 and BST16.

MARKING

TYPE NUMBER	MARKING CODE
BST39	AT1
BST40	AT2

PINNING

PIN	DESCRIPTION
1	emitter
2	collector
3	base

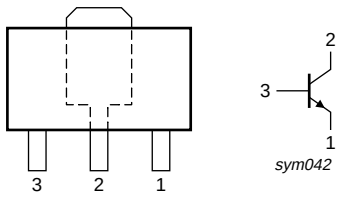


Fig.1 Simplified outline (SOT89) and symbol.

ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
BST39	SC-62	plastic surface mounted package; collector pad for good heat transfer; 3 leads	SOT89
BST40			

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter			
	BST39		–	400	V
	BST40		–	300	V
V_{CEO}	collector-emitter voltage	open base			
	BST39		–	350	V
	BST40		–	250	V
V_{EBO}	emitter-base voltage	open collector	–	5	V
I_C	collector current (DC)		–	100	mA
I_{CM}	peak collector current		–	200	mA
I_{BM}	peak base current		–	100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C; note 1	–	1.3	W
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	150	°C
T_{amb}	ambient temperature		–65	+150	°C

Note

1. Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 6 cm^2. For other mounting conditions, see *"Thermal considerations for SOT89 in the General Part of associated Handbook".*

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th(j-a)}$	thermal resistance from junction to ambient	note 1	96	K/W
$R_{th(j-s)}$	thermal resistance from junction to soldering point		16	K/W

Note

1. Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 6 cm^2. For other mounting conditions, see *"Thermal considerations for SOT89 in the General Part of associated Handbook".*

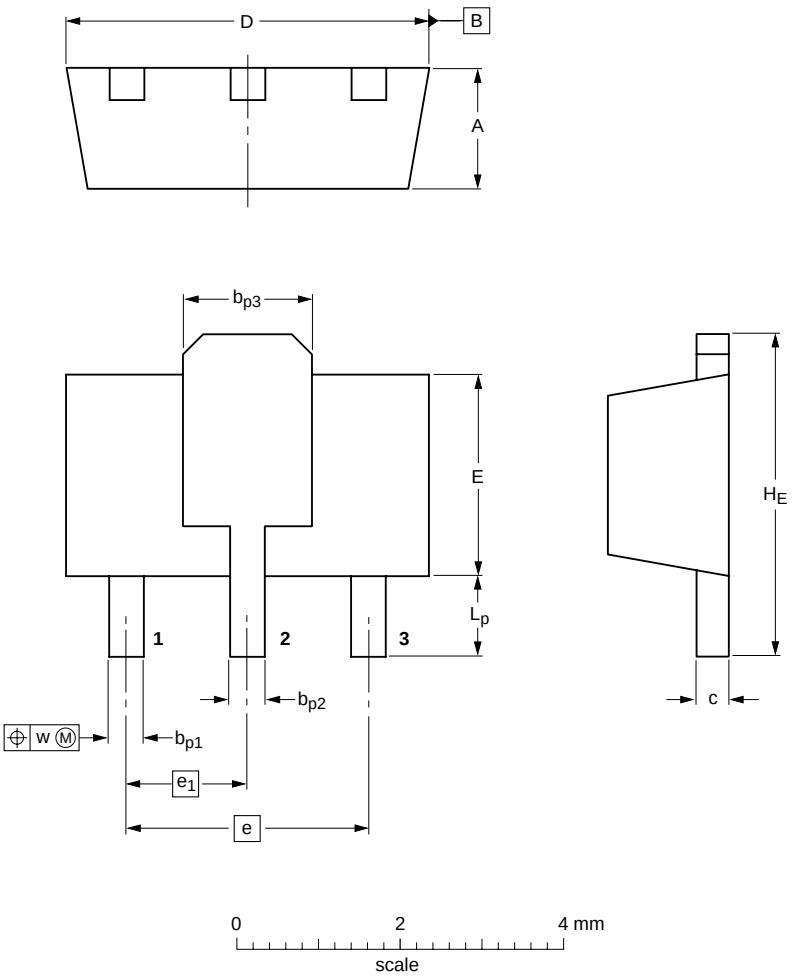
CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I_{CBO}	collector-base cut-off current	I_E = 0 A; V_{CB} = 300 V	–	20	nA
I_{EBO}	emitter-base cut-off current	I_C = 0 A; V_{EB} = 5 V	–	100	nA
h_{FE}	DC current gain	I_C = 20 mA; V_{CE} = 10 V	40	–	
V_{CEsat}	collector-emitter saturation voltage	I_C = 50 mA; I_B = 4 mA	–	500	mV
C_c	collector capacitance	$I_E = i_e$ = 0 A; V_{CB} = 10 V; f = 1 MHz	–	2	pF
f_T	transition frequency	I_C = 10 mA; V_{CE} = 10 V; f = 100 MHz	70	–	MHz

PACKAGE OUTLINE

Plastic surface mounted package; collector pad for good heat transfer; 3 leads **SOT89**



DIMENSIONS (mm are the original dimensions)

UNIT	A	b_{p1}	b_{p2}	b_{p3}	c	D	E	e	e_1	H_E	L_p	w
mm	1.6 1.4	0.48 0.35	0.53 0.40	1.8 1.4	0.44 0.23	4.6 4.4	2.6 2.4	3.0	1.5	4.25 3.75	1.2 0.8	0.13

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT89		TO-243	SC-62			~~99-09-13~~ 04-08-03

DATA SHEET STATUS

LEVEL	DATA SHEET STATUS[1]	PRODUCT STATUS[2][3]	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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