



Product Change Notification / SYST-28LKZS657

Date:

31-Mar-2023

Product Category:

Clock and Timing - Clock and Data Distribution

PCN Type:

Document Change

Notification Subject:

Data Sheet - Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control

Affected CPNs:

[SYST-28LKZS657_Affected_CPN_03312023.pdf](#)
[SYST-28LKZS657_Affected_CPN_03312023.csv](#)

Notification Text:

SYST-28LKZS657

Microchip has released a new Datasheet for the Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control of devices. If you are using one of these devices please read the document located at [Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control](#)

Notification Status: Final

Description of Change:

1. Updated Figures 2-1 and 2-7 and removed the "Detect Clock Low Timeout" row from Table 4-23.
2. Updated package marking drawing with MSCC logo instead of MCHP logo.
3. Also changed all instances of master/slave to host/client and corrected TOC page numbers.

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 31 Mar 2023

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices::N/A

Attachments:

[Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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If you wish to change your PCN profile, including opt out, please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

Affected Catalog Part Numbers (CPN)

ZL40268LDG1

ZL40268LDF1