Customer Information Notification | 201910017l

Issue Date: 11-Jan-2020
Effective Date: 12-Jan-2020

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QUALITY

Management Summary

NXP Semiconductors announces the Kinetis MK64F 120 MHz CSP device (1N83J mask set) reference manual has been updated to rev 4 and Data Sheet has been updated to revision 8.

Change Category

[ ] Wafer Fab Process	[ ] Assembly [ ] Product Marking Process	[ ] Test Location	[ ] Design
[ ] Wafer Fab Materials	[ ] Assembly [ ] Mechanical Materials Specification	[ ]Test Process	[ ] Errata
[ ] Wafer Fab Location	[ ] Assembly [ ] Location Packing/Shipping/Labeling	[ ] Test Equipment	[ ] Electrical spec./Test coverage
[ ] Firmware	[X] Other - Documentation update		

MK64F 120Mhz Data Sheet Revision 8 and Reference Manual Revision 4 Update

Description

The MK64FN1M0CAJ12 120 MHz CSP Data Sheet has been updated to revision 8 and Reference Manual has been updated to revision 4. The revision history included in the updated document provides a detailed description of the changes.

http://www.nxp.com/files/microcontrollers/doc/data_sheet/K64P142M120SF5.pdf
http://www.nxp.com/docs/en/reference-manual/K64P144M120SF5RM.pdf

Reason

The MK64FN1M0CAJ12 120 MHz Data Sheet and Reference Manual have been updated to correct errors on 142-PIN CSP pinout table assignment.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No impact on form, fit, function, reliability or quality.

Data Sheet Revision

A new datasheet will be issued

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

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Affected Part Number

MK64FN1M0CAJ12R