


PCN Number:		20190802000.2		PCN Date:		Sept. 18, 2019							
Title:		TPS92662 TITL Offload to TIPI											
Customer Contact:		PCN Manager			Dept:		Quality Services						
Proposed 1st Ship Date:		March 18, 2020		Estimated Sample Availability:		Date provided at sample request							
Change Type:													
<input checked="" type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design	<input type="checkbox"/>	Wafer Bump Site	<input type="checkbox"/>							
<input type="checkbox"/>	Assembly Process	<input type="checkbox"/>	Data Sheet	<input type="checkbox"/>	Wafer Bump Material	<input type="checkbox"/>							
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change	<input type="checkbox"/>	Wafer Bump Process	<input type="checkbox"/>							
<input type="checkbox"/>	Mechanical Specification	<input checked="" type="checkbox"/>	Test Site	<input type="checkbox"/>	Wafer Fab Site	<input type="checkbox"/>							
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process	<input type="checkbox"/>	Wafer Fab Materials	<input type="checkbox"/>							
		<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process								
PCN Details													
Description of Change:													
Texas Instruments Incorporated is announcing the qualification of TPS92662QPHPxQ1 devices at TI Philippines (TIPI) Assembly/Test site.													
<table border="1"> <thead> <tr> <th>Description</th> <th>Current A/T</th> <th>Additional A/T</th> </tr> </thead> <tbody> <tr> <td>Assembly/Test site</td> <td>TI Taiwan</td> <td>TI Philippines</td> </tr> </tbody> </table>								Description	Current A/T	Additional A/T	Assembly/Test site	TI Taiwan	TI Philippines
Description	Current A/T	Additional A/T											
Assembly/Test site	TI Taiwan	TI Philippines											
No change to BOM.													
Reason for Change:													
Maintain continuity of supply for increase in demand.													
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):													
None.													
Changes to product identification resulting from this PCN:													
The product box label site origin codes will change as described below:													
Current													
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TI Taiwan	TAI	TWN											
New													
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Assembly Site	Assy site code (22L)	Assy country code (23L)											
TI Philippines	PHI	PHL											
Example sample product shipping label (not actual product label):													
 <p> TEXAS INSTRUMENTS MADE IN: Malaysia 2DC: 29: MSL '2 / 260C / 1 YEAR SEAL DT MSL 1 / 235C / UNLIM 03/29/04 OPT: ITEM: 39 LBL: 5A (L)T0:1750 </p> <p> (1P) SN74LS07NSR (Q) 2000 (D) 0336 (31T) LOT: 3959047MLA (4W) TKY (1T) 7523483SI2 (P) (2P) REV: (V) 0033317 (22L) CSO: SML (23L) CCO: USA (22L) ASO: MLA (23L) ACO: MYS </p>													
Product Affected:													
TPS92662QPHPRQ1 TPS92662QPHPTQ1													



Automotive New Product Qualification Summary

(As per AEC-Q100 and JEDEC Guidelines)

Approved 30-Jan-2019

Product Attributes

Attributes	Qual Device: <u>TPS92662QPHPQ1</u> <u>PG2.0</u>	QBS Product Reference: <u>TPS92662QPHPQ1</u> <u>PG1.0</u>	QBS Product Reference: <u>TPS92662QPHPQ1</u> <u>PG2.0</u>	QBS Process Reference: <u>S0704038C0PLPR</u>
Automotive Grade Level	Grade 1	Grade 1	Grade 1	Grade 1
Operating Temp Range	-40 to +125 C	-40 to +125 C	-40 to +125 C	-40 to +125 C
Product Function	Power Management	Power Management	Power Management	Power Management
Wafer Fab Supplier	RFAB	RFAB	RFAB	RFAB
Die Revision	A	A	B	C1
Assembly Site	TIPI (PHI)	TI TAIWAN	TI TAIWAN	TI TAIWAN
Package Type	QFP	QFP	QFP	QFP
Package Designator	PHP	PHP	PHP	PLP
Ball/Lead Count	48	48	48	128

- QBS: Qual By Similarity

- Qual Device TPS92662QPHPQ1 is qualified at LEVEL3-260C

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS/Lot	Test Name / Condition	Duration	Qual Device: <u>TPS92662QPHP</u> <u>Q1 PG2.0</u>	QBS Product Reference: <u>TPS92662QPHP</u> <u>Q1 PG1.0</u>	QBS Product Reference: <u>TPS92662QPHP</u> <u>Q1 PG2.0</u>	QBS Process Reference: <u>S0704038C0PLPR</u>	
Test Group A – Accelerated Environment Stress Tests											
PC	A 1	JESD2 2-113	3	As required	Preconditioning	Per MSL rating	No Fails	No Fails	No Fails	No Fails	
HAST	A 2	JEDEC JESD2 2-A110	3	77	Biased HAST, 130/85%RH	96 Hours	3/231/0	3/231/0	-	3/231/0	
AC	A 3	JEDEC JESD2 2-A102	3	77	Autoclave 121C	96 Hours	3/231/0	3/231/0	-	3/231/0	
TC	A 4	JEDEC JESD2 2-A104 and Appendix 3	3	77	Temperature Cycle, -65/150	500 Cycles	3/231/0	3/231/0	1/77/0	3/231/0	
TC-BP	A 4	MIL-STD883 Method 2011	1	30	Post Temp. Cycle Bond Pull	Post 500 Cycles	1/30/0	1/30/0	-		
PTC	A 5	JEDEC JESD2 2-A105	1	45	Power Temperature Cycle	1000 Cycles	1/45/0	1/45/0	1/45/0	N/A	

HTS L	A 6	JEDEC JESD2 2-A103	3	45	High Temp. Storage Bake, 175C	500 Hours	3/231/0	3/231/0	-	3/45/0
Test Group B – Accelerated Lifetime Simulation Tests										
HTO L	B 1	JEDEC JESD2 2-A108	3	77	Life Test, 125C	1000 Hours	-	-	-	3/231/0
HTO L	B 1	JEDEC JESD2 2-A108	3	77	Life Test, 150C	300 Hours	1/77/0	-	1/77/0	-
ELF R	B 2	AEC Q100- 008	3	800	Early Life Failure Rate, 150C	48 Hours	-	-	-	3/2400/0
EDR	B 3	AEC Q100- 005	3	77	NVM Endurance, Data Retention, and Operational Life	-	N/A	N/A	N/A	N/A
Test Group C – Package Assembly Integrity Tests										
WBS	C 1	AEC Q100- 001	3	30	Bond Shear (<i>Cpk</i> >1.67)	Wires	3/90/0	2/60/0	1/30/0	3/90/0
WBP	C 2	MIL- STD88 3 Method 2011	3	30	Bond Pull (<i>Cpk</i> >1.67)	Wires	3/90/0	2/60/0	1/30/0	3/90/0
SD	C 3	JEDEC JESD2 2-B102	1	15	Surface Mount Solderability >95% Lead Coverage	PbFree & Pb	1/15/0	-	3/30/0	2/30/0
PD	C 4	JEDEC JESD2 2-B100 and B108	3	10	Physical Dimensions (<i>Cpk</i> >1.67)	-	3/30/0	2/60/0	1/30/0	3/90/0
SBS	C 5	AEC Q100- 010	3	50	Solder Ball Shear (<i>Cpk</i> >1.67)	Post HTSL/Bu mp	N/A	N/A	-	-
LI	C 6	JEDEC JESD2 2-B105	1	50	Lead Integrity	Leads	-	-	-	1/22/0
Test Group D – Die Fabrication Reliability Tests										
EM	D 1	JESD6 1	-	-	Electromigrati on	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDD B	D 2	JESD3 5	-	-	Time Dependent Dielectric Breakdown	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D 3	JESD6 0 & 28	-	-	Hot Injection Carrier	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements

	NBTI	D 4	-	-	-	Negative Bias Temperature Instability	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
	SM	D 5	-	-	-	Stress Migration	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Test Group E – Electrical Verification Tests											
	HBM	E 2	AEC Q100-002	1	3	ESD - HBM	3000 V	-	-	1/3/0	-
	CDM	E 3	AEC Q100-011	1	3	ESD - CDM	750 V/* Corner Pins	1/3/0	-	1/3/0	-
	LU	E 4	AEC Q100-004	1	6	Latch-up	(Per AEC Q100-004)	-	-	1/6/0	1/6/0
	ED	E 5	AEC Q100-009	3	30	Electrical Distributions	Cpk > 1.67 Room, hot, and cold test	3/90/0	-	3/90/0	3/90/0

A1 (PC): Preconditioning:
 Performed for THB, Biased HAST, AC, uHAST & TC samples, as applicable.

Junction Operating Temperature by Automotive Grade Level:
 Grade 0 (or E): -40°C to +150°C
 Grade 1 (or Q): -40°C to +125°C
 Grade 2 (or T): -40°C to +105°C
 Grade 3 (or L): -40°C to +85°C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):
 Room/Hot/Cold: HTOL, ED
 Room/Hot: THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
 Room: AC/uHAST

Green/Pb-free Status:
 Qualified Pb-Free(SMT) and Green

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