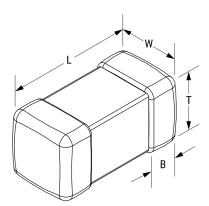


L1007C3R3MDWST

Aliases (L1007C3R3MDWST)

Not for New Design • Scheduled EOL - Last date to buy 30 JUN 2023

KEMET, L-DWS, Power, 3.3 uH, 20%



Click here for the 3D model.

Dimensions	
Chip Size	1007
L	2.5mm +/-0.2mm
W	1.8mm +/-0.2mm
Т	1.8mm +/-0.2mm
LL	0.5mm +/-0.2mm
В	0.5mm +/-0.2mm

Packaging Specifications	
Packaging	T&R
Packaging Quantity	2000

General Information		
Series	L-DWS	
Style	SMD Chip	
Description	Surface Mount Inductor	
Features	Standard Type	
RoHS	Yes	
Notes	Last Order Date June 30, 2023. Suggested Replacement - https://www.chilisin.com/en- global/Inductor/Product/BKPB_252010/BKPB002520103R3MA2.	

Specifications	
Inductance	3.3 uH (7.96 MHz)
Inductance Tolerance	20%
Temperature Range	-40/+105°C
Current	440 mAmps (Isat, 30% Drop In Inductance), 1200 mAmps (Irms, 40C Rise By Self Heating)
DC Resistance	0.11 Ohms
Self-Resonant Frequency	54 MHz MIN

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