

C0805T103K5RACTU

Aliases (C0805T103K5RAC7800)

SMD COTS X7R, Ceramic, 0.01 uF, 10%, 50 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 0805



Click [here](#) for the 3D model.

General Information	
Series	SMD COTS X7R
Style	SMD Chip
Description	SMD, MLCC, COTS, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Tin
Marking	No
Failure Rate	Testing per MIL-PRF-55681 PDA 8%
AEC-Q200	No
Component Weight	11 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
Chip Size	0805
L	2mm +/-0.2mm
W	1.25mm +/-0.2mm
T	0.78mm +/-0.10mm
S	0.75mm MIN
B	0.5mm +/-0.25mm

Specifications	
Capacitance	0.01 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	10%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	100 GOhms

Packaging Specifications	
Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

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