SPECIFICATIONS

TEMP CLASS 105°C
DESIGNED TO MEET UL1950/IEC-950 REQUIREMENTS

1. INDUCTANCE : @ 0.1 VRMS, 10 kHz
 5-2 (TIE 3&4) = 213 TO 260 µH
 6-9 (TIE 7&8) = 426 TO 521 µH

2. LEAKAGE INDUCTANCE: @ 0.1 VRMS, 100 kHz
 5-2 W/3&4, 6&8, 7&9 SHORTED = 10 µH MAX

3. IWC: @ 0.1 VRMS, 100 kHz
 5-2 TO 6-9 = 25 pF MAX

4. TURNS RATIO: 5-2 : 6-9 TIE 7&8, 3&4 = 1 : 1.41 ± 1%

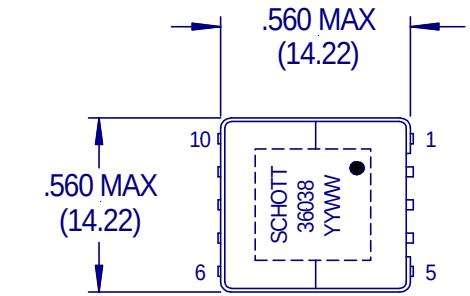
5. DC RESISTANCE: OHMS MAX
 5-3-4-2 = 0.50
 6-8-7-9 = 1.00

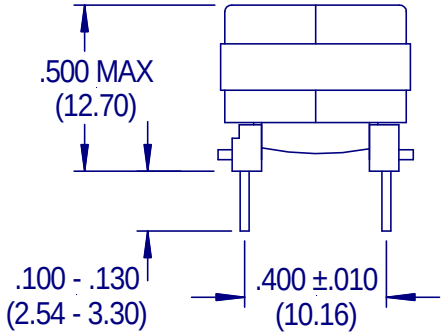
6. LONGITUDINAL BALANCE:
 V1 = 0 dBm @ 20 kHz TO 250 kHz, Rs = 100 OHMS INPUT
 Rl = 50 OHM LOAD, LINE = 6-9 GROUND PIN #2,
 CHIP = 5-2, V2 = 60 dB MIN

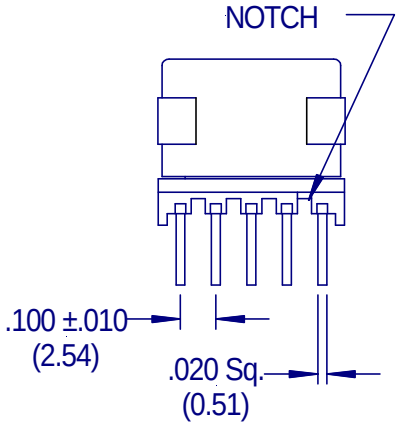
7. DIELECTRIC STRENGTH: 1800 VAC, 1 SECOND
 5&4 TO 6&7
 ALL WINDINGS TO CORE

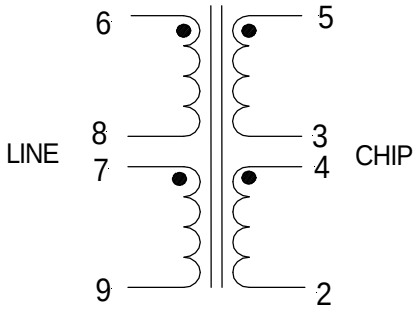
8. HARMONIC DISTORTION: @ 100 kHz, 6-9 = 100
 OHM INPUT, 5-2 = 50 OHM LOAD, V1 = 15 Vp-p
 V2 = -85 dB MAX (5.32VRMS)

9. INSERTION LOSS: = 0.5 dB MAX
 @ 100 kHz TO 1.0 MHz, LINE = 6-9,
 CHIP = 5-2, Rs = 100 OHMS, Rl = 50 OHMS









TOLERANCES UNLESS OTHERWISE SPECIFIED			
INCHES-DECIMALS	.XX = ± .02	.XXX = ±.010	ANGLES ± 1°
MILLIMETERS (mm)	.XX = ± 0.2		