Product / Process Change Notification



N° 2016-068-A

Dear Customer,

Please find attached our INFINEON Technologies PCN:

Introduction of additional wafer test in Kulim for dedicated products

Important information for your attention:

- Please respond to this PCN by indicating your decision on the approval form, sign it and return to your sales partner before 28. April 2017.
- Infineon aligns with the widely-recognized JEDEC STANDARD"JESD46", which stipulates: "Lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change."

Your prompt reply will help Infineon Technologies to assure a smooth and well executed transition. If Infineon does not hear from your side by the due date, we will assume your full acceptance to this proposed change and its implementation.

Your attention and response to this matter is greatly appreciated.

Infineon Technologies AG Postal Address Headquarters: Am Campeon 1-12, D-85579 Neubiberg, Phone +49 (0)89 234-0 Chairman of the Supervisory Board: Wolfgang Mayrhuber Management Board: Dr. Reinhard Ploss (CEO), Dominik Asam, Dr. Helmut Gassel, Jochen Hanebeck Registered Office: Neubiberg Commercial Register Amtsgericht München HRB 126492

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Products affected:	Sales Name	SP N°	OPN	Package
	TLE6220GP	SP000691118	TLE6220GPAUMA2	PG-DSO-20
	TLE6230GP	SP000691114	TLE6230GPAUMA1	PG-DSO-36
	TLE6232GP	SP000691110	TLE6232GPAUMA2	PG-DSO-36
	TLE6240GP	SP000627670	TLE6240GPAUMA1	PG-DSO-36
	TLE8102 CHIP	SP000665148	TLE8102CHIPX6SA1	N.A.
	TLE8102-1SG	SP000999094	TLE81021SGAUMA1	PG-DSO-12
	TLE8102SG	SP000377621	TLE8102SGAUMA1	PG-DSO-12
	TLE8104E	SP000766202	TLE8104EXUMA2	PG-DSO-20

Detailed Change Information:

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Subject:	Introduction of additional wafer test in Kulim for dedicated products.					
Reason:	Expansion of wafer test to assure continuity of supply and enable flexible testing.					
Description:	Old	New				
Wafer test	 Infineon Technologies AG, Villach, Austria 	 Infineon Technologies (Kulim) Sdn. Bhd, Kulim, Malaysia or Infineon Technologies AG, Villach, Austria 				
Product Identification:	Traceability is ensured by lot number.					
Impact of Change:	No change in form fit or function and no impact on quality and reliability of the final product. The wafer test location verification is performed via the Advanced Measurement System Analysis (AMSA) methodology.					
Attachments:	N.A.					
Time Schedule:						
Final qualification report:	AMSA assessment on demand					
First samples available:	No samples planned due to no change of product.					
Intended start of delivery:	01-July-2017 or earlier after customer release					

If you have any questions, please do not hesitate to contact your local Sales office.