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|---|-----------------------------|-------------------------------------|---------------------|
| PCN Number: | 20190909001 | PCN Date: | Oct. 1, 2019 |
| Title: | Datasheet for TPS1663 | | |
| Customer Contact: | PCN Manager | Dept: | Quality Services |
| Proposed 1st Ship Date: | Jan. 1, 2020 | | |
| Change Type: | | | |
| <input type="checkbox"/> | Assembly Site | <input type="checkbox"/> | Design |
| <input type="checkbox"/> | Assembly Process | <input checked="" type="checkbox"/> | Data Sheet |
| <input type="checkbox"/> | Assembly Materials | <input type="checkbox"/> | Part number change |
| <input type="checkbox"/> | Mechanical Specification | <input type="checkbox"/> | Test Site |
| <input type="checkbox"/> | Packing/Shipping/Labeling | <input type="checkbox"/> | Test Process |
| <input type="checkbox"/> | | <input type="checkbox"/> | Wafer Bump Site |
| <input type="checkbox"/> | | <input type="checkbox"/> | Wafer Bump Material |
| <input type="checkbox"/> | | <input type="checkbox"/> | Wafer Bump Process |
| <input type="checkbox"/> | | <input type="checkbox"/> | Wafer Fab Site |
| <input type="checkbox"/> | | <input type="checkbox"/> | Wafer Fab Materials |
| <input type="checkbox"/> | | <input type="checkbox"/> | Wafer Fab Process |
| Notification Details | | | |
| Description of Change: | | | |
| Texas Instruments Incorporated is announcing a change. The product datasheet(s) is being updated as summarized below. | | | |
|  | | TPS1663 | |
| <small>SLVSET9D – SEPTEMBER 2018 – REVISED AUGUST 2019</small> | | | |
| Changes from Revision C (March 2019) to Revision D | | | Page |
| • Changed the absolute maximum voltage in Features | | | 1 |
| • Changed the adjustable output power limiting in Features | | | 1 |
| • Changed the Absolute Maximum Ratings IN, P_IN, OUT, UVLO, \overline{FLT} , PGOOD maximum input voltage..... | | | 5 |
| • Added $T_A = 25^\circ\text{C}$ to the Absolute Maximum Ratings IN, P_IN (10ms transient) input voltage | | | 5 |
| • Changed the $V_{(OVPE)}$ maximum in Electrical Characteristics | | | 6 |
| • Changed $V_{(SEL_PLIM)}$, $I_{(PLIM)}$, and $I_{(dVdT)}$ minimum and maximum..... | | | 6 |
| • Changed the $P_{(PLIM)}$ minimum, typical, and maximum..... | | | 6 |
| The datasheet number will be changing. | | | |
| Device Family | Change From: | Change To: | |
| TPS1663 | SLVSET9C | SLVSET9D | |
| These changes may be reviewed at the datasheet links provided. http://www.ti.com/product/TPS1663 | | | |
| Reason for Change: | | | |
| To accurately reflect device characteristics. | | | |
| Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative): | | | |
| Electrical specification performance changes as indicated above. | | | |
| Changes to product identification resulting from this PCN: | | | |
| None. | | | |
| Product Affected: | | | |
| TPS16630RGER | TPS16630RGET | TPS16632RGER | TPS16632RGET |

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

| Location | E-Mail |
|-----------------|--|
| USA | PCNAmericasContact@list.ti.com |
| Europe | PCNEuropeContact@list.ti.com |
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