SURFACE MOUNT top hat

Directional Coupler TCD-16-12W-75X+

75Ω 16.5dB 5 to 1218 MHz

FEATURES

- Wideband, 5 to 1218 MHz
- Low mainline loss, 0.7 dB typ.
- Aqueous washable
- Leads for excellent solderability
- Protected by US Patent 6,140,887

Generic photo used for illustration purposes only

CASE STYLE: DB1627

+RoHS Compliant
The +Suffix identifies RoHS Compliance.
See our website for methodologies and qualifications

APPLICATIONS

- DOCSIS® Systems
- VHF/UHF
- CATV
- Cellular

ELECTRICAL SPECIFICATIONS AT 25°C

Parameter	Frequency (MHz)	Min.	Typ.	Max.	Unit
Frequency Range	—	5	—	1218	MHz
Mainline Loss[1] (above theoretical 0.1 dB)	5-50 50-870 870-1218	— — —	0.65 0.5 0.70	0.9 0.8 1.0	dB
Coupling	5-1218	—	16.0±0.5	—	dB
Coupling Flatness (±)	5-1218	—	±0.8	±1.2	dB
Directivity	5-50 50-870 870-1218	25 11 8	35 16 12	— — —	dB
Return Loss (Input)	5-50 50-870 870-1218	18 14 16	22 18 19	— — —	dB
Return Loss (Output)	5-50 50-870 870-1218	20 17 18	25 19 22	— — —	dB
Return Loss (Coupling)	5-50 50-870 870-1218	18 15 14	22 17 16	— — —	dB
Input Power	5-1218	—	—	1.0	W

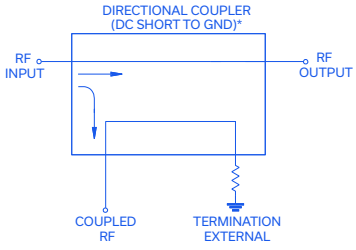
1. Mainline loss includes theoretical power loss at coupled port.

MAXIMUM RATINGS

Parameter	Ratings
Operating Temperature	-40°C to 85°C*
Storage Temperature	-55°C to 100°C

Permanent damage may occur if any of these limits are exceeded.
* Case temperature is defined as temperature on ground leads.

ELECTRICAL SCHEMATIC



*Electrical schematic is for Directional coupler with internal transformer(s) and external termination

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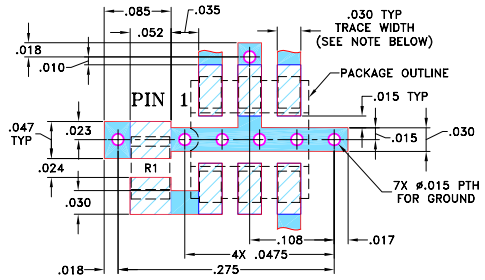
PIN CONNECTIONS

INPUT	3
OUTPUT	4
COUPLED	1
GROUND	2
75Ω TERM EXTERNAL	6
NOT USED	5

PRODUCT MARKING: UR

DEMO BOARD MCL P/N: TB-72

SUGGESTED PCB LAYOUT (PL-010)



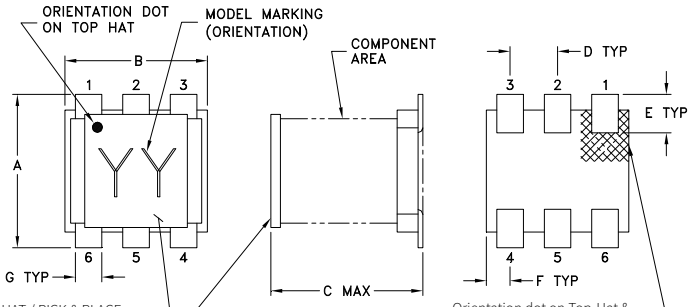
RESISTOR R1: 75 ± 1% Ohm, 0805 SIZE

NOTES: 1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS 0.030" ± 0.002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

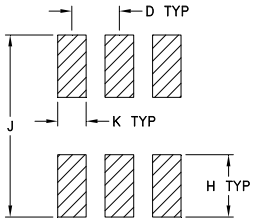
OUTLINE DRAWING



TOP-HAT / PICK & PLACE SURFACE AREA (.10X.10) MIN
TOP-HAT TOTAL THICKNESS: .013 inches MAX.

Orientation dot on Top-Hat & orientation feature on substrate corresponds to pin #1.

PCB Land Pattern



SUGGESTED LAYOUT
TOLERANCE TO BE WITHIN ±.002

OUTLINE DIMENSIONS (Inches / mm)

A	B	C	D	E	F
.160	.150	.160	.050	.040	.025
4.06	3.81	4.06	1.27	1.02	0.64

G	H	J	K		wt
.028	.065	.190	.030		grams
0.71	1.65	4.83	0.76		0.15

TAPE & REEL INFORMATION: F47

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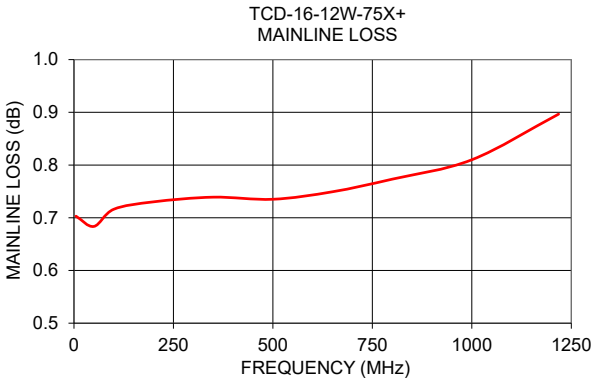
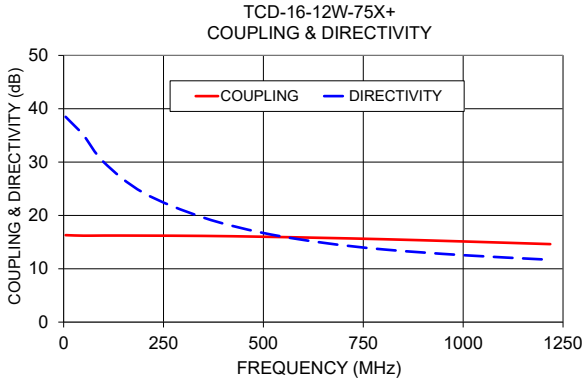
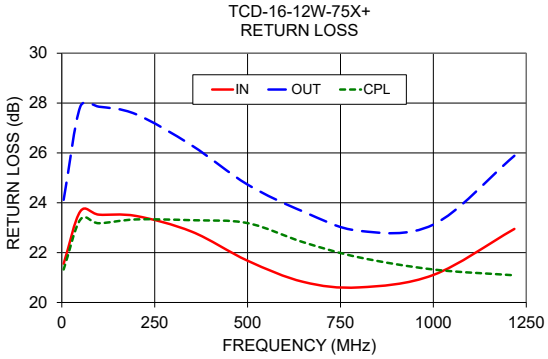
TYPICAL PERFORMANCE DATA

Frequency (MHz)	Mainline Loss (dB) In-Out	Coupling (dB) In-Cpl	Directivity (dB)	Return Loss (dB) In	Out	Cpl
5	0.70	16.30	38.48	21.56	24.12	21.32
50	0.68	16.20	34.99	23.66	27.86	23.32
100	0.72	16.23	30.01	23.52	27.85	23.18
200	0.73	16.21	24.23	23.47	27.55	23.33
350	0.74	16.16	19.59	22.84	26.30	23.30
500	0.74	16.00	16.71	21.68	24.73	23.18
650	0.75	15.79	14.87	20.83	23.65	22.42
800	0.77	15.54	13.63	20.61	22.88	21.81
1000	0.81	15.12	12.55	21.11	23.12	21.32
1218	0.90	14.62	11.68	22.95	25.89	21.09







NOTES

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.

B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.

C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard. Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/terms/viewterm.html